

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
 For the value, it can be read by the number before R. (R means resistor)
 For the tolerance, it can be read from the last letter.
 For the rating, we don't show on the symbol name.
 For the size, R2=>0402, R3=>0603, R5=>0805,.....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

ICH8-M GPIOn	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	Caramel-1 Pre-DW/DV	SA
	0	0	0	1	Caramel-1 SIV	SB
	0	0	1	0	Caramel-1 SIT	SC
	0	0	1	1	Caramel-1 SVT	
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

EC HISTORY

[illegible]

PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Reference

Size	Document Number
------	-----------------

References

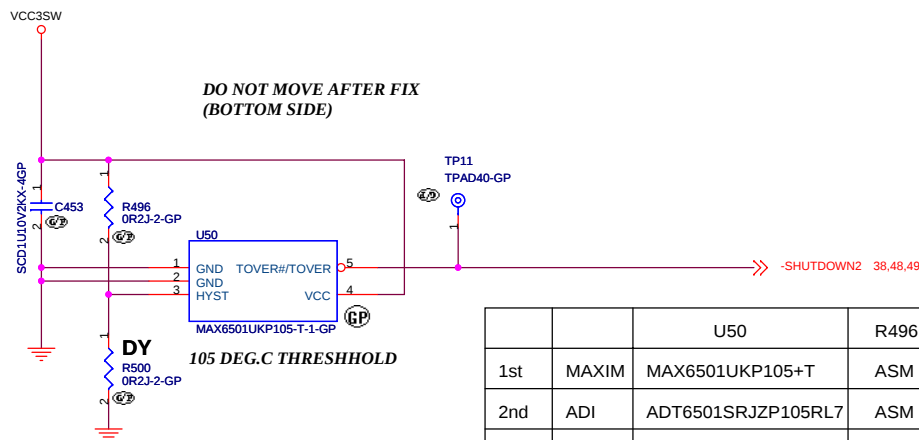
Rev

Date: Thursday, July 31, 2008

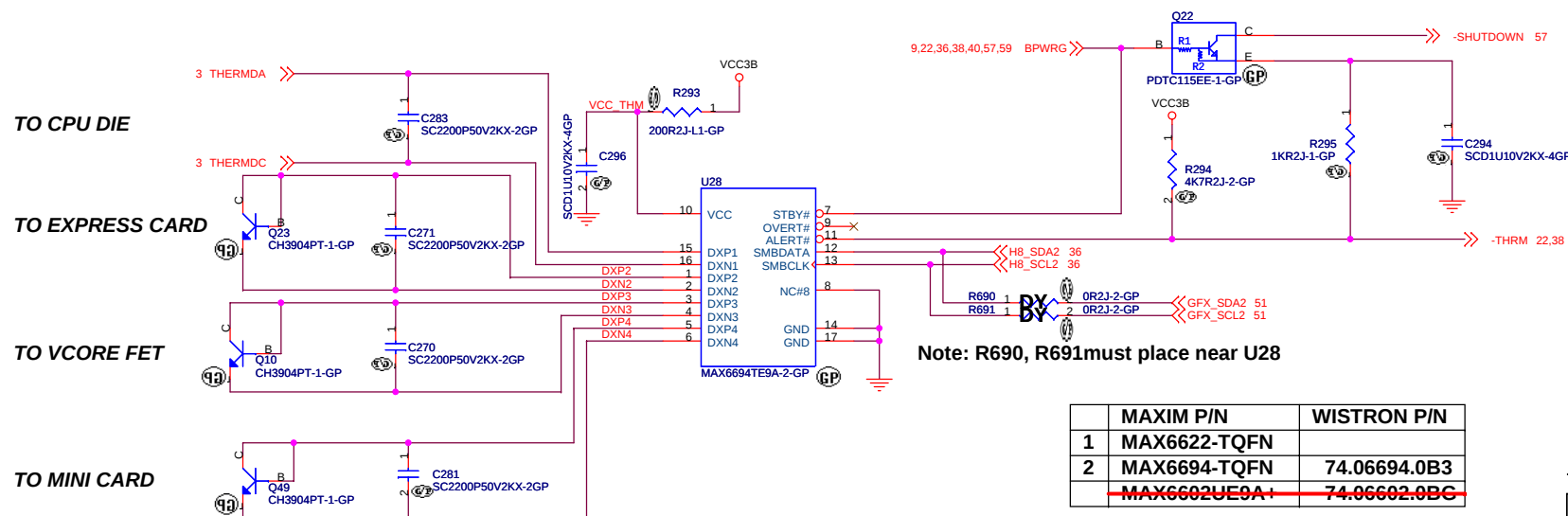
Sheet 2 of

62

Thermal Sensor for CPU



THESE CAPS MUST BE PLACED AS CLOSE AS POSSIBLE TO MAX6602



	MAXIM P/N	WISTRON P/N
1	MAX6622-TQFN	
2	MAX6694-TQFN	74.06694.0B3
	MAX6602UE9A-1	74.06602.0B3

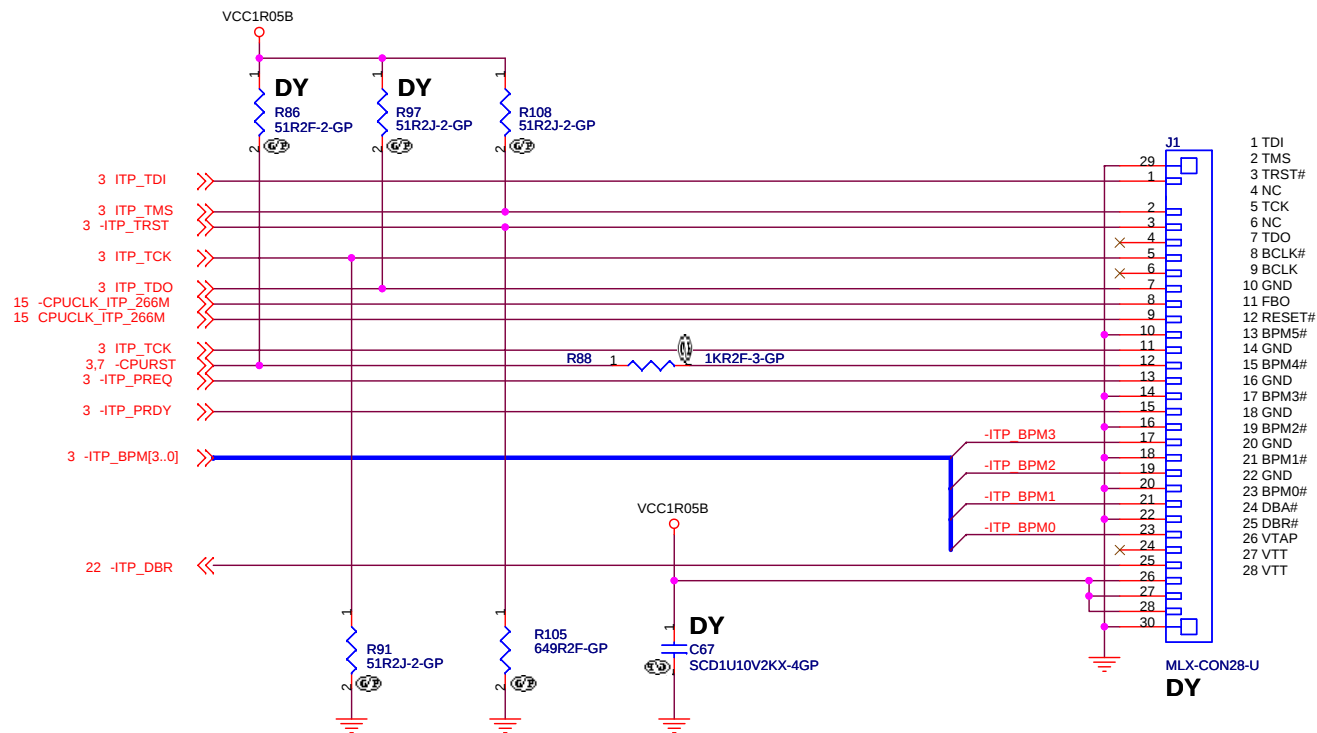
Layout Comment :

- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace lines either.

PLACE UNDER PC-CARD SLOT
H8 I2C Bus 2 ADDRESS : 9AH
TEST PAD FOR BOARD MFG TEST

<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title: THERMAL SENSOR			
Size: A3	Document Number: Caramel-1		Rev: 2
Date: Thursday, July 31, 2008		Sheet: 5 of 62	



- 1 TDI
- 2 TMS
- 3 TRST#
- 4 NC
- 5 TCK
- 6 NC
- 7 TDO
- 8 BCLK#
- 9 BCLK
- 10 GND
- 11 FBO
- 12 RESET#
- 13 BPM5#
- 14 GND
- 15 BPM4#
- 16 GND
- 17 BPM3#
- 18 GND
- 19 BPM2#
- 20 GND
- 21 BPM1#
- 22 GND
- 23 BPM0#
- 24 DBA#
- 25 DBR#
- 26 VTAP
- 27 VTT
- 28 VTT

(*1) TCK SIGNAL IS BRANCHED AT CPU's PIN

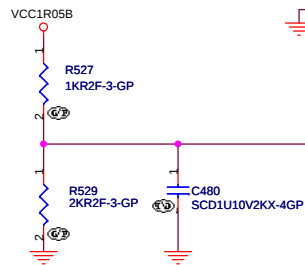
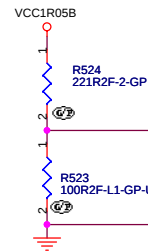
(*2) CPURST# SIGNAL IS BRANCHED AT GMCH's PIN

Ref Des	For ITP-XDP
J1	NO_ASM-->ASM
C67	NO_ASM-->ASM
R88	NO_ASM-->1K 5% ASM
R105	ASM (No Change)
R86	ASM-->NO_ASM
R97	ASM (No Change)
R91	ASM-->51 5% ASM
R108	ASM-->51 5% ASM

<Variant Name>

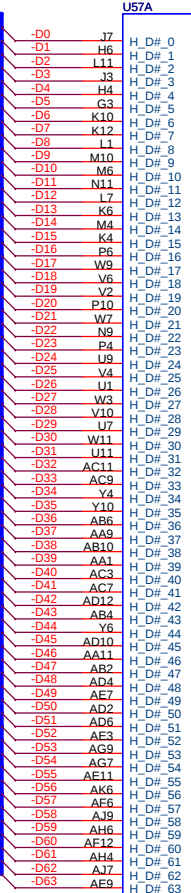
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
ITP_XDP CONN			
Size	Document Number	Rev	
Custom		2	
Date: Thursday, July 31, 2008		Sheet 6	of 62

Route H_XSWING & H_YSWING
10 mil wide / 20 mil spacing



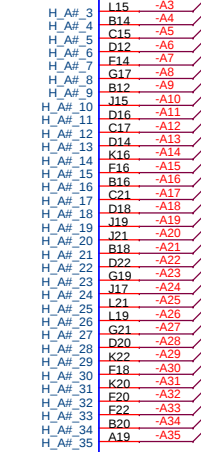
Route H_XRCOMP & H_YRCOMP 10 mil wide / 20 mil spacing

3 -D[63..0] <<>>

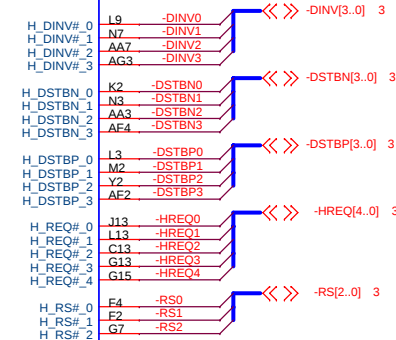


HOST

1 OF 10



<<>>-A[35..3] 3

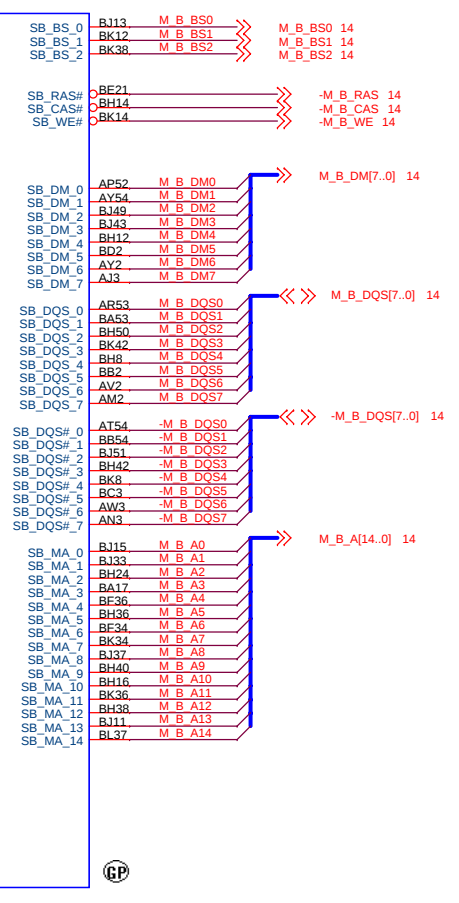
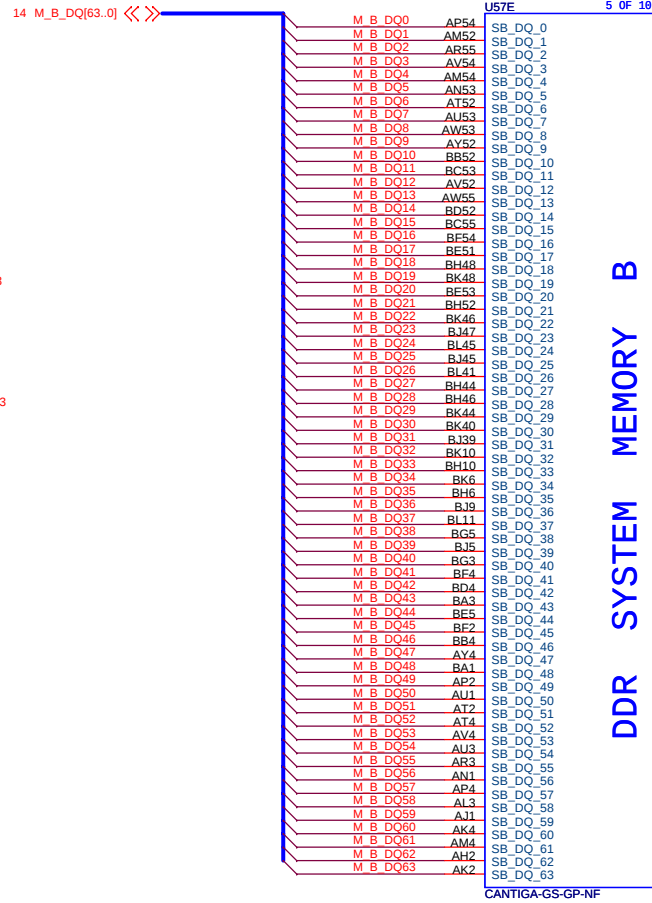
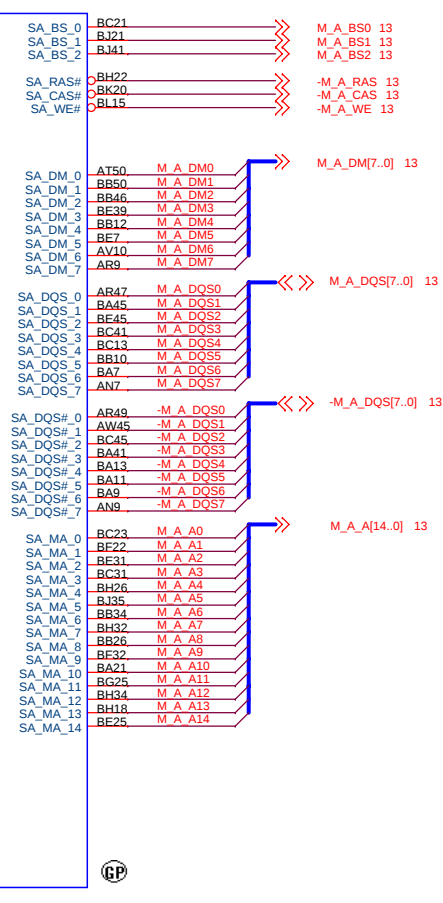
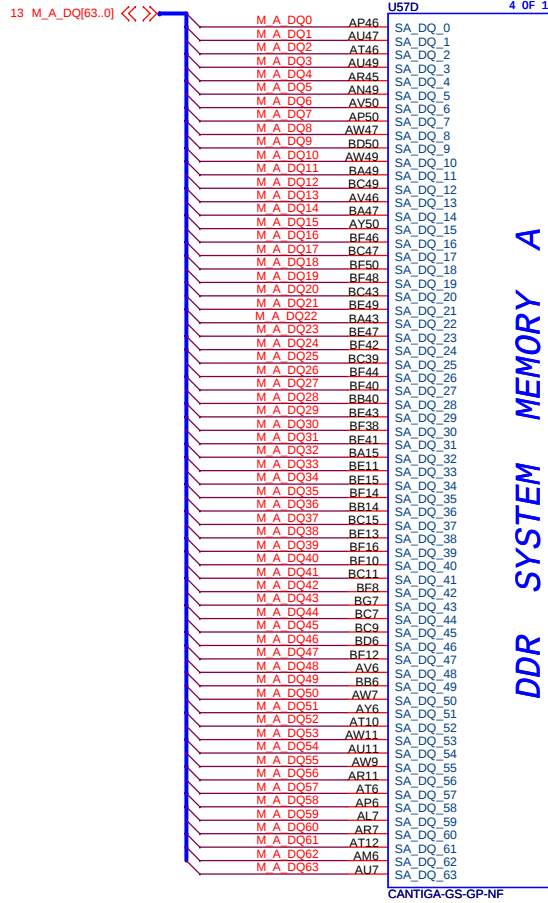


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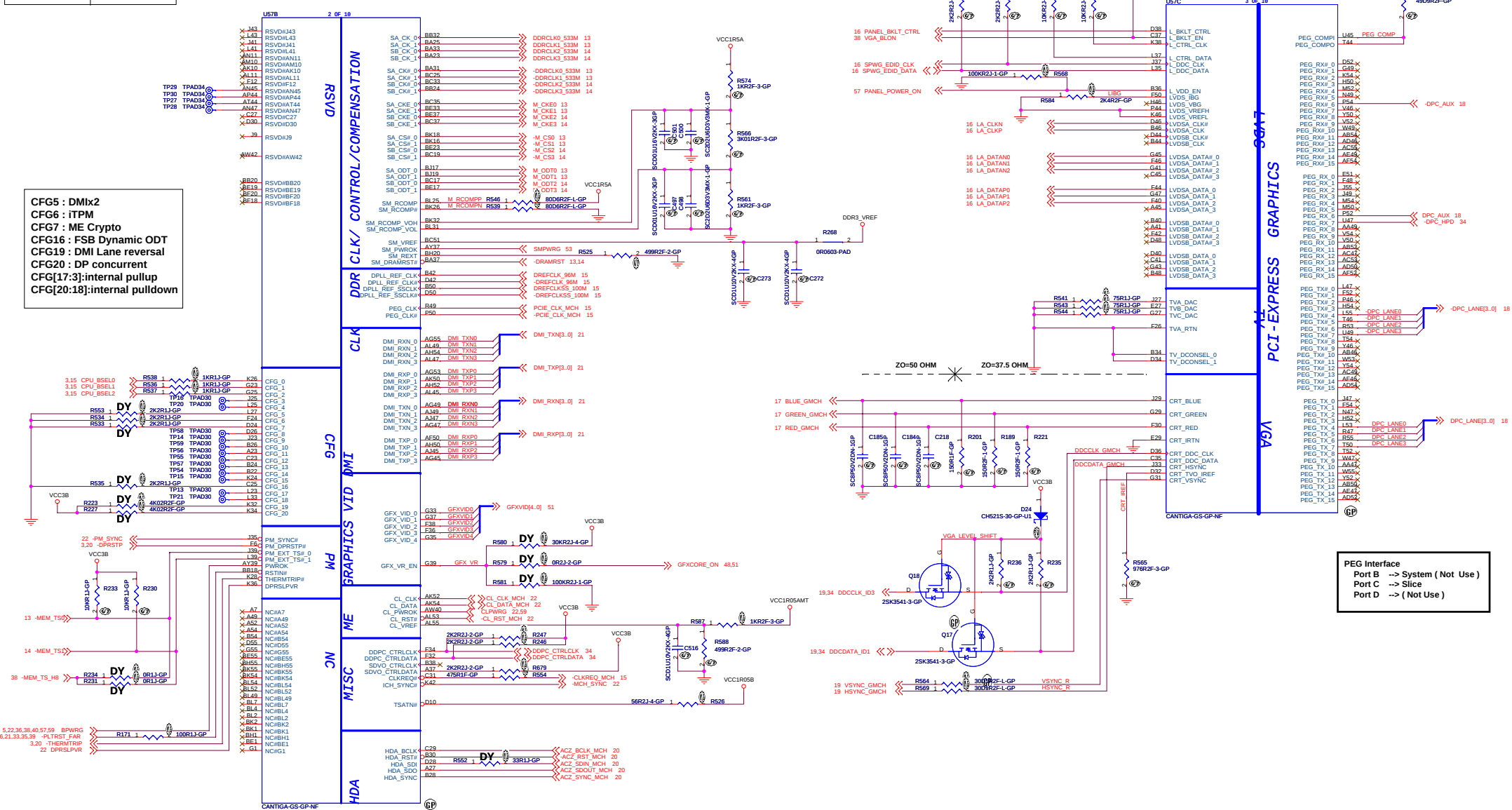
緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

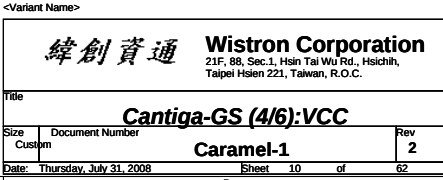
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Cantiga-GS (1/6):HOST I/F				
Size	Document Number			Rev
A3	Caramel-1			2
Date: Thursday, July 31, 2008		Sheet 7 of 62		

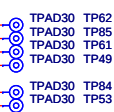
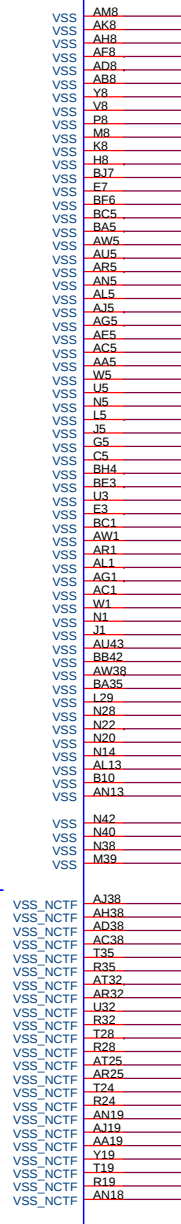
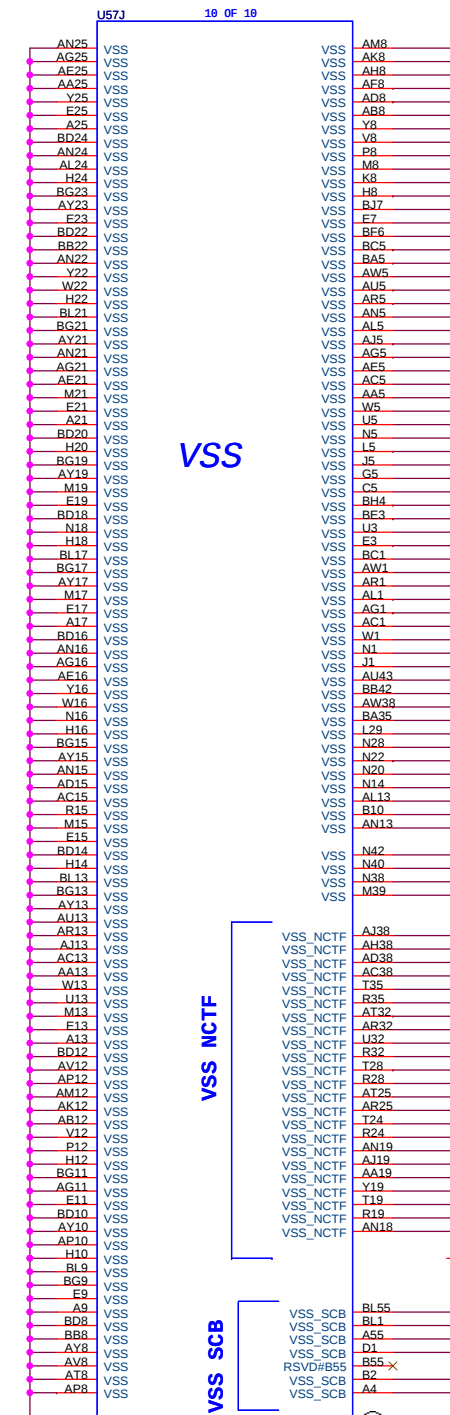
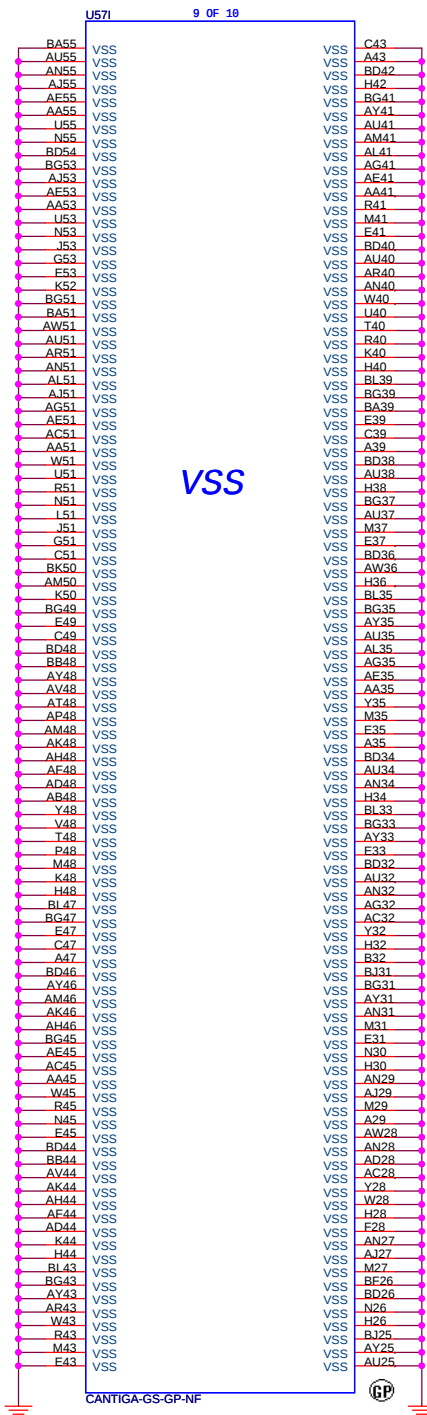


RESERVED#AN45	ME_JTAG_TCK
RESERVED#AP44	ME_JTAG_TDI
RESERVED#AT44	ME_JTAG_TDO
RESERVED#AN47	ME_JTAG_TMS



<Variant Name>	
緯創資通	Wistron Corporation 21F, 8th, Sec. 1, Hsin Tai Wu Rd., Hsinchu, Taippei Hsien 221, Taiwan, R.O.C.
File	
Cantiga-GS (3/6):DMI/PM/CFG/GF	
Size A2	Document Number Caramel-1
Date: Thursday, July 31, 2008	Page: 9 of 62





<Variant Name>

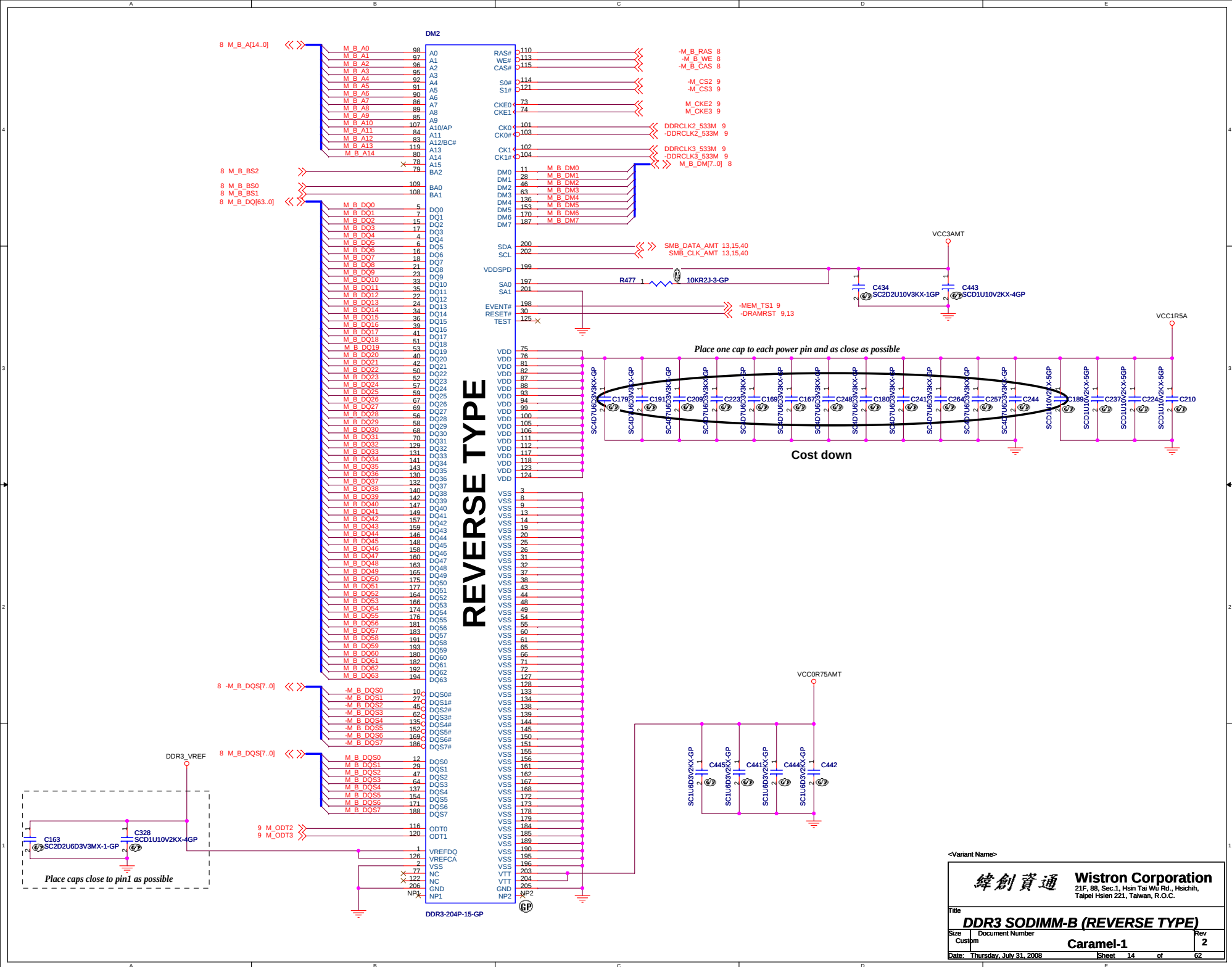
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
Cantiga-GS (6/6):GND

Size A3 Document Number
Caramel-1

Date: Thursday, July 31, 2008 Sheet 12 of 62

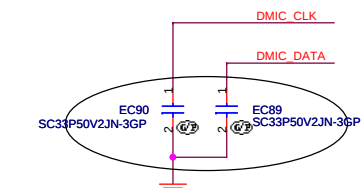
Rev 2



<Comment>

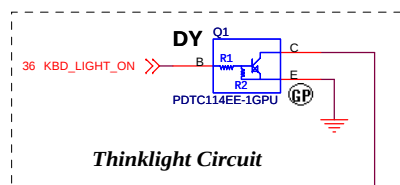
R19, R18, R2, R3, R4, R14, R5 and R16
will be removed after SDV evaluation.

LCD / Inverter Connector

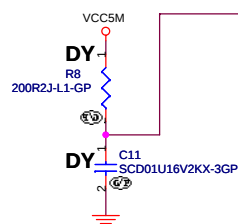


Please place EC89,EC90 near LCD CONN.

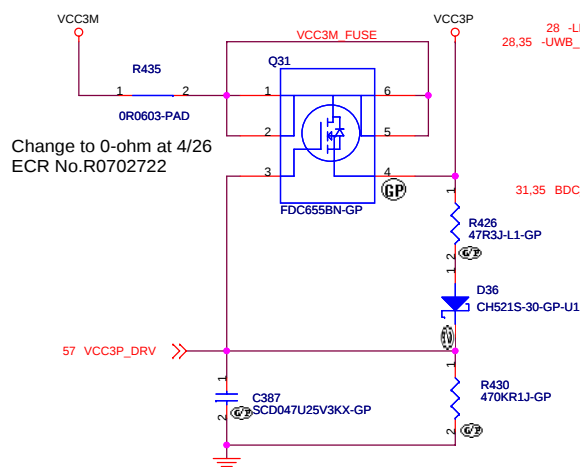
Wide Pattern



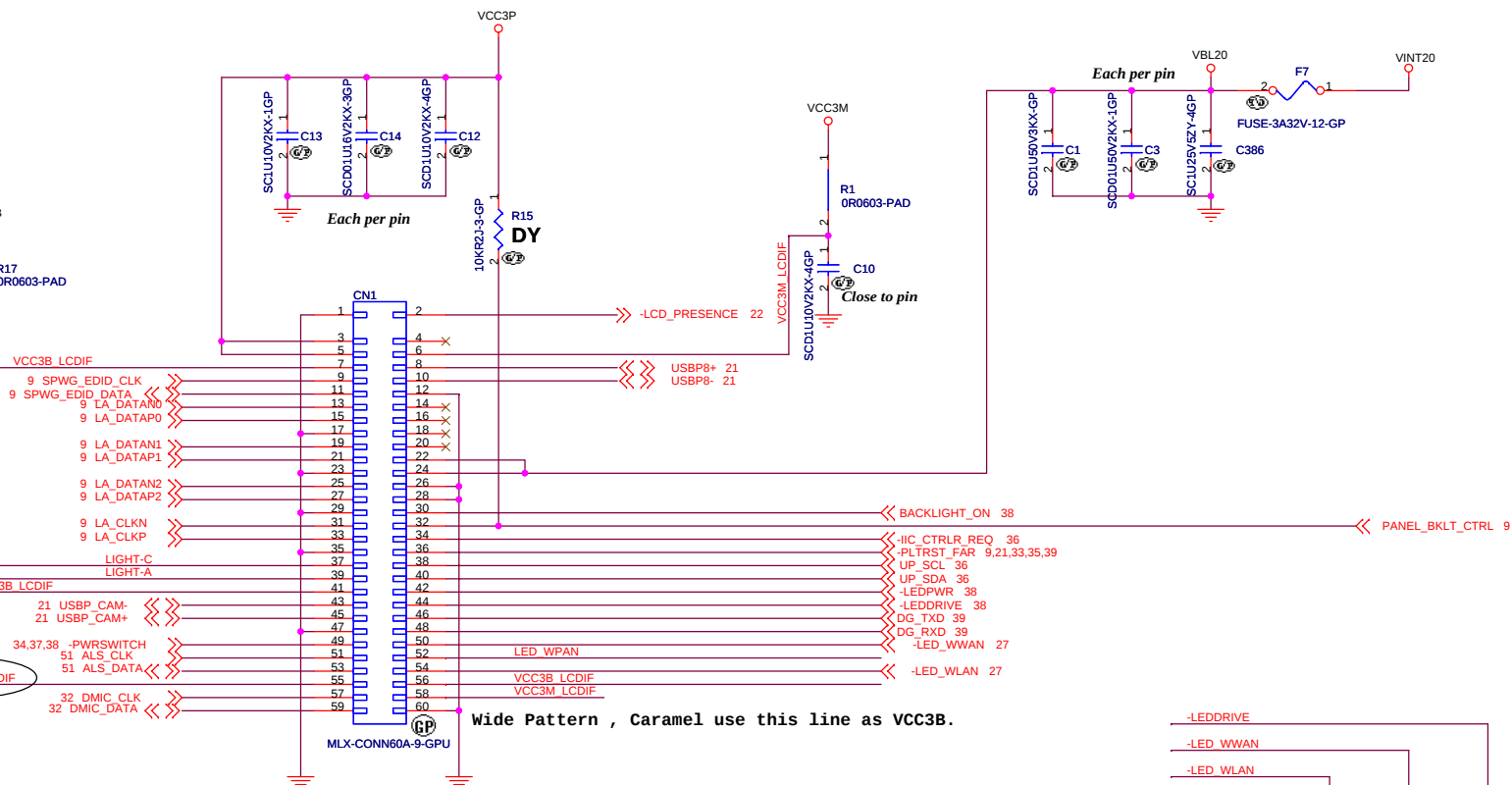
Thinklight Circuit



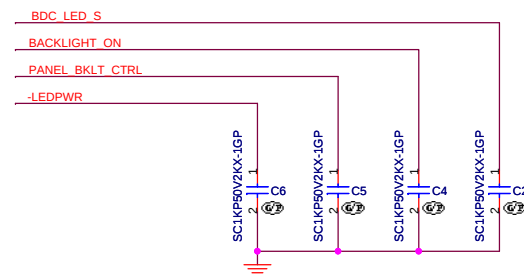
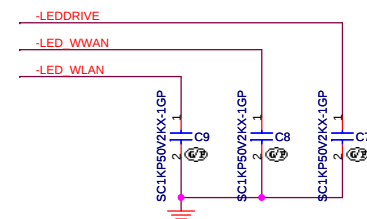
AND GATE
U51 U52 : TC7SH08FU
OR GATE
U53 : TC7SH32FU
NAND GATE
U16 : TC7SH00FU



Change to 0-ohm at 4/26
ECR No.R0702722

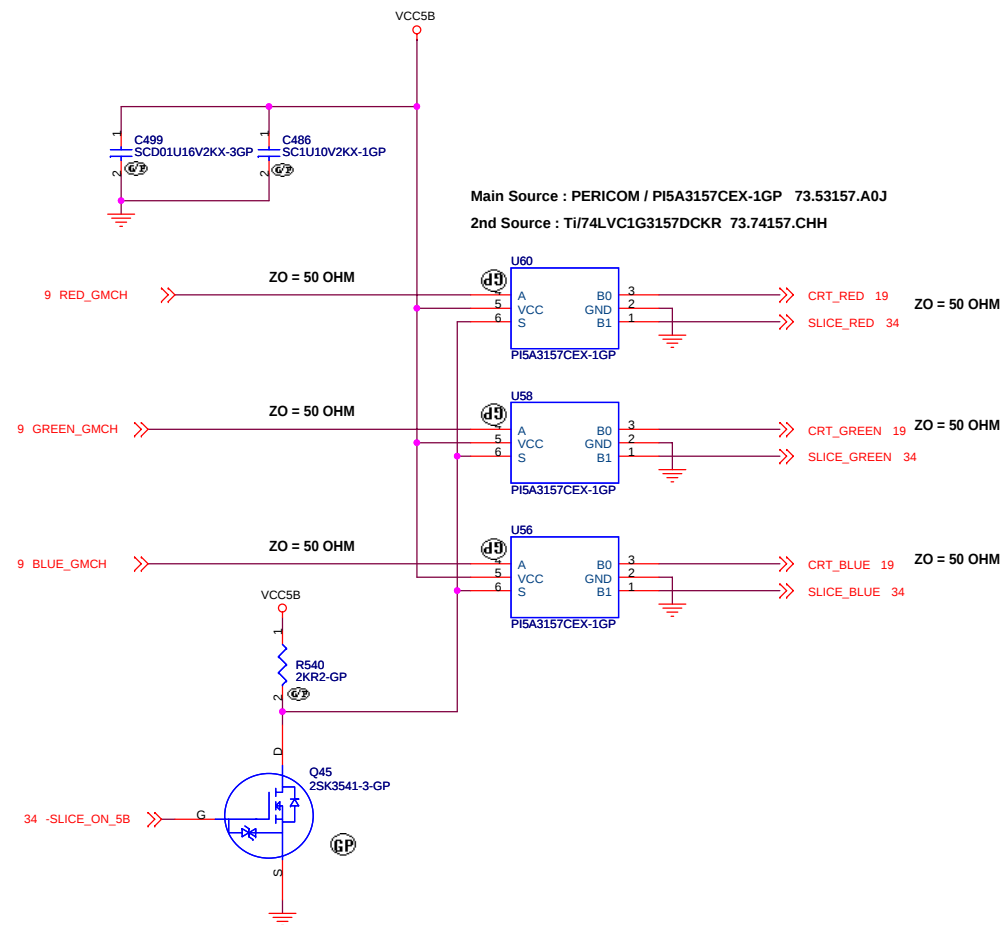


Wide Pattern , Caramel use this line as VCC3B.



<Variant Name>

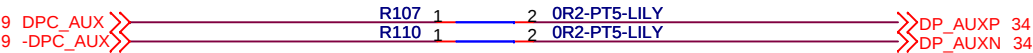
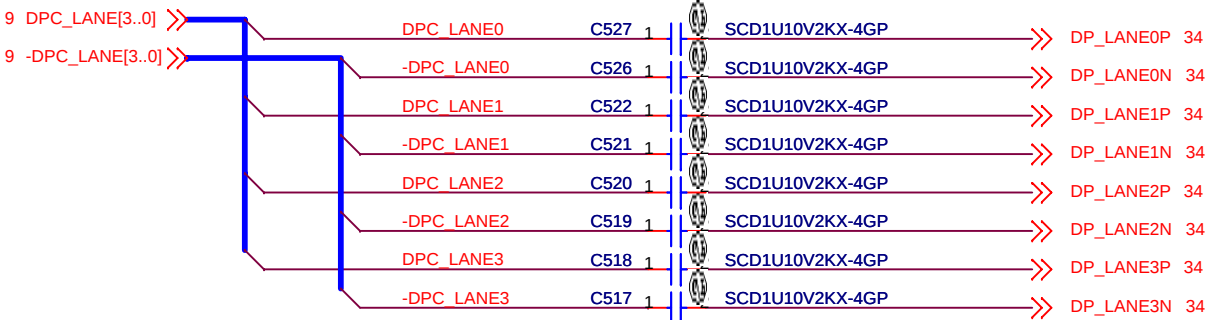
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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
LCD CONNECTOR			
Size A3	Document Number		Rev 2
Caramel-1			
Date: Thursday, July 31, 2008	Sheet 16	of	61



<Variant Name>

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Title			
CRT SELECTOR			
Size A3	Document Number Caramel-1		Rev 2
Date: Thursday, July 31, 2008		Sheet 17 of	62

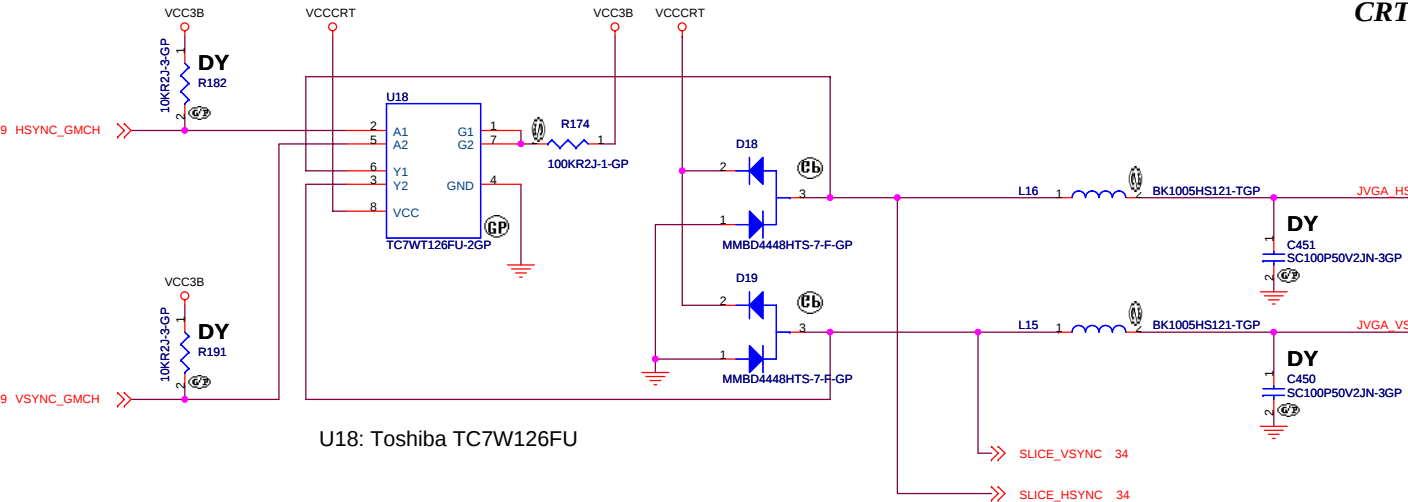
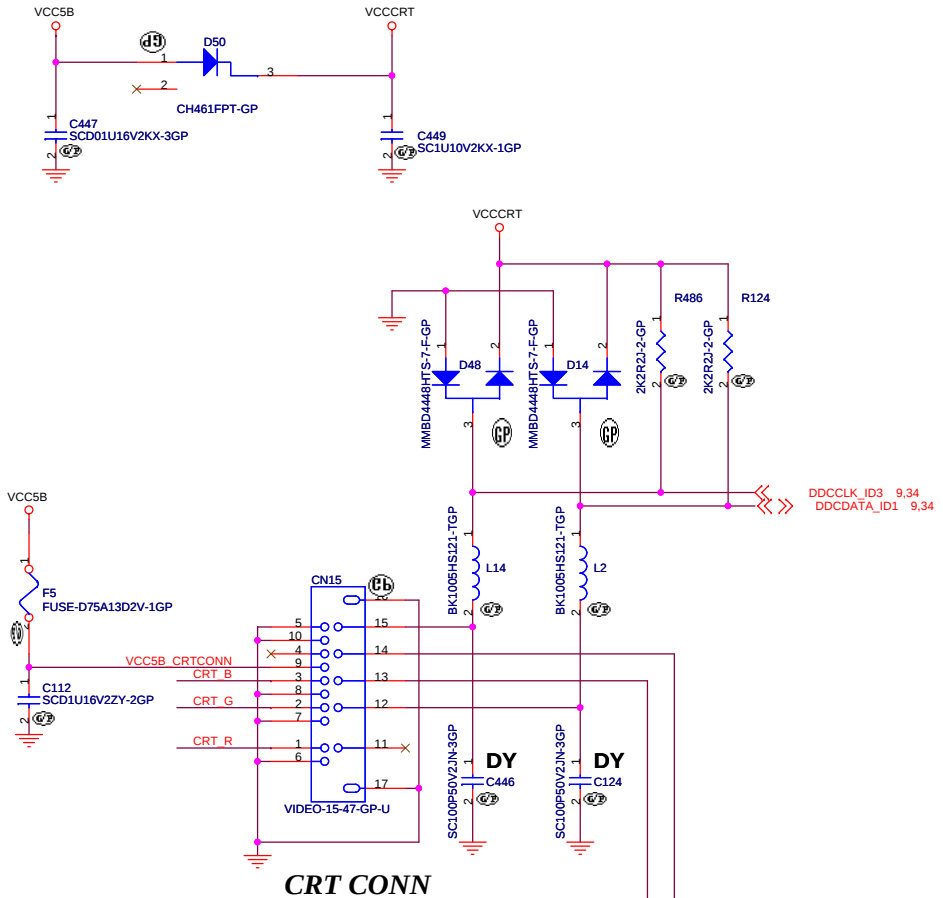
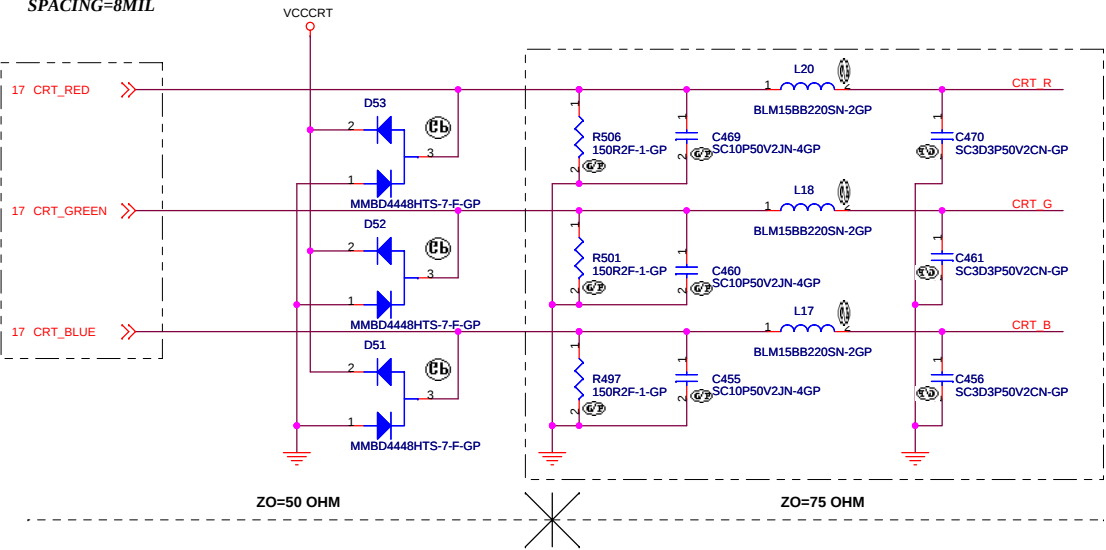
System DP: GMCH to SLICE Connector



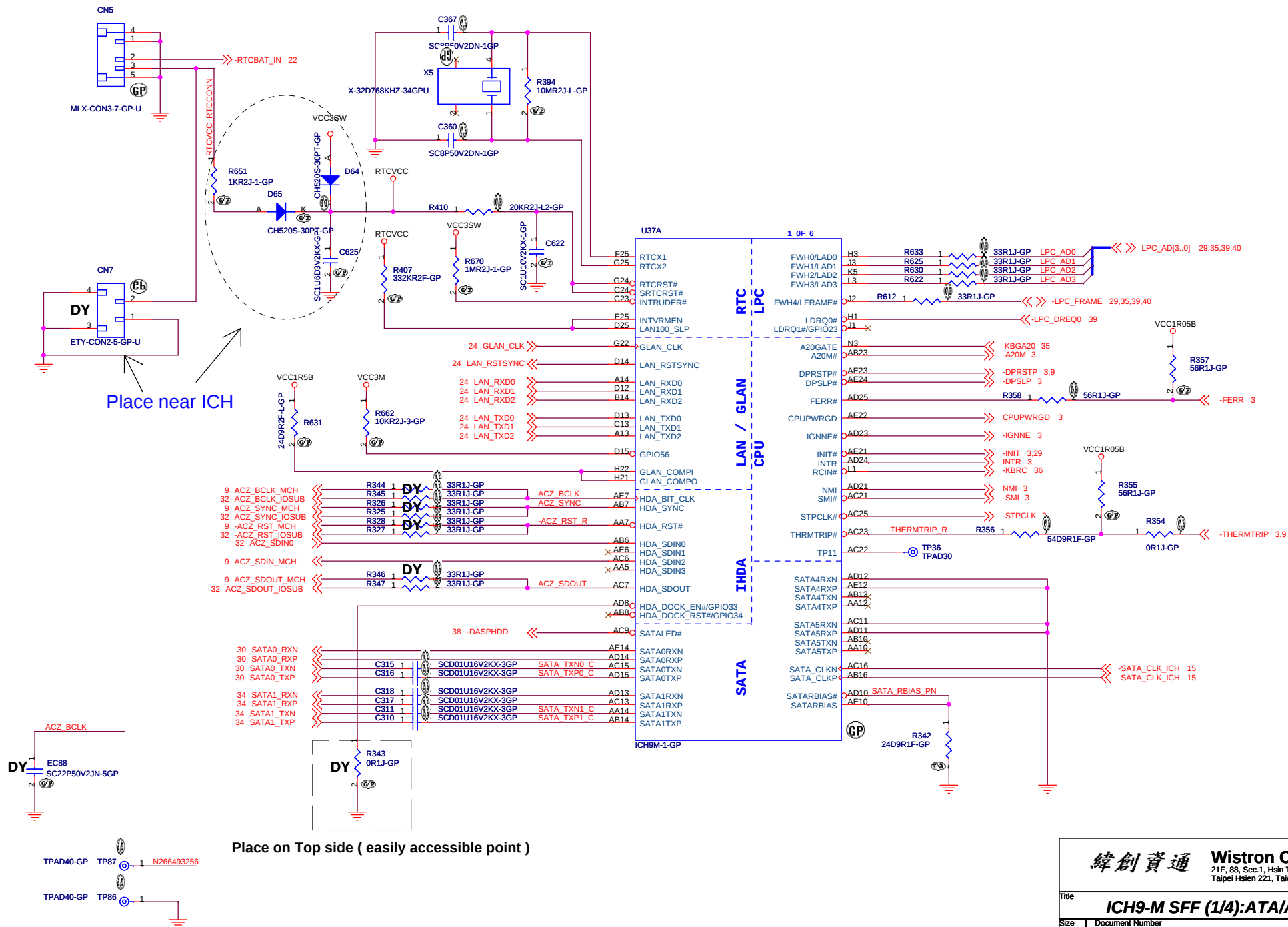
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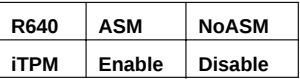
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title Display Port AC Coupling		
Size A4	Document Number Caramel-1	Rev 2
Date: Thursday, July 31, 2008		Sheet 18 of 62

GND GUARDING
EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)
SPACING=8MIL



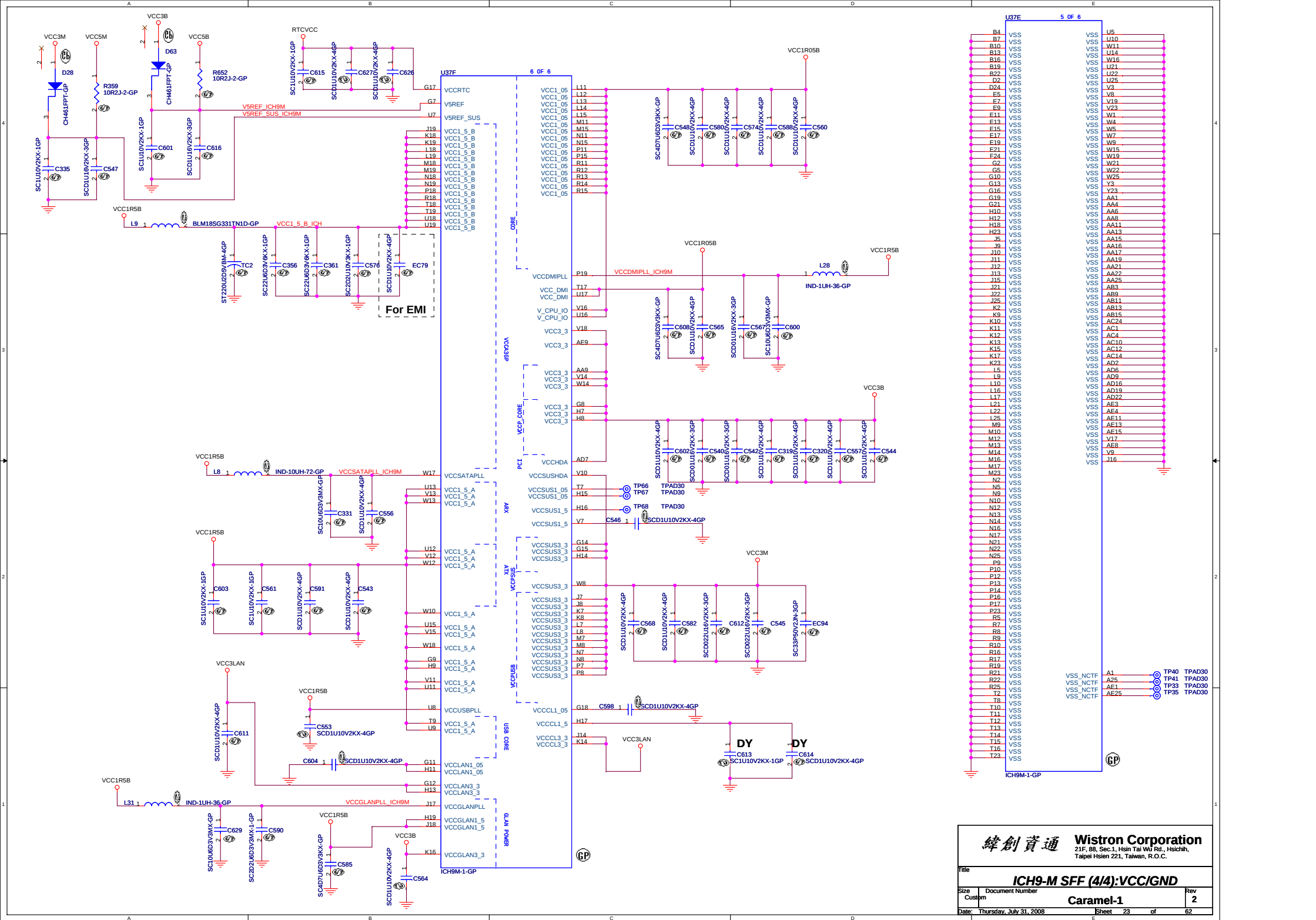
U18: Toshiba TC7W126FU

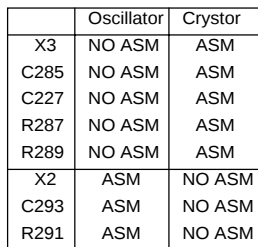
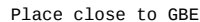




- USB0 : Port0 (USB EXT BOARD)
- USB1 : Port1
- USB2 : WLAN
- USB3 : WWAN
- USB4 : UWB
- USB5 : 3-in-1
- USB6 : Port3 (IOSUB CARD)
- USB7 : Express
- USB8 : FPR (LCD CONN)
- USB9 : BDC
- USB10 : Slice
- USB11 : Camera (LCD CONN)

-GNT0	BIOS
LOW	SPI
HIGH	LPC(FWH)

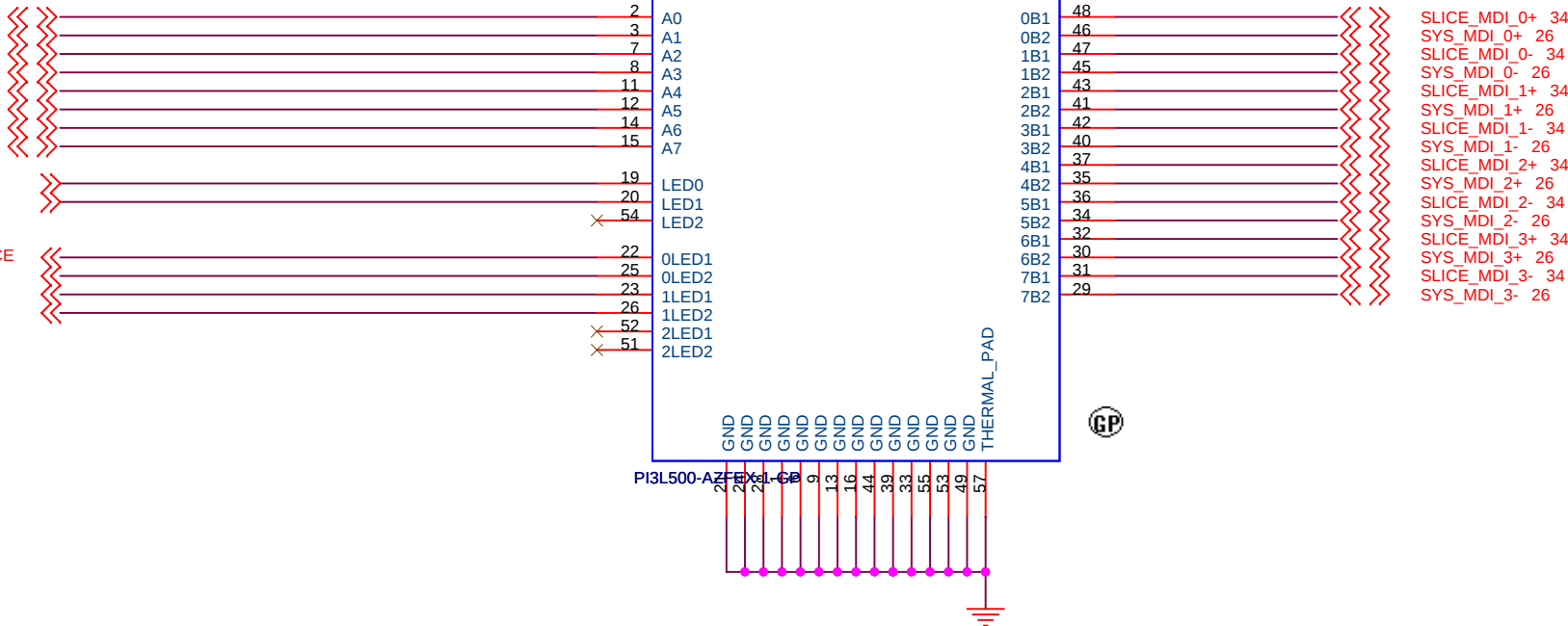




24 MDI_0+
24 MDI_0-
24 MDI_1+
24 MDI_1-
24 MDI_2+
24 MDI_2-
24 MDI_3+
24 MDI_3-

24 -RJ45_ACTIVITY
24 -RJ45_LINKUP

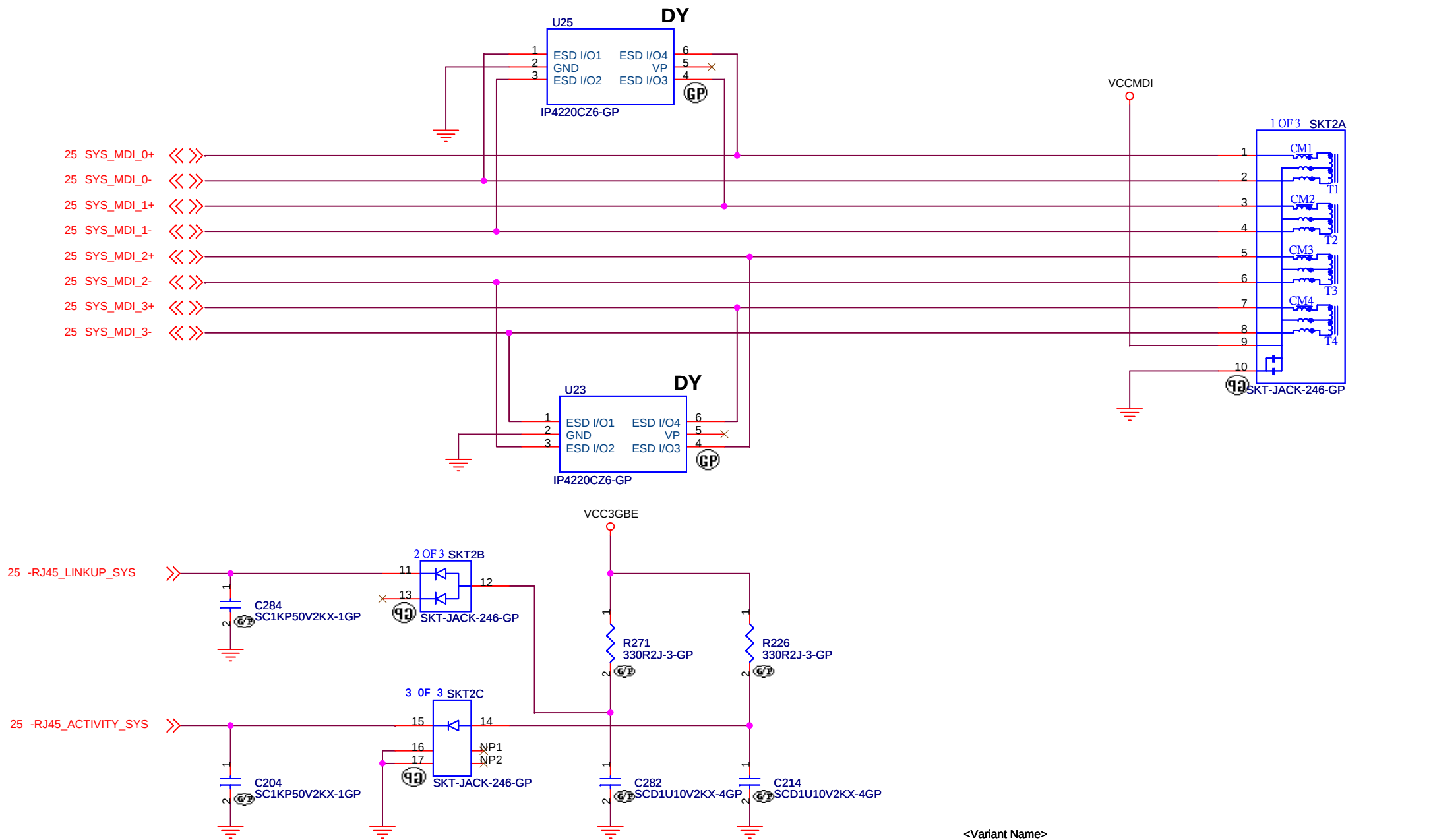
34 -RJ45_ACTIVITY_SLICE
26 -RJ45_ACTIVITY_SYS
34 -RJ45_LINKUP_SLICE
26 -RJ45_LINKUP_SYS



		Vendor P/N	Wistron P/N
1st	Pericom	PI3L500AZFEX	73.3L500.003
2nd	TI	TS3L500AERHUR	73.3L500.A0V
3rd	MAXIM	MAX4927ETN+	74.04927.073

<Variant Name>

緯創資通			Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.					
Title					
GBE LAN SW					
Size A4	Document Number				Rev
	Caramel-1				2
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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

RJ45 CONN

Size
A4

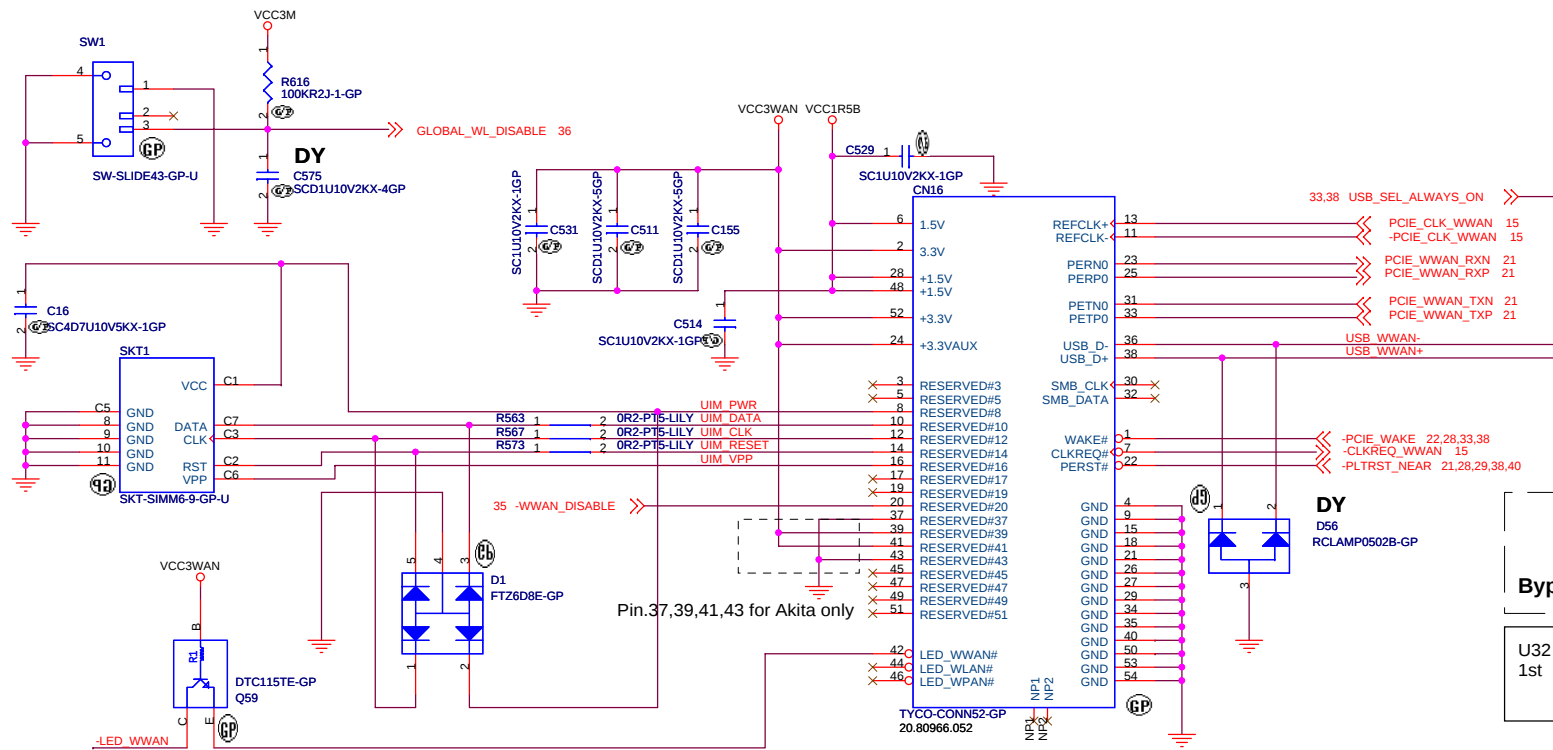
Document Number

Caramel-1

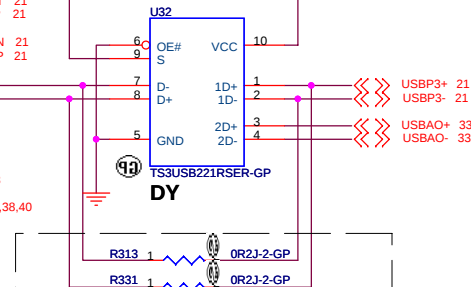
Rev
2

Date: Thursday, July 31, 2008

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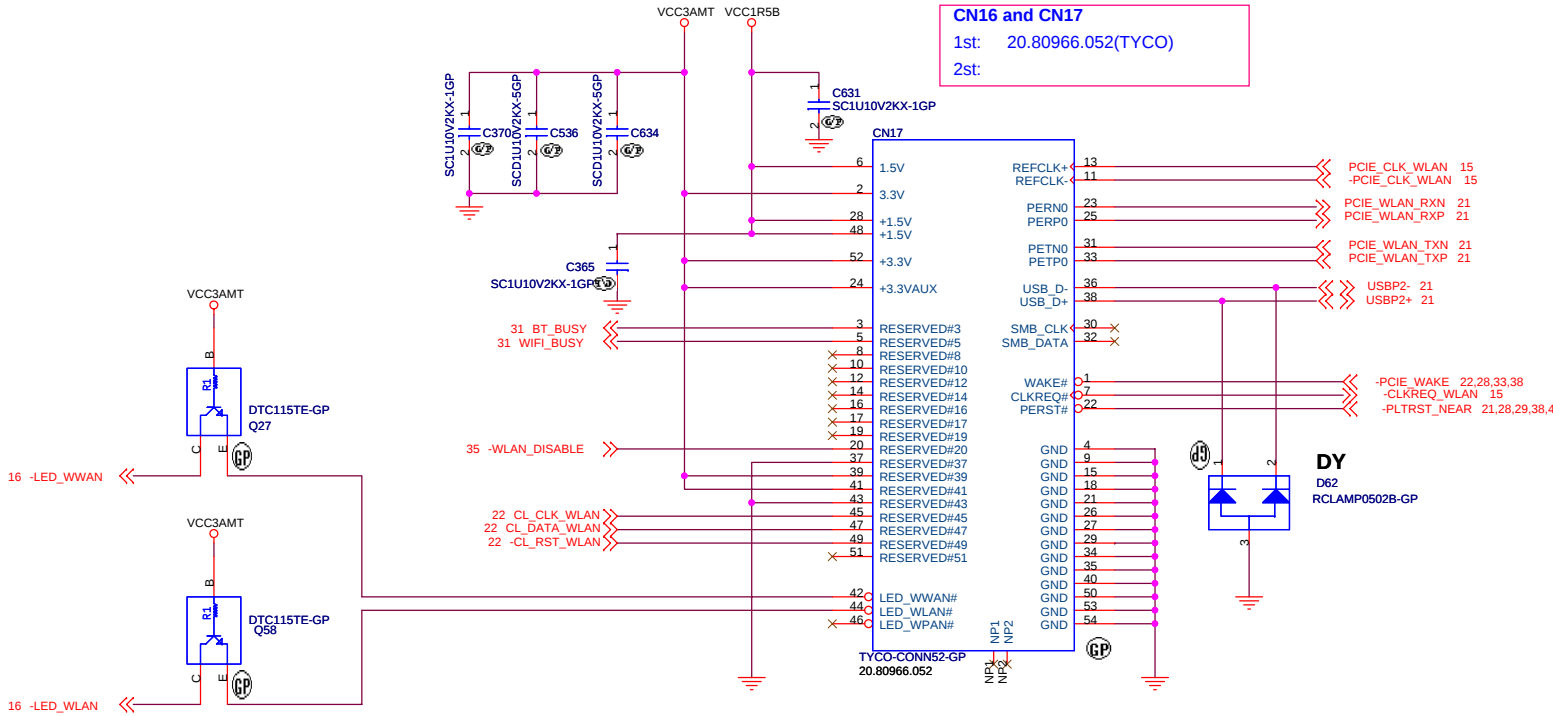
Always ON	Regular	Lite/BB/NO
U32	ASM	No_ASM
R313	No_ASM	ASM
R331	No_ASM	ASM



Bypass U32 as short as possible

U32
1st PI3USB10MZK (73.3US10.A03)

CN16 and CN17
1st: 20.80966.052(TYCO)
2st:



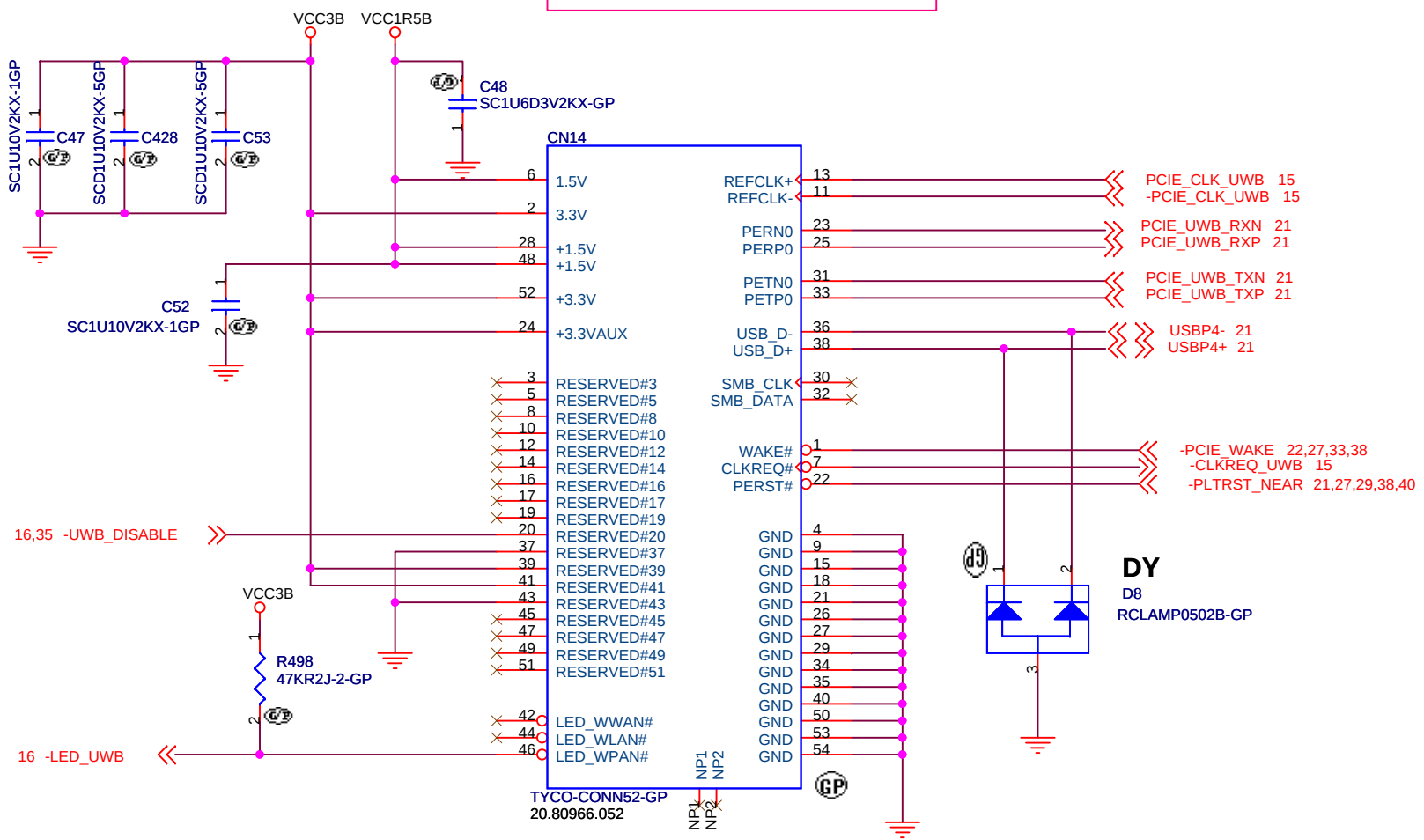
WWAN CONTROL TABLE

PAGE	LOCATION	YES	NO
27	CN16 SKT1 D1 C16 C531 C511 C155 C529	ASM	NON_ASM
58	Q14 R149 D17 R160 C153 R143	ASM	NON_ASM
60	H12 H13	ASM	NON_ASM

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

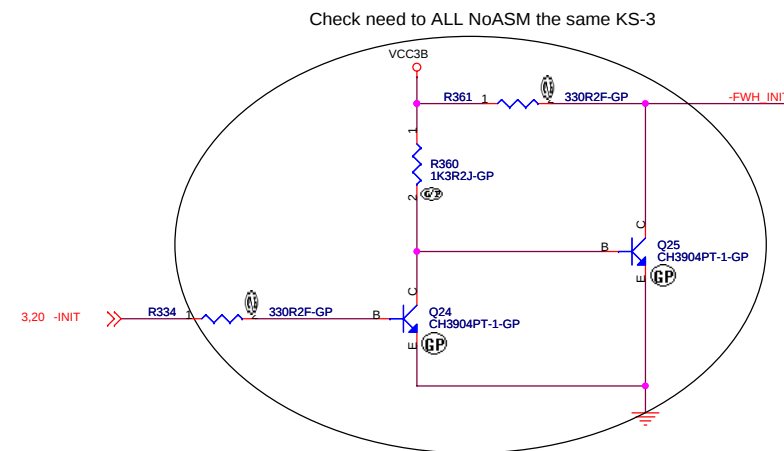
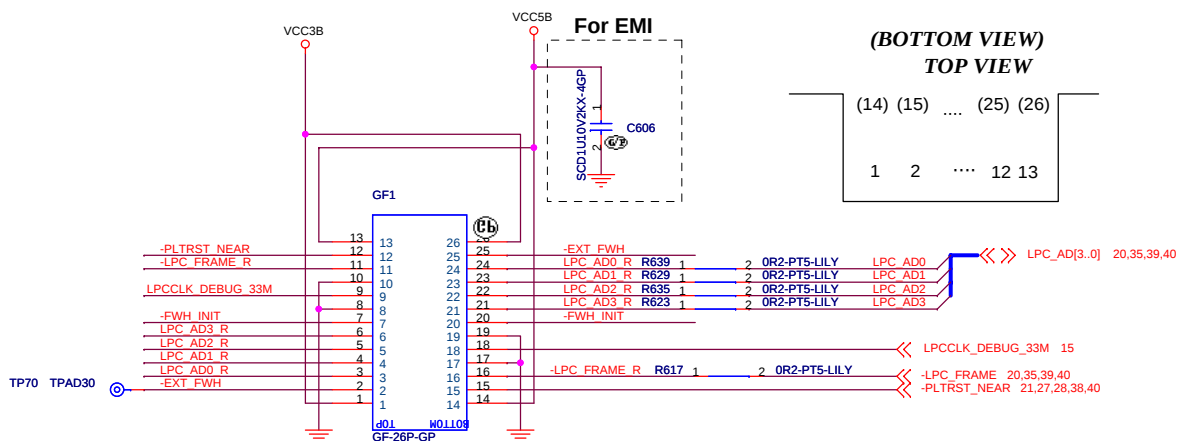
Title		
Size A3		
Document Number	Caramel-1	Rev 2
Date: Thursday, July 31, 2008	Sheet 27	of 62



CN14
1st: 20.80966.052(TYCO)
2st:

DY
D8
RCLAMP0502B-GP

Golden Finger for Debug Board



64Mbit SPI FLASH :

Macronix MX25L6405DZNI-12G: 72.25640.B01

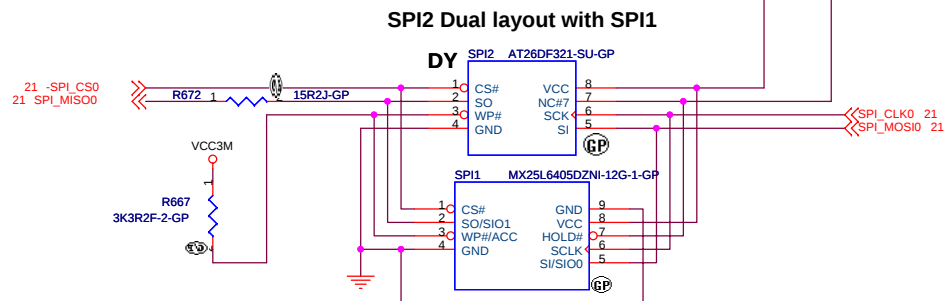
Winbond W25X64VZEIG

ATMEL XXXXXX

32Mbit SPI FLASH :

ATMEL/AT26DF321-SU:72.26321.A01

MXIC/MX25L3205DM2I-12G:72.25325.A01



SO8 and WSON8 are both supported!

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SPI

Size
A3

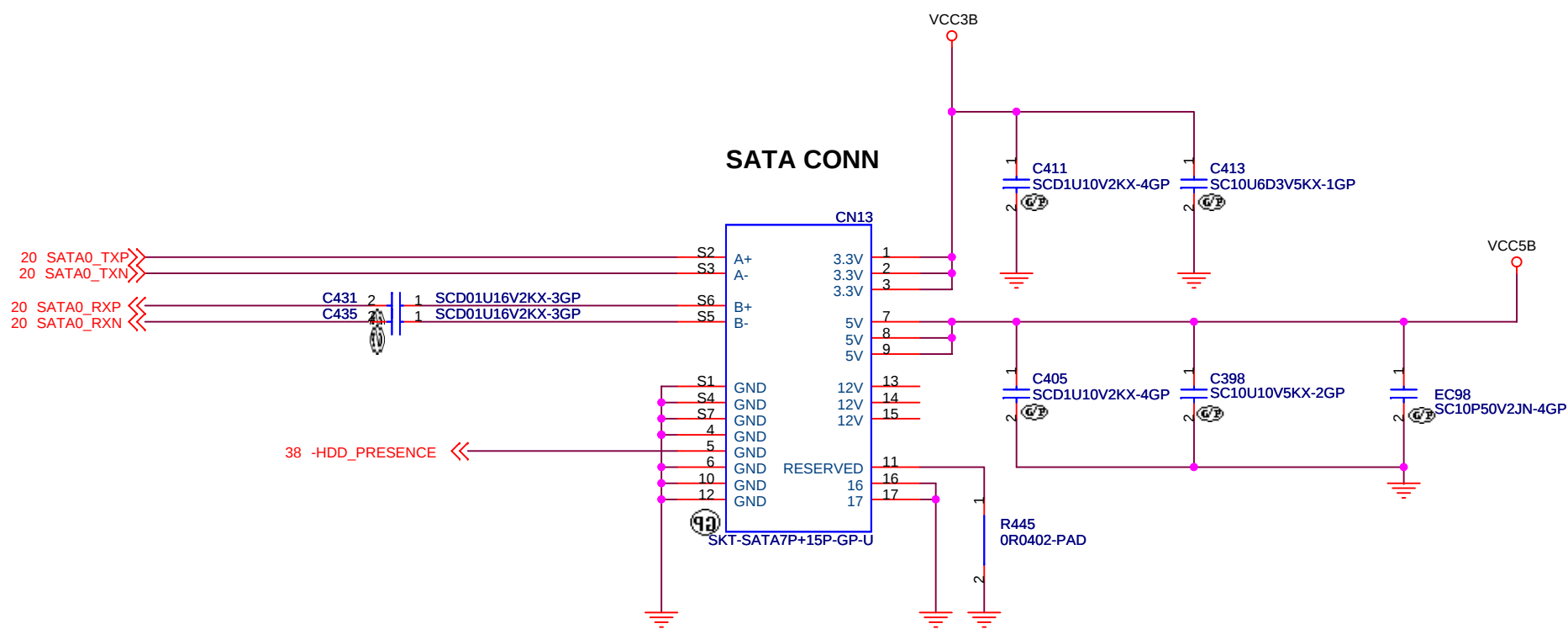
Document Number

Caramel-1

Rev
2

Date: Thursday, July 31, 2008

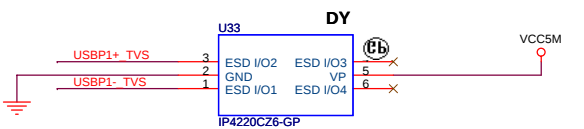
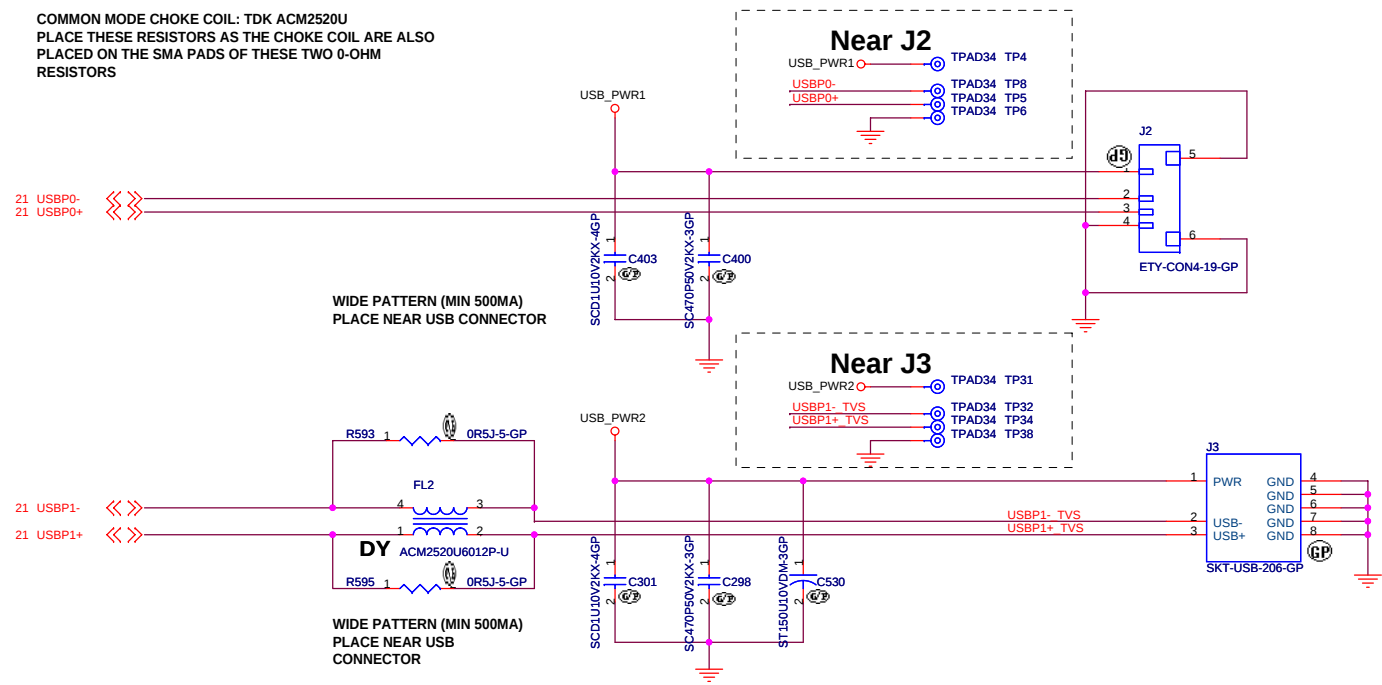
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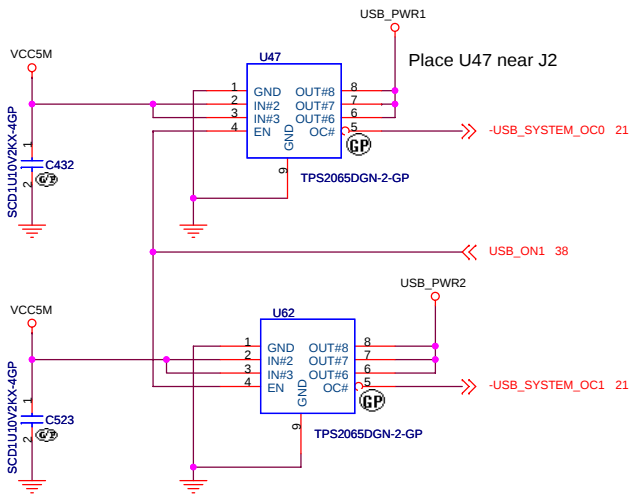
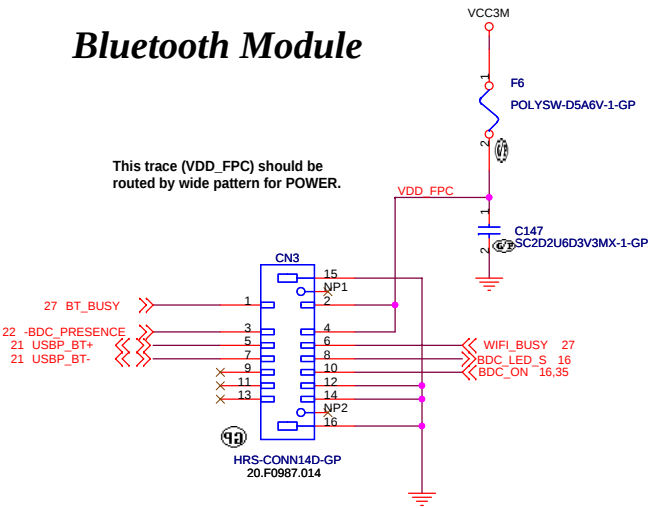
<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
2.5 inch SATA Connector			
Size	Document Number		Rev
A4	Caramel-1		2
Date:	Thursday, July 31, 2008	Sheet	30 of 62
2		1	

COMMON MODE CHOKE COIL: TDK ACM2520U
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO
PLACED ON THE SMA PADS OF THESE TWO 0-OHM
RESISTORS



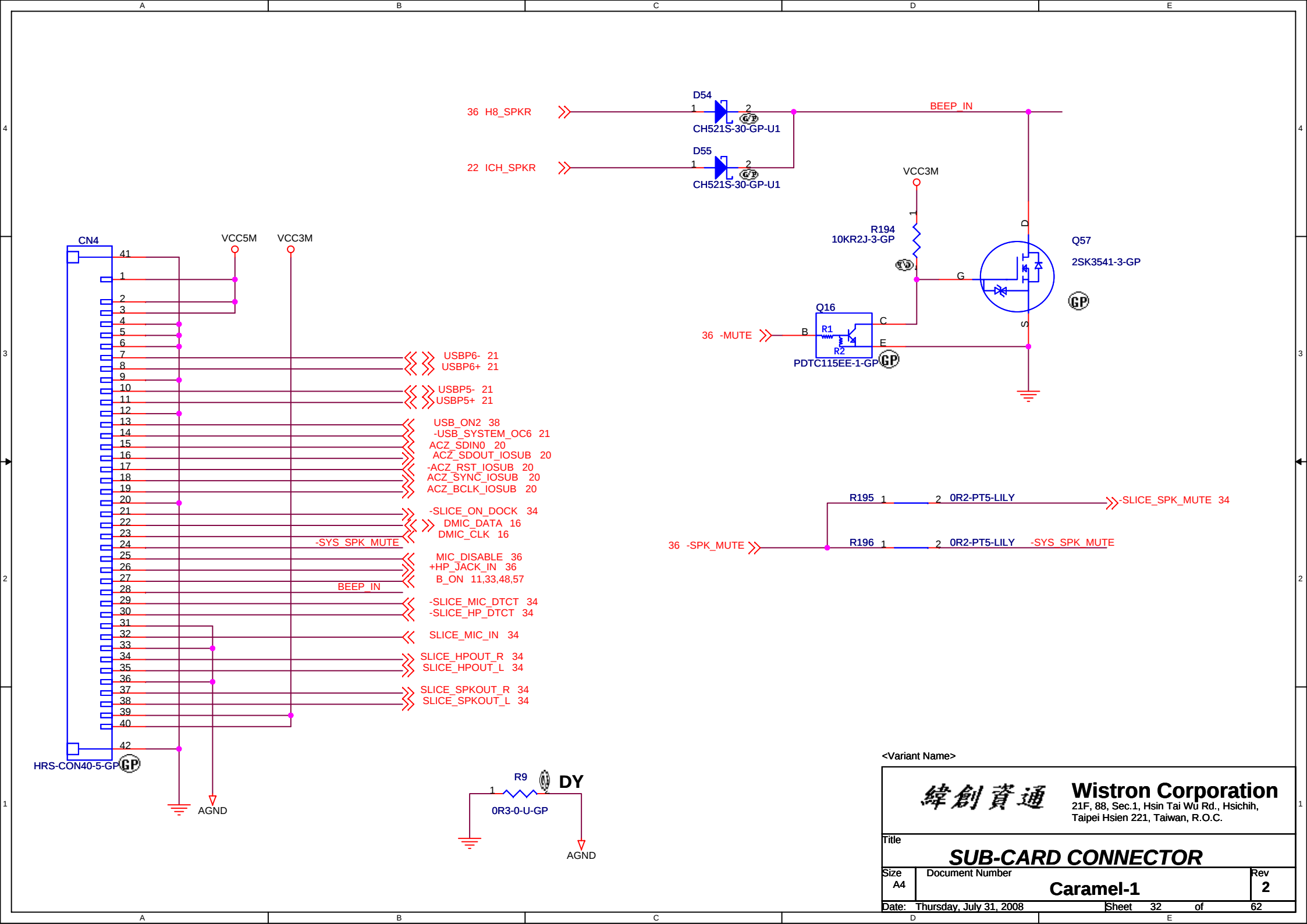
Bluetooth Module



	TI P/N	WISTRON P/N
1	TPS2065DGN-GP	74.02065.079
2	TPS2065DGN-1-GP	74.02065.A79

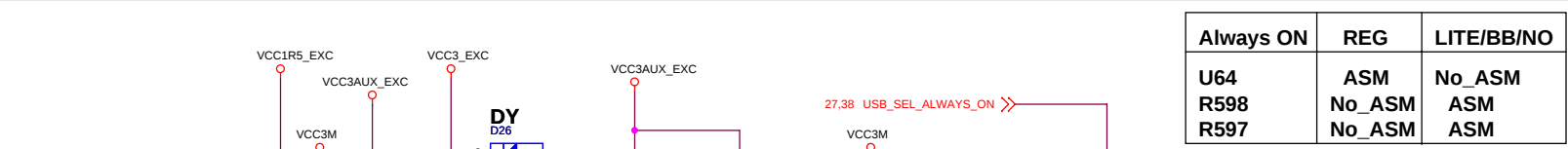
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緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
USB POWER / CONNECTOR			
Size	Document Number	Rev	
A3		2	
Date: Thursday, July 31, 2008		Sheet	31 of 62

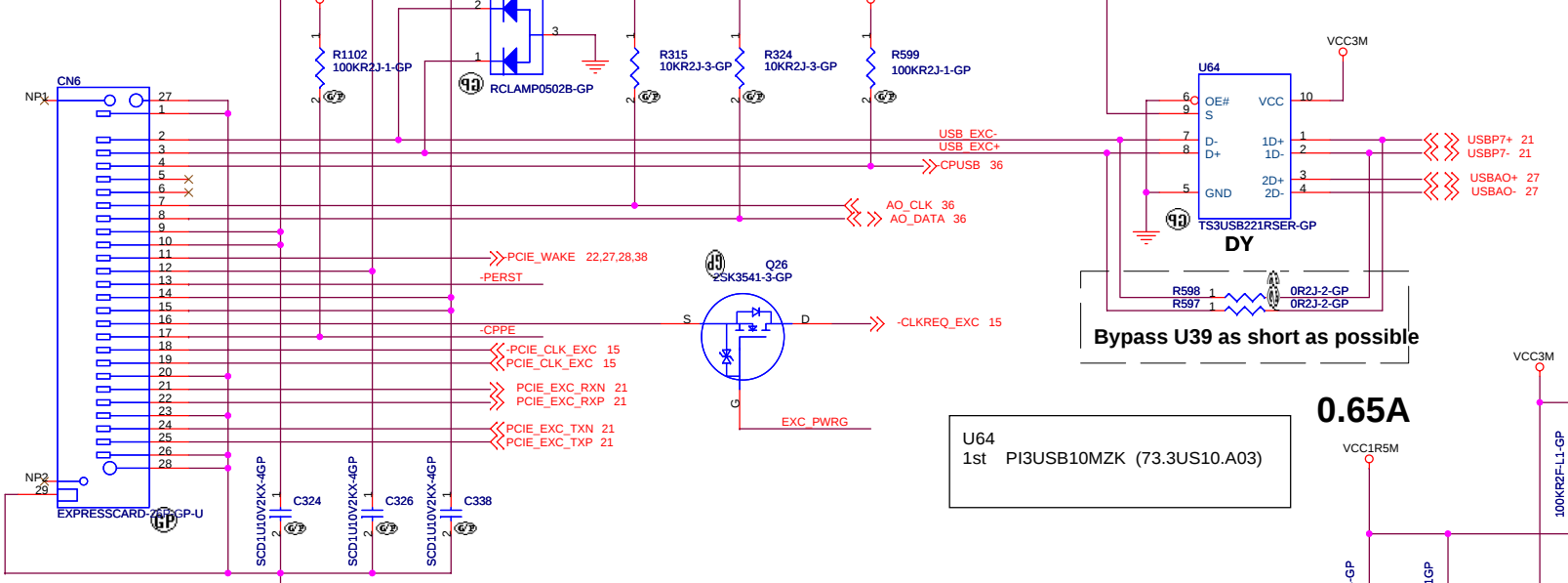


<Variant Name>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
SUB-CARD CONNECTOR			
Size A4	Document Number Caramel-1		Rev 2
Date: Thursday, July 31, 2008		Sheet 32 of	62

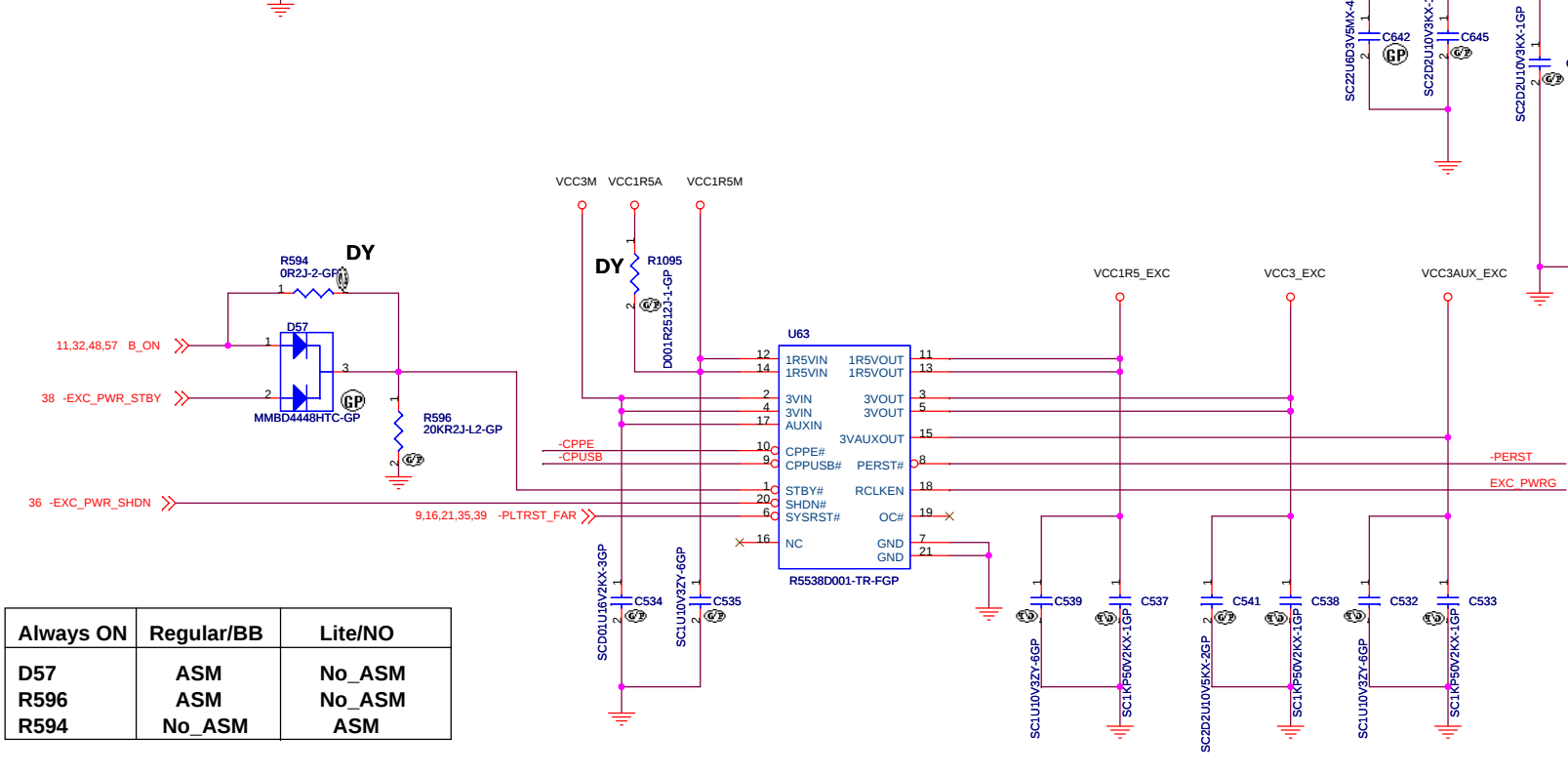


Always ON	REG	LITE/BB/NO
U64	ASM	No_ASM
R598	No_ASM	ASM
R597	No_ASM	ASM



U64
1st PI3USB10MZK (73.3US10.A03)

0.65A



Always ON	Regular/BB	Lite/NO
D57	ASM	No_ASM
R596	ASM	No_ASM
R594	No_ASM	ASM

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

Express Card Slot/PWR

Size

Document Number

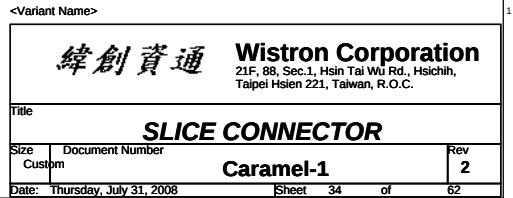
Rev

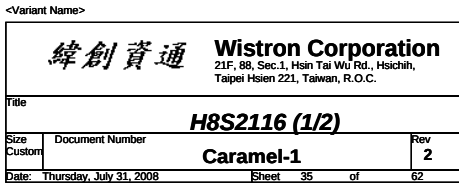
Caramel-1

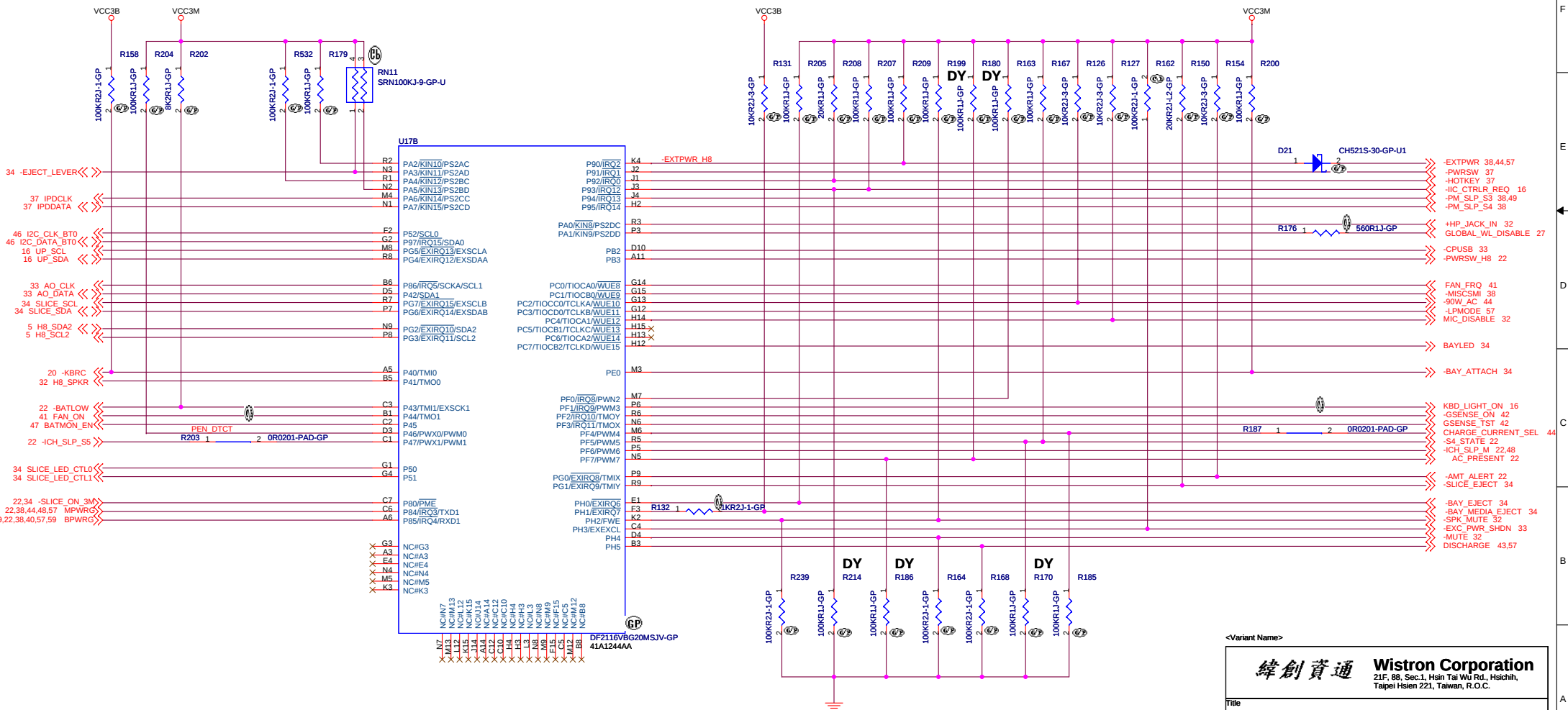
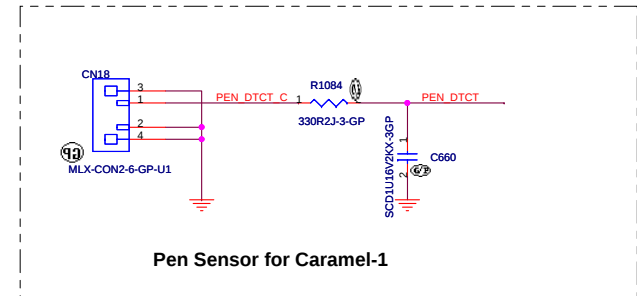
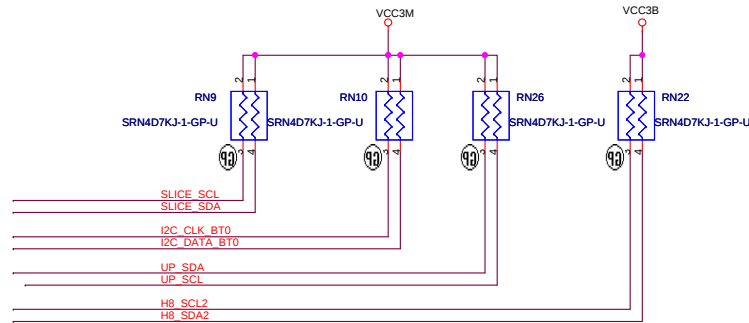
2

Date: Thursday, July 31, 2008

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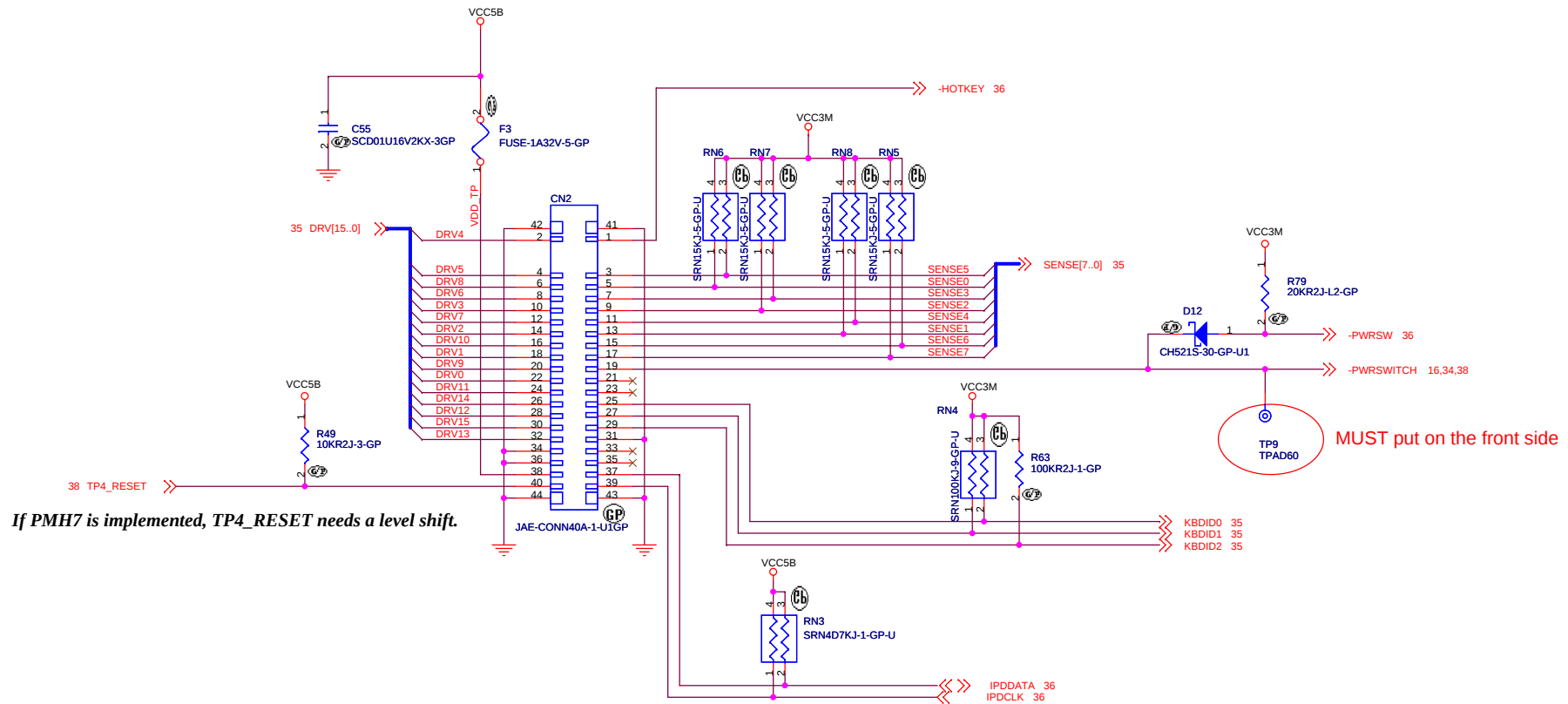


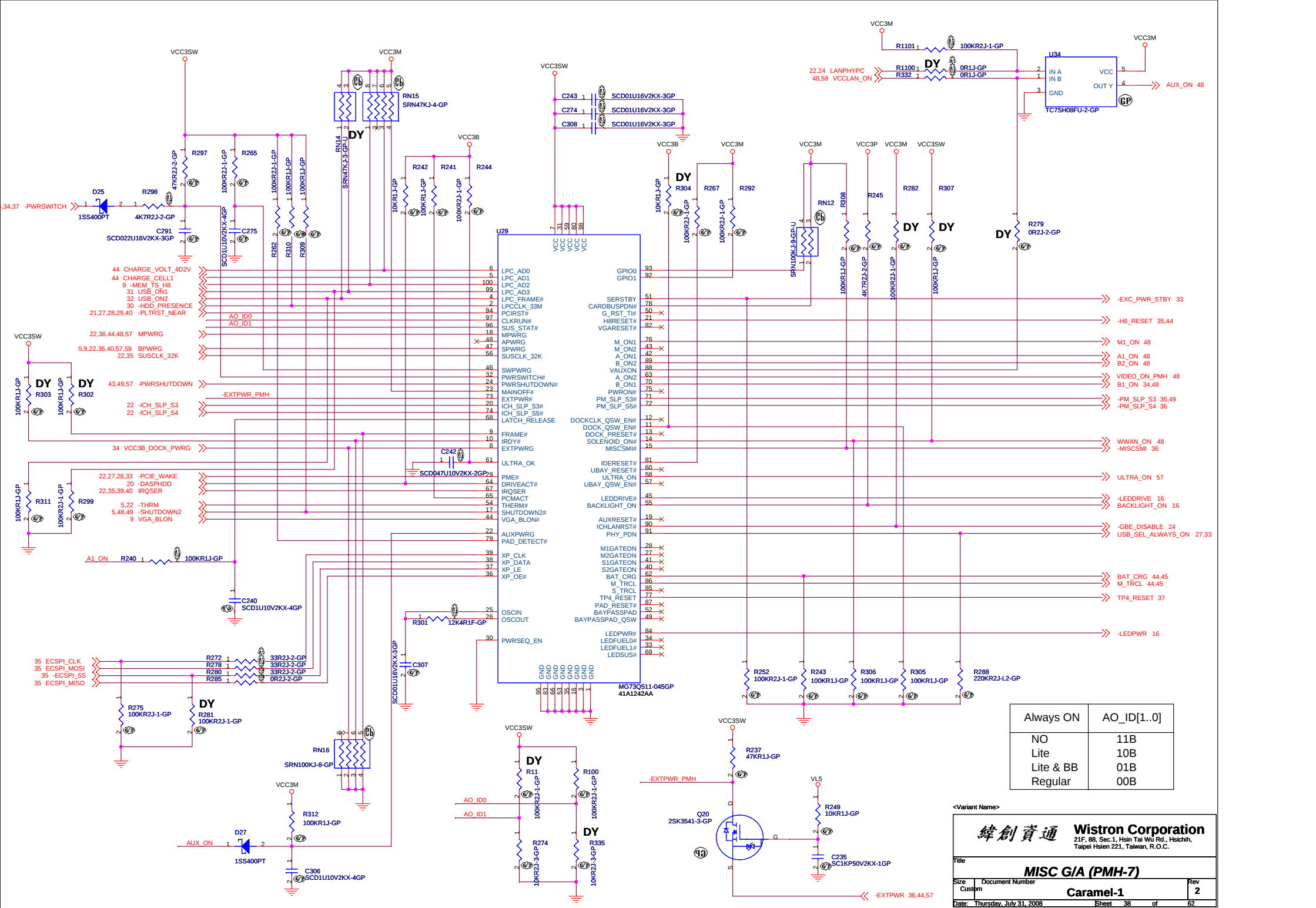


HOTKEY# **IPDCLK**

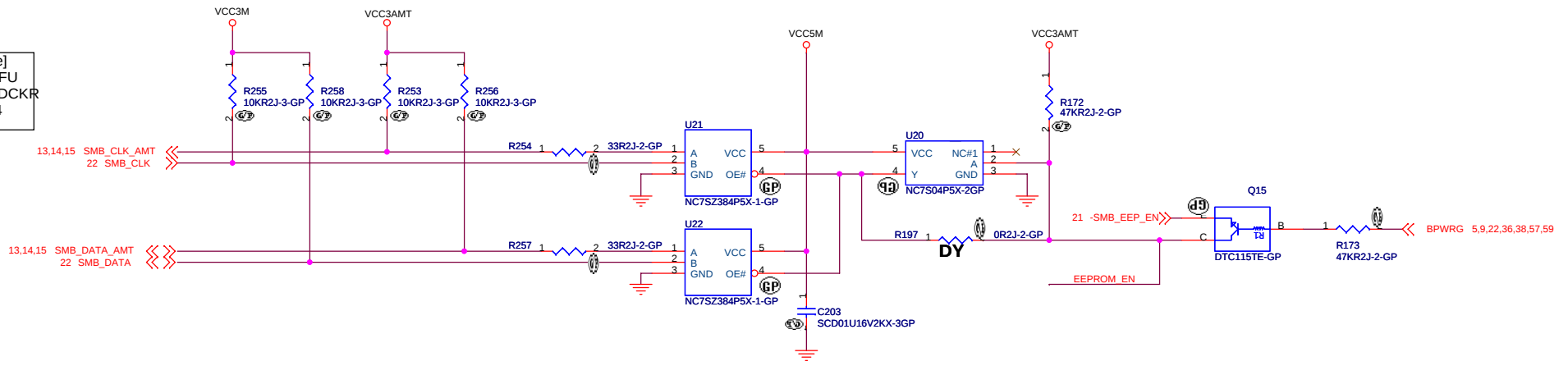
DRV4 **to R425/Q18**

<Variant Name>

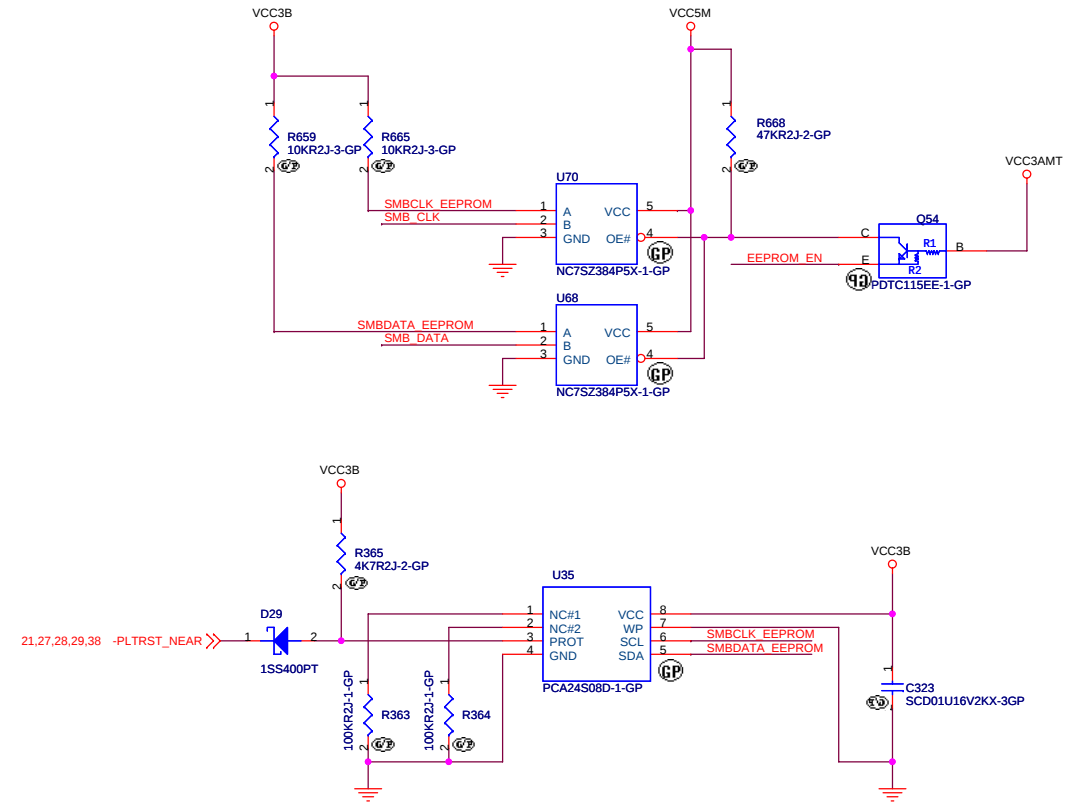




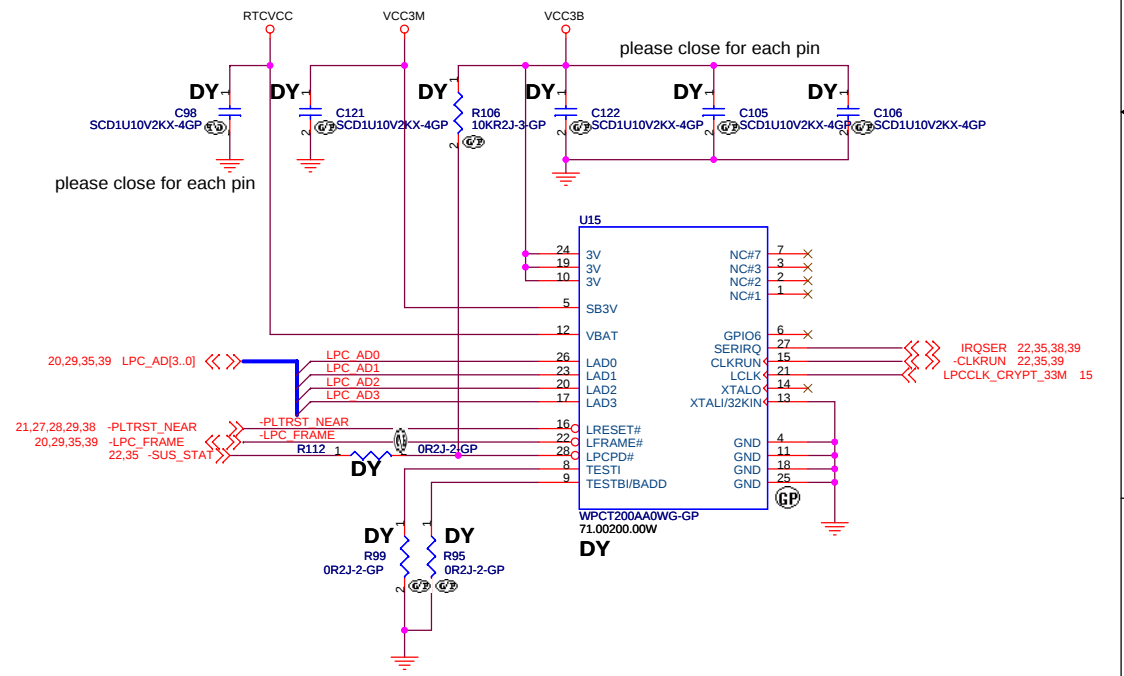
[Sourcing Candidate]
Toshiba TC7SB384FU
TI SN74CBT1G384DCKR
Fairchild NC7SZ384



EEPROM



TCPA

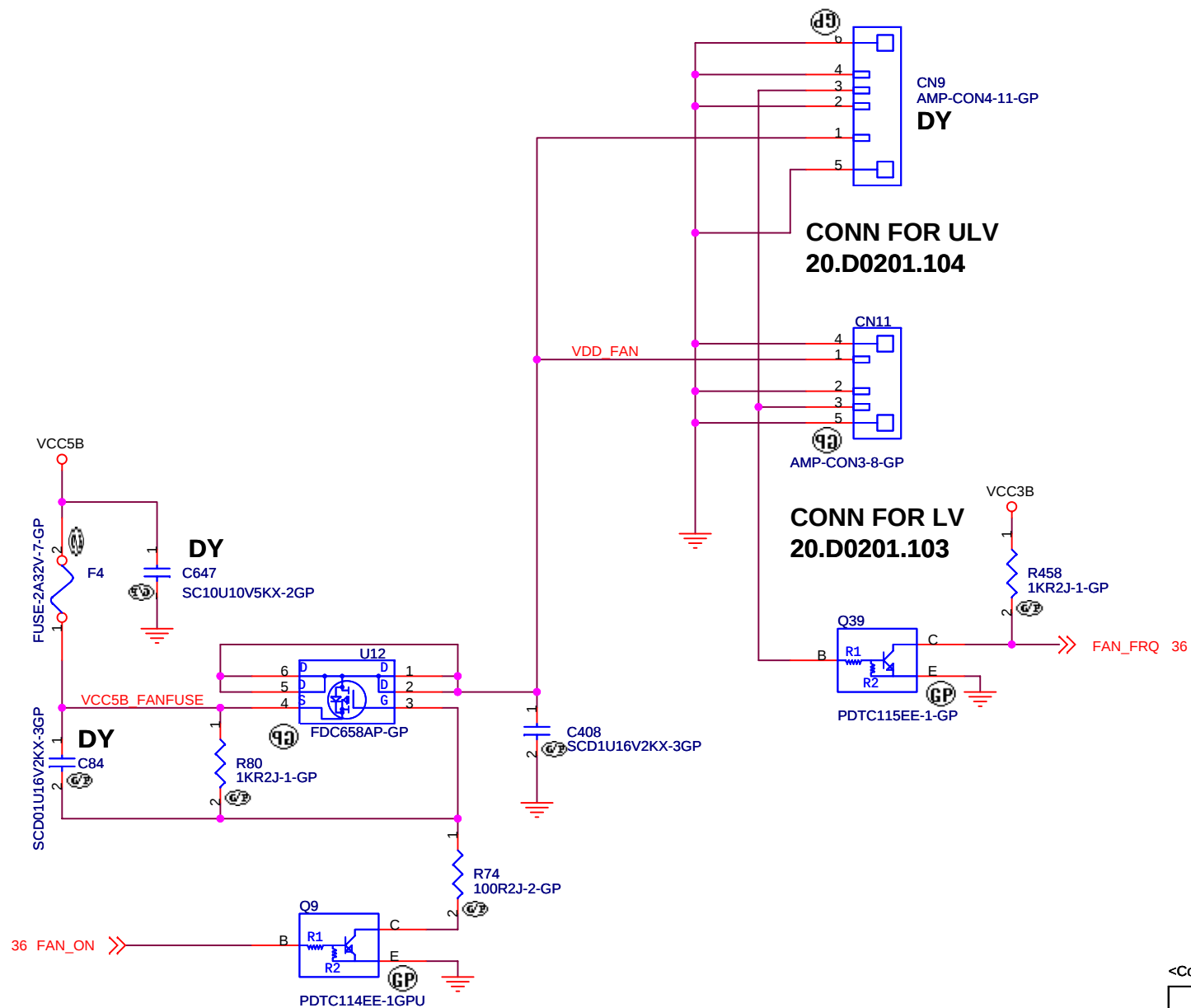


<Core Design>

Title		Wistron Corporation	
Size		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
A3		Document Number	
Date: Thursday, July 31, 2008		Sheet 40 of 62	
E		Rev 2	

EEPROM/TCPA

Caramel-1



<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

FAN CONTROL

Size
A4

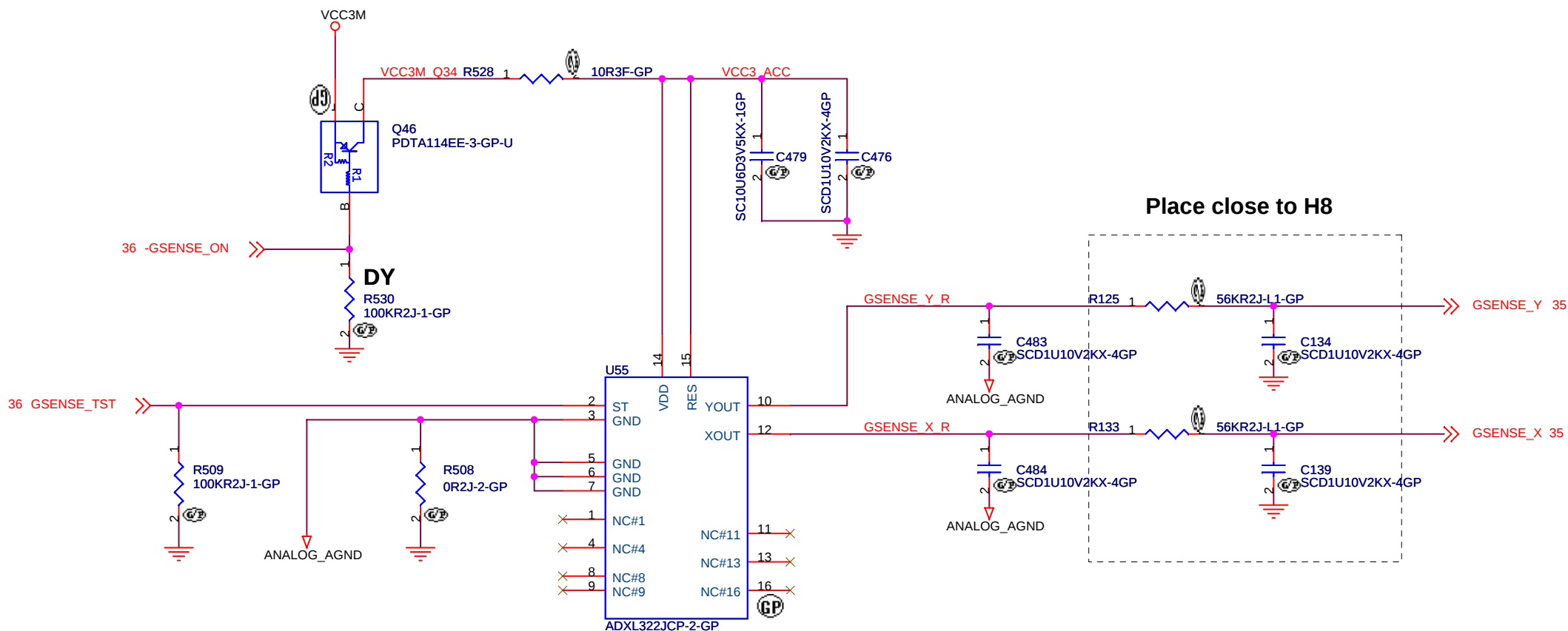
Document Number

Caramel-1

Rev
2

Date: Thursday, July 31, 2008

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Primary : ADI ADXL322
 2nd : STMicro LIS244AL
 3rd : STMicro LIS34AL: 74.00034.0BZ

Layout Comment :

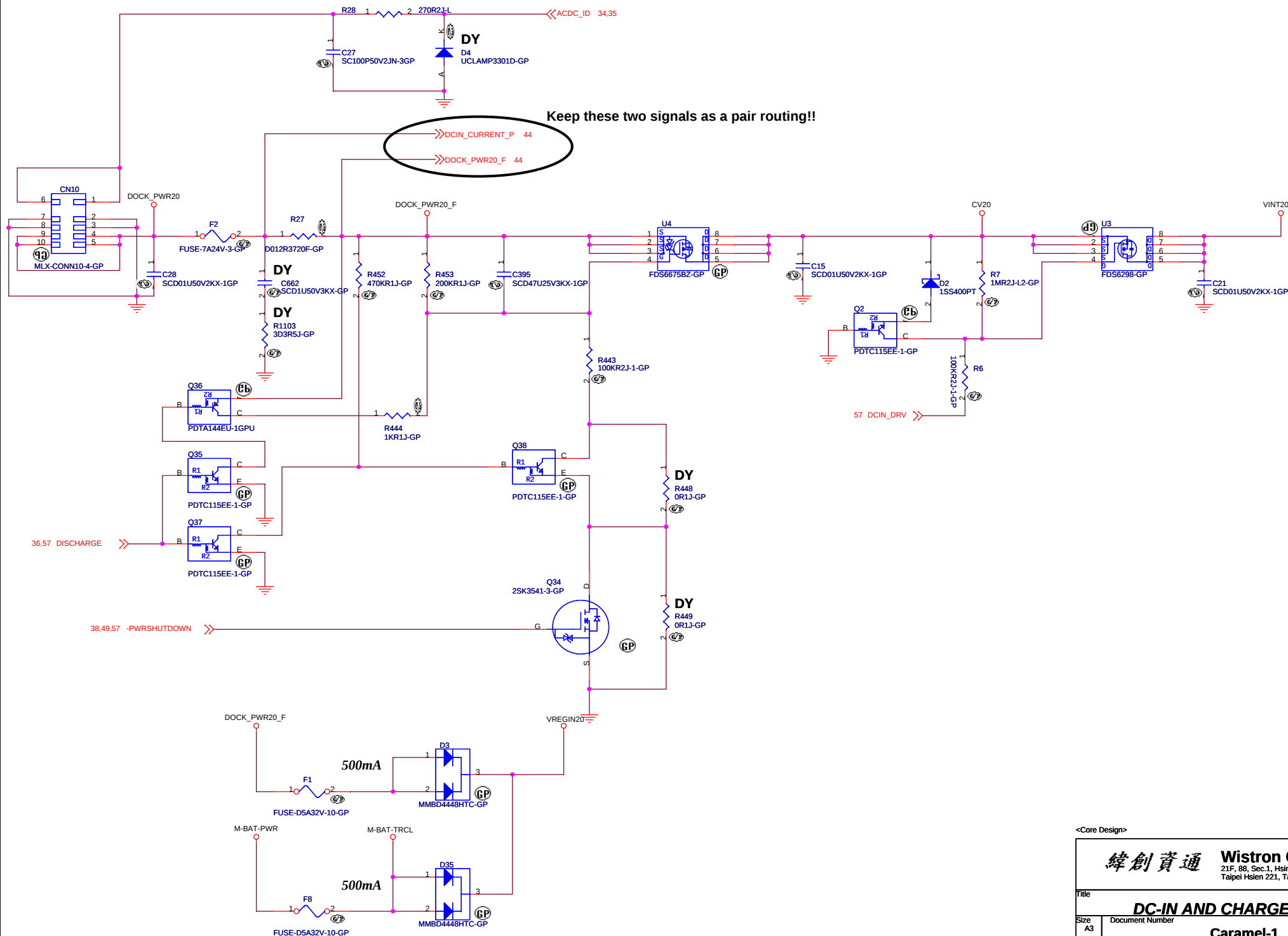
- (1) Place C483, C484, Q46, R528, R530, C479, C476, R509, R508 close to U55.
- (2) Avoid routing under DCDC switching area.

Width = 6 mil & Spacing = 10 mil
 for three Output traces

	ADXL322	
	LIS244AL	No Accel
	LIS34AL	
R530	NO_ASM	ASM
R509	ASM	ASM
All other	ASM	NO_ASM

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
G-SENSOR	
Size A4	Document Number Caramel-1
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<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

DC-IN AND CHARGER (1/2)

Size

A3 Document Number

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Rev

2

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MAX Power Setting		65W	90W
-90W_AC	R36	DY	470K
Logic	Q7	DY	2SK3541

43 DCIN_CURRENT_P

43 DOCK_PWR20_F

SC2200P50V2KX-2GP

R42

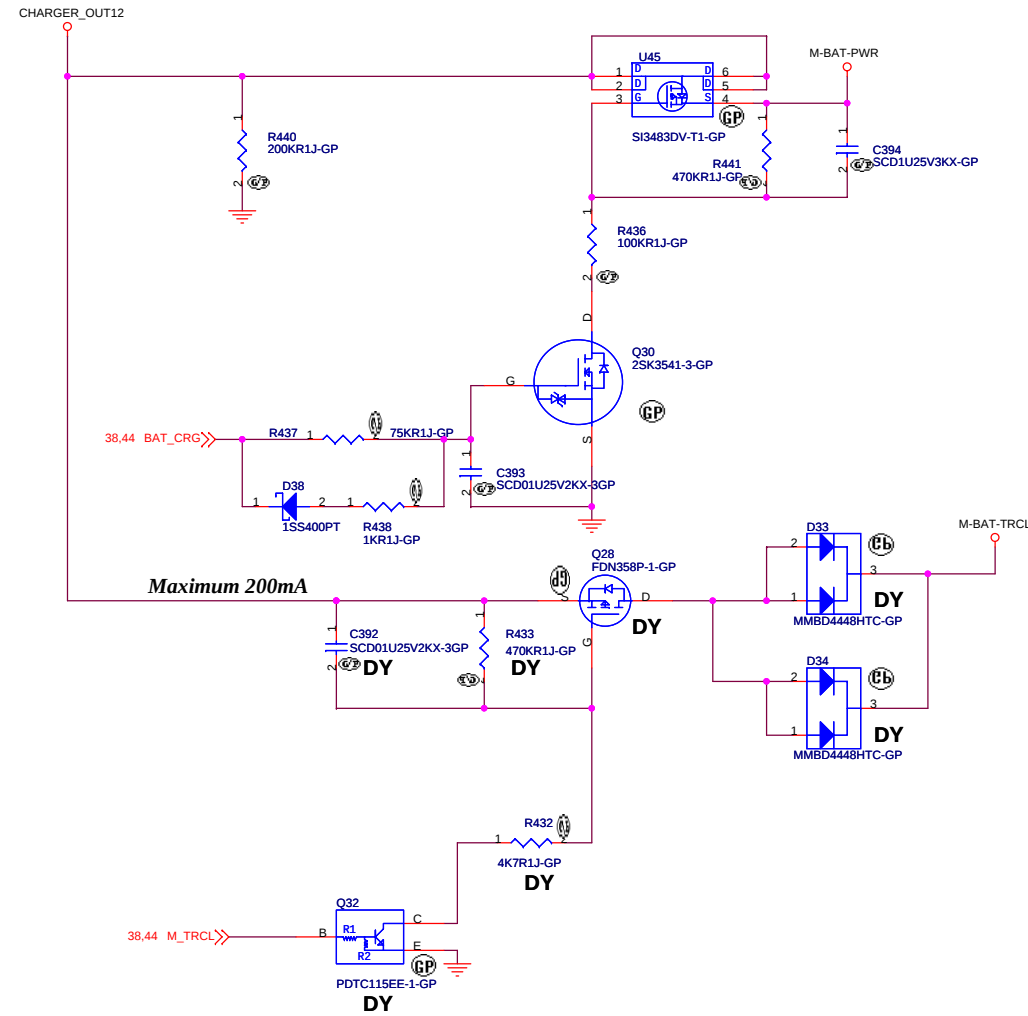
340R2F-GP

C40



緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
DC-IN AND CHARGER (2/2)			
Size	Document Number	Rev	
Custom	Caramel-1	2	
Date: Thursday, July 31, 2008	Sheet 44	of	62

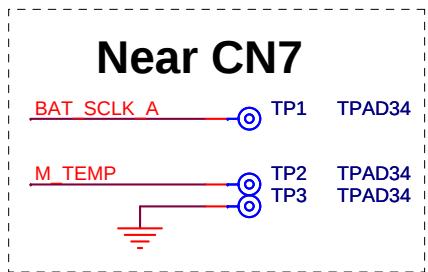
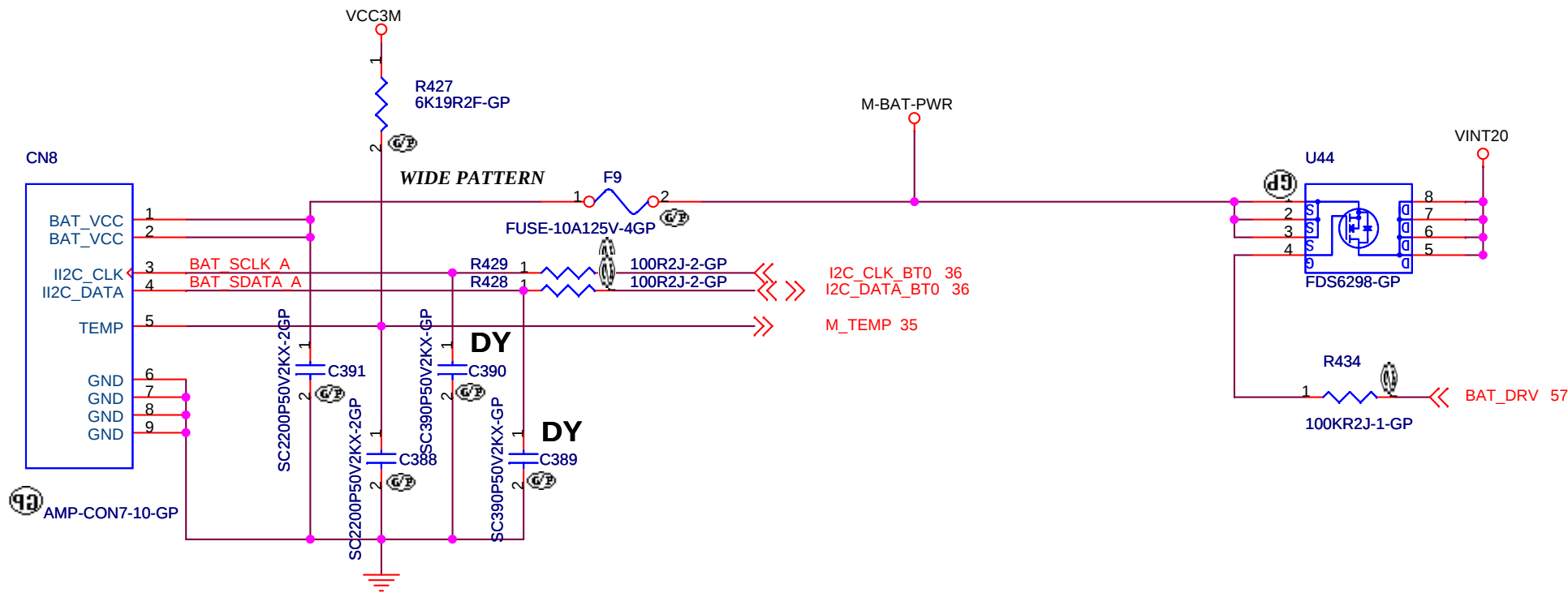


<Core Design>

緯創資通

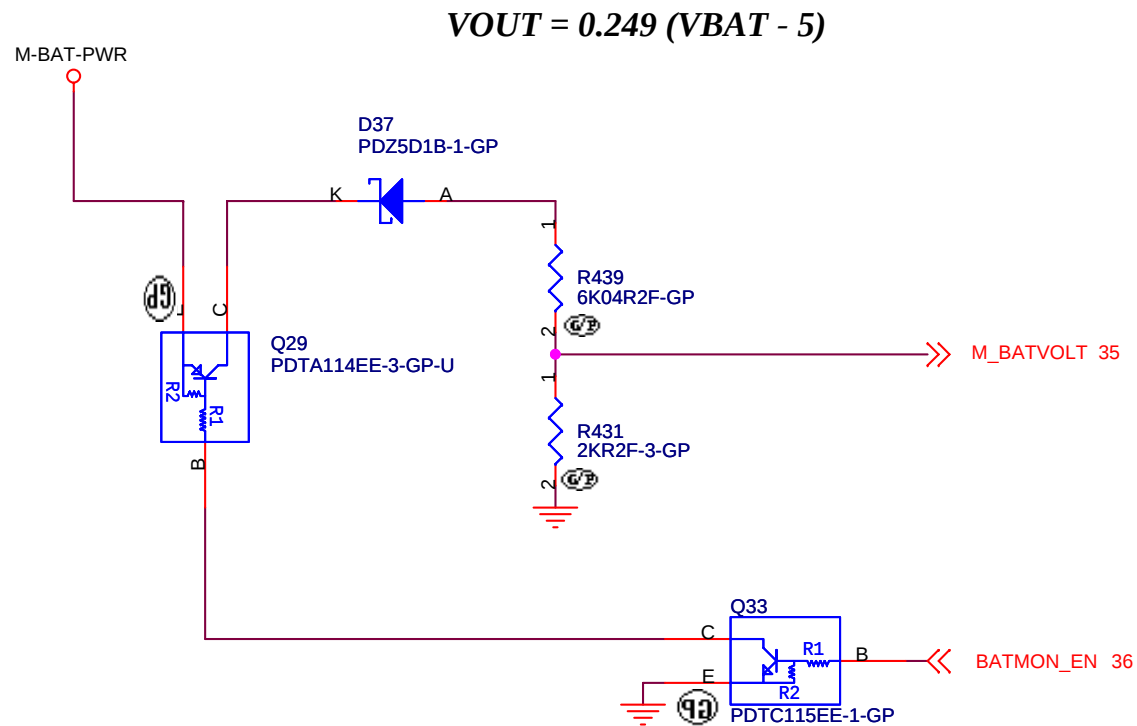
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			CHARGER SELECT	
Size	Document Number		Rev	
A3			Caramel-1	
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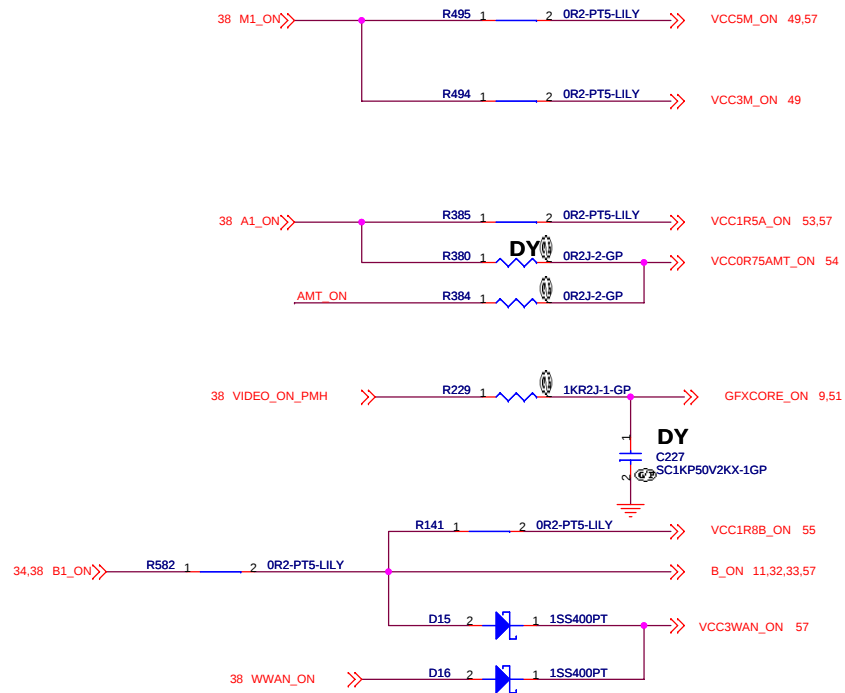
<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
BATTERY INPUT	
Size A	Document Number Caramel-1
	Rev 2
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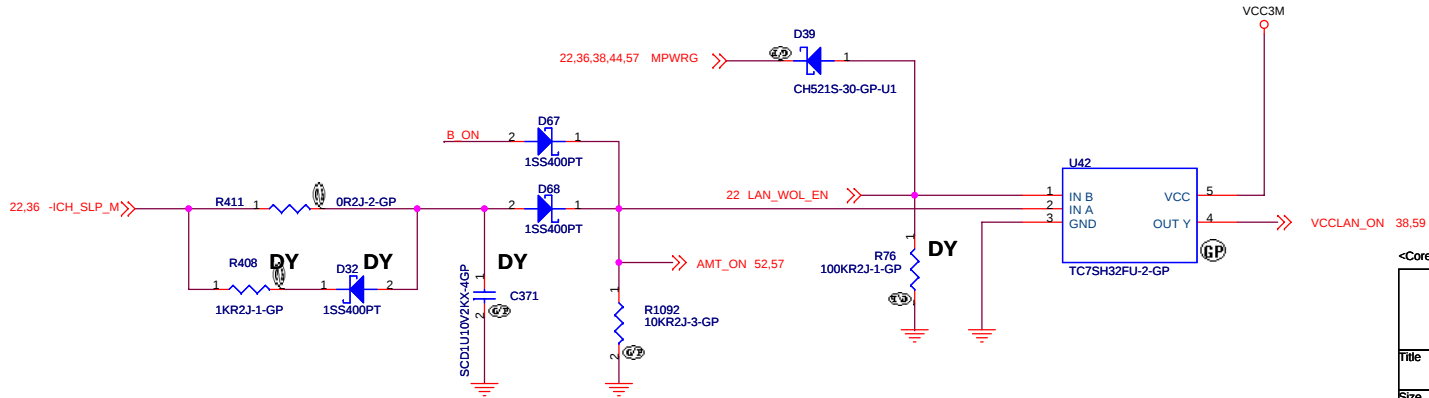
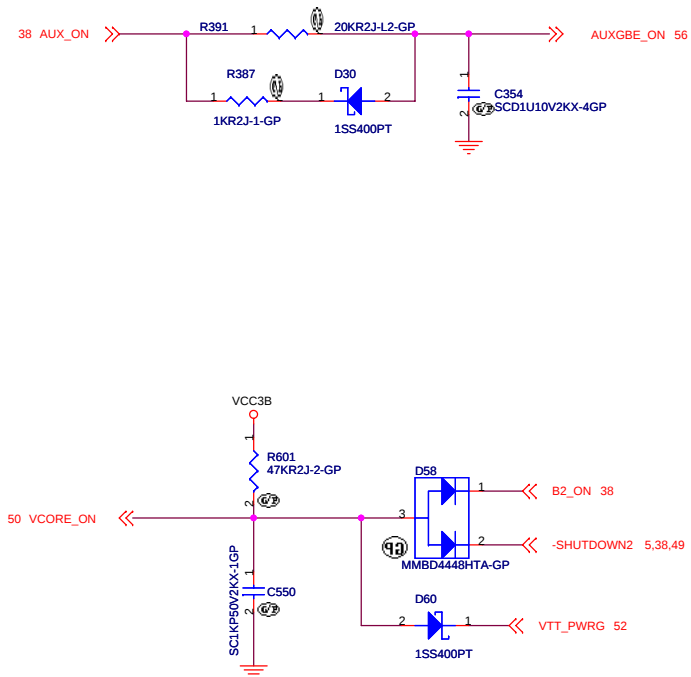


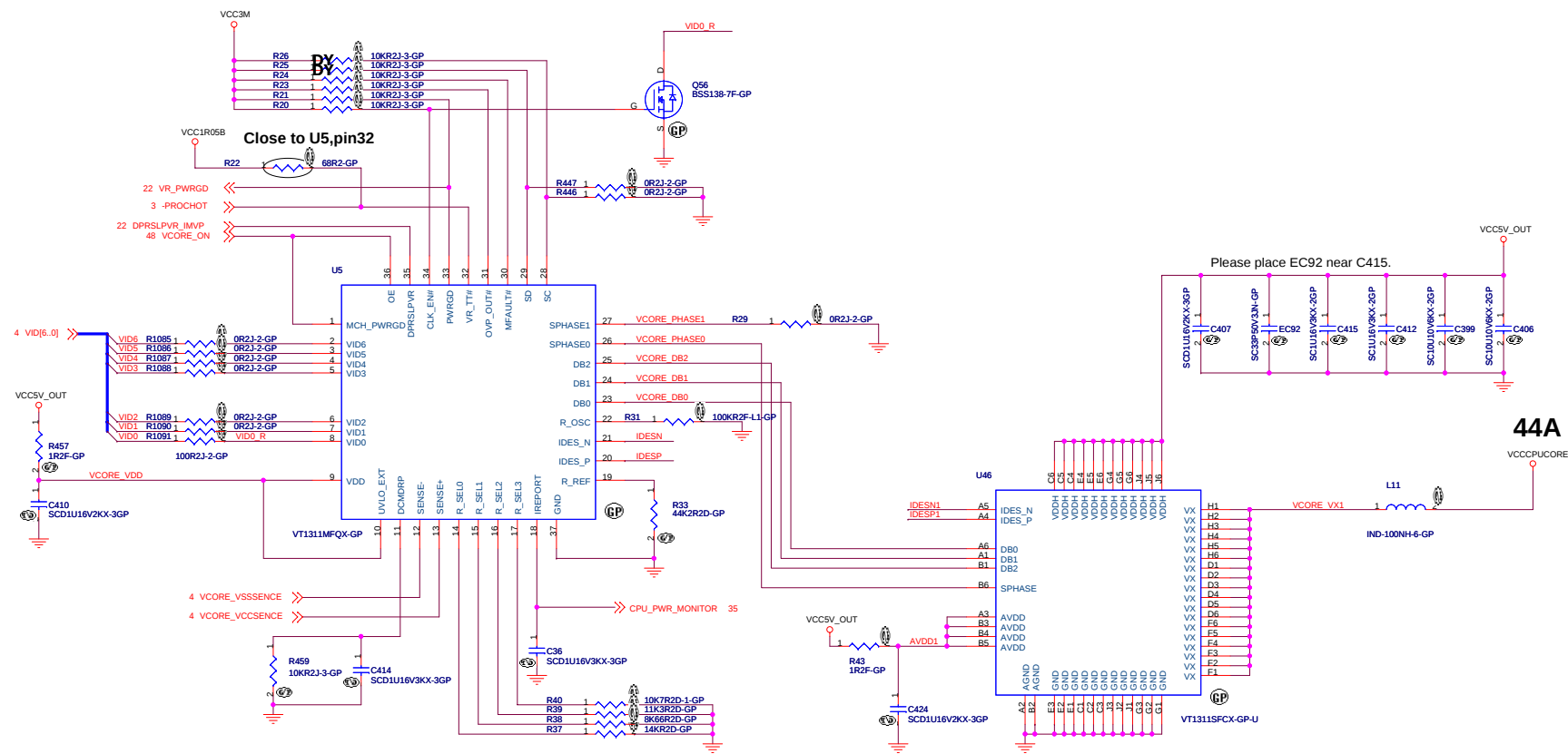
<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
BATTERY MONITOR			
Size A	Document Number		Rev
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Always ON	Enable	Disale
D15	ASM	No_ASM
D16	ASM	No_ASM





	LV dual core	ULV dual core	ULV single core
LoadLine	2.1m-Ohm	2.1m-Ohm	5.1m-Ohm
Fsw	1MHz	1MHz	1MHz
Imax	28A	24A	10A
DCM IpK	8A	8A	8A
RseL0	39.2K-ohm	14K-ohm	2.4K-ohm
RseL2	3.1K-ohm	3.1K-ohm	13K-ohm
Rldes	9.76+7.5K ohm	9.76+7.5K ohm	14+1.87K ohm
L11	100nH	150nH	200nH
Cout	40x 10uF	40x 10uF	40x 10uF

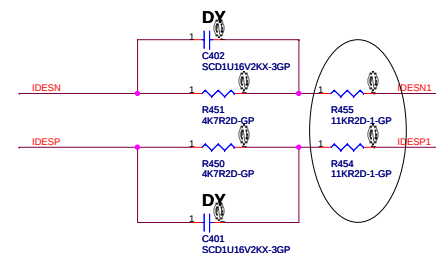
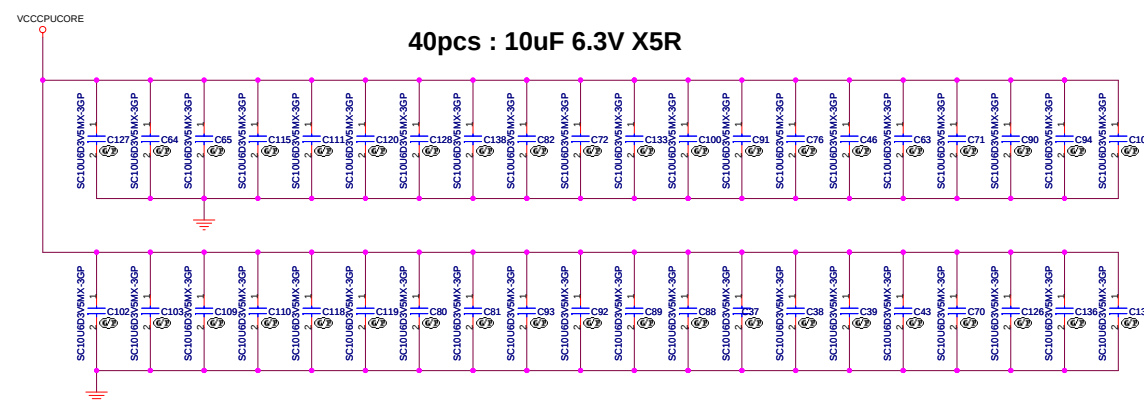
Note1: UVLO_ext not use.

Note2: L2 and Cout value.

Subject to optimize.

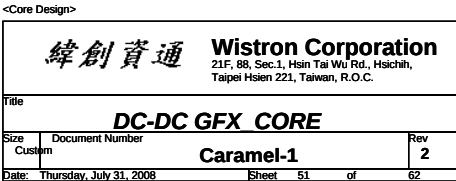
	Wistron P/N	Vendor P/N
LV Dual Core	68.1011N.10A	PA1682.101NL
ULV Dual Core	68.1511N.101	PA1682.151NL
ULV Single core	68.1911N.101	PA1682.191NL

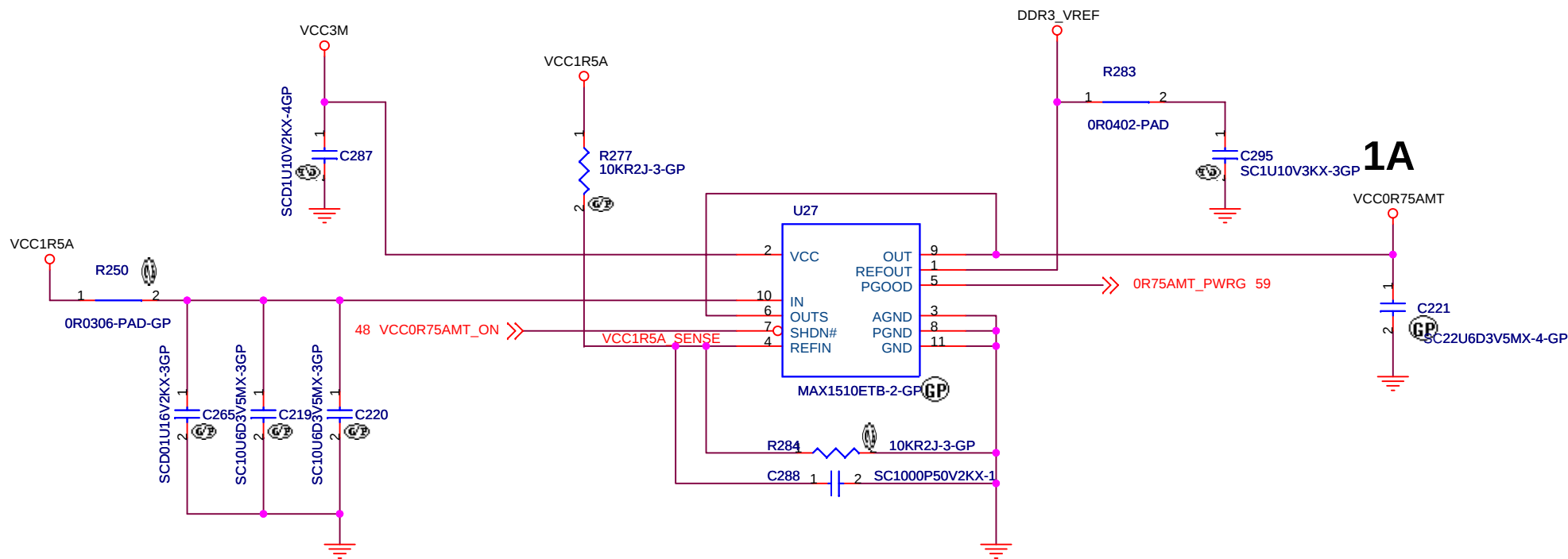
	Pre DV	DV
R455	12.1k ohm	11k ohm
R454	12.1k ohm	11k ohm



<Core Design>

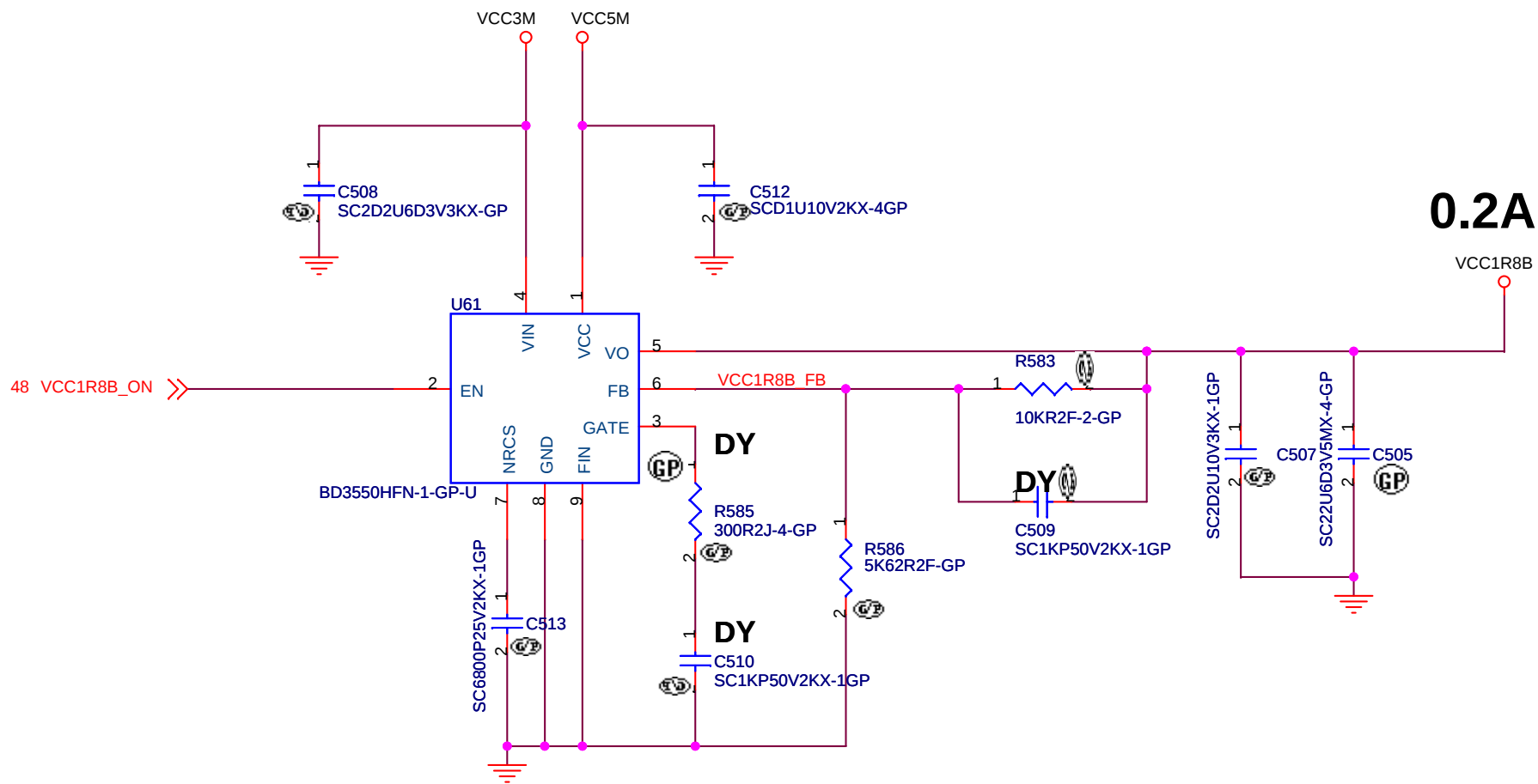
緯創資通 Wistron Corporation	
21F, 8th, Sec.1, Hsin Tai Wu Rd., Hsichin, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
DC-DC VCCCPUCORE LV/ULV	
Size	Document Number
Custom	Caramel-1
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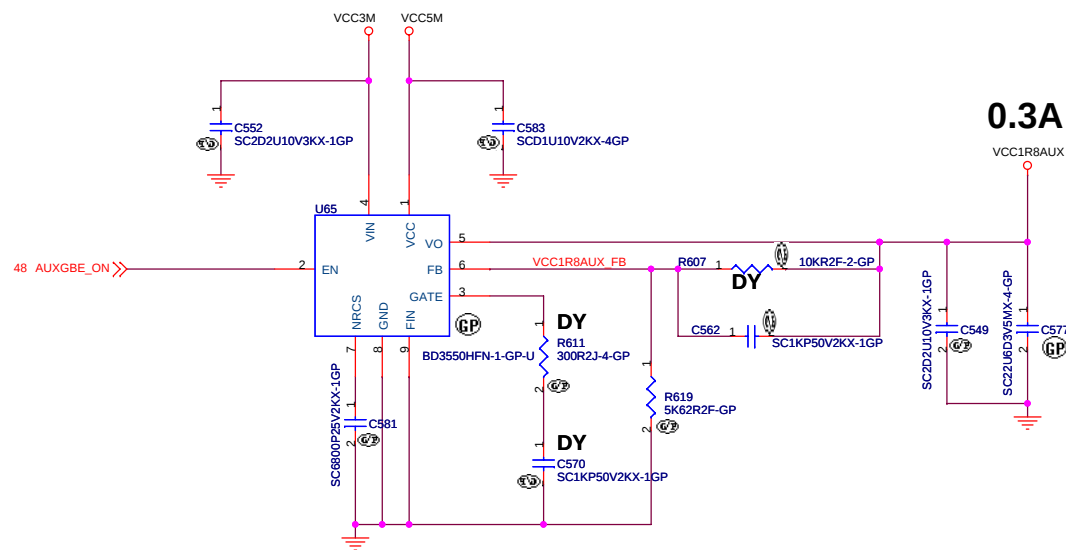
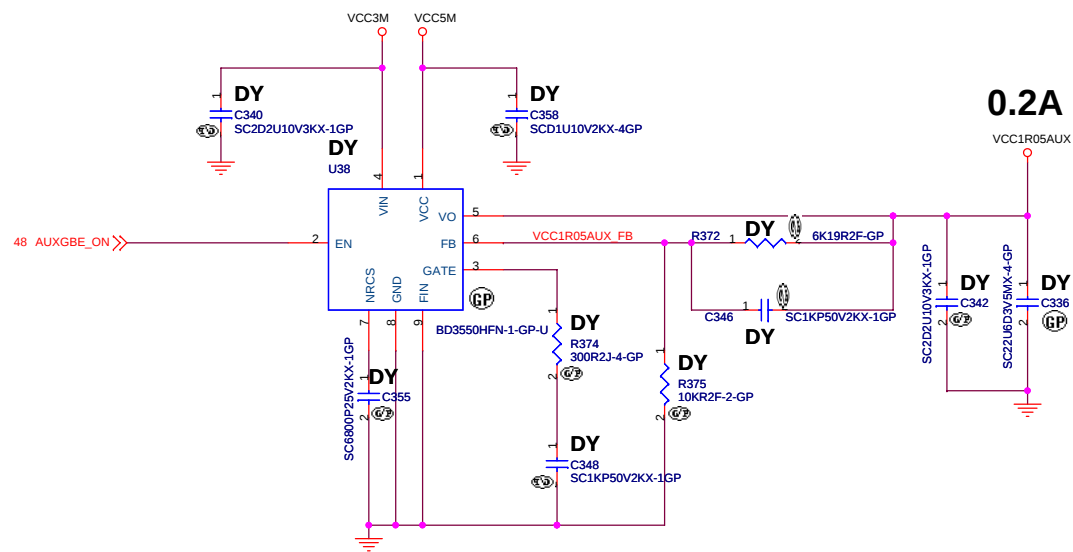
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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
DC-DC VCC0R75AMT	
Size A4	Document Number
	Caramel-1
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<Core Design>

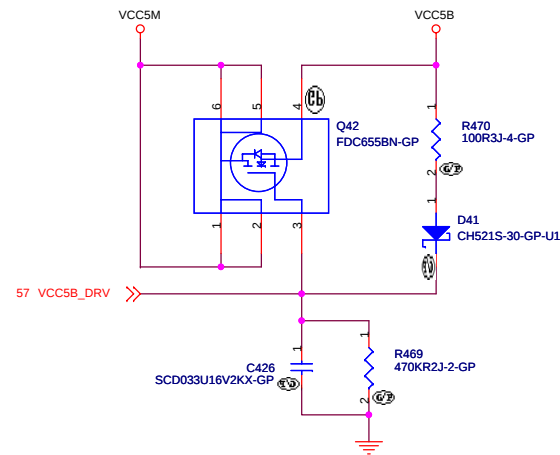
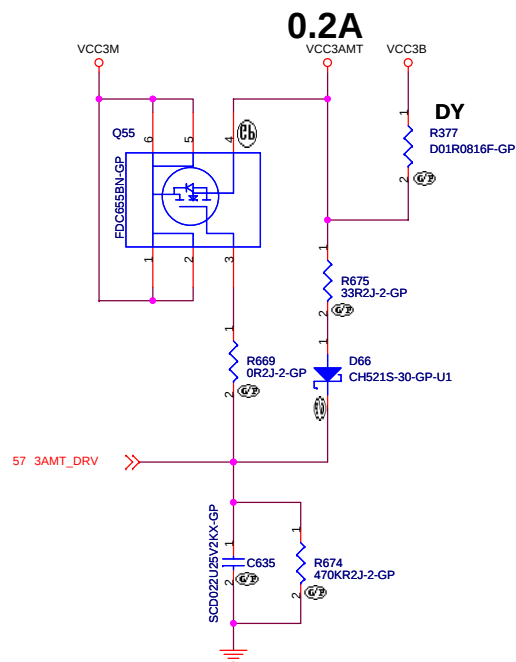
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title DC-DC VCC1R8B			
Size A	Document Number Caramel-1		Rev 2
Date:	Thursday, July 31, 2008	Sheet 55 of	62



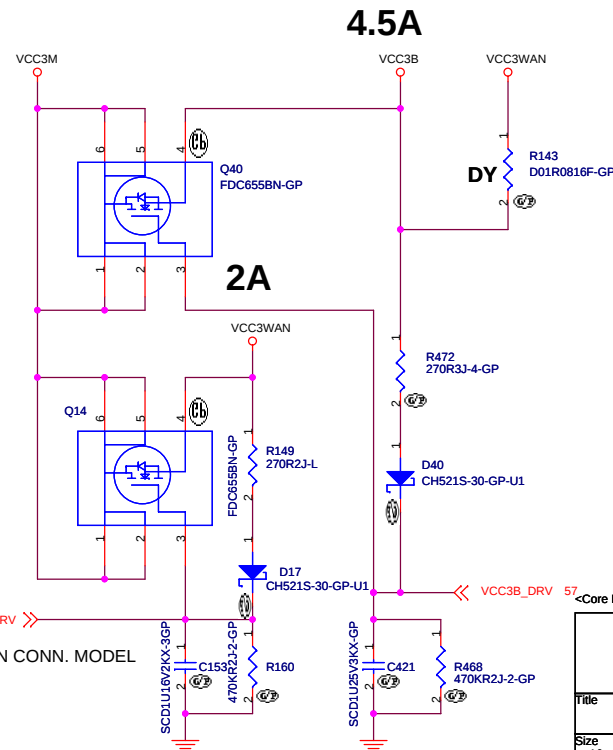
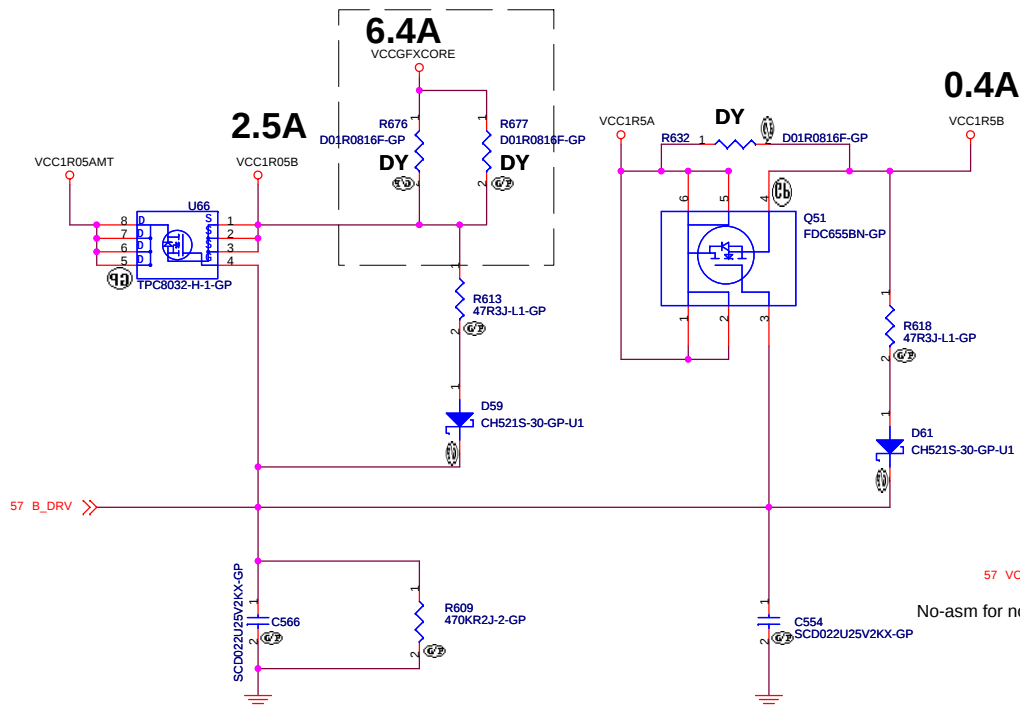
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
DC-DC VCC1R05AUX/VCC1R8AUX			
Size	Document Number		Rev
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Place near VCCGFXCORE logic



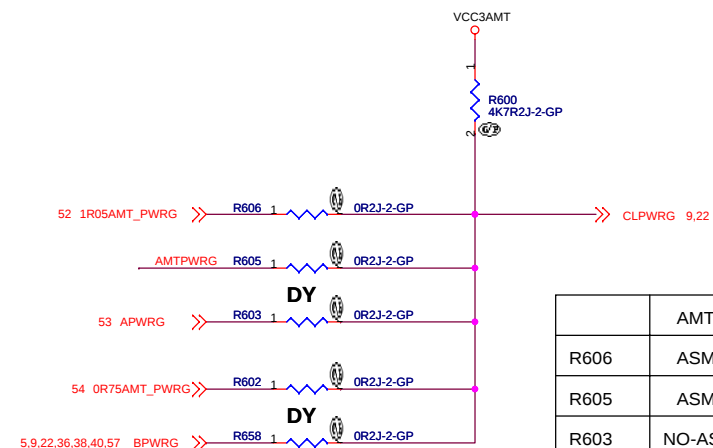
Always ON	Enable	Disable
Q14	ASM	No_ASM
R149	ASM	No_ASM
D17	ASM	No_ASM
R160	ASM	No_ASM
C153	ASM	No_ASM
R143	No_ASM	ASM

No-asm for no WWAN CONN. MODEL

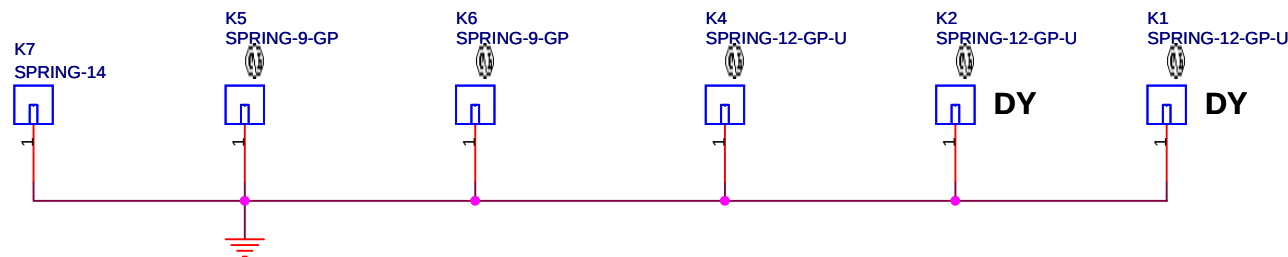
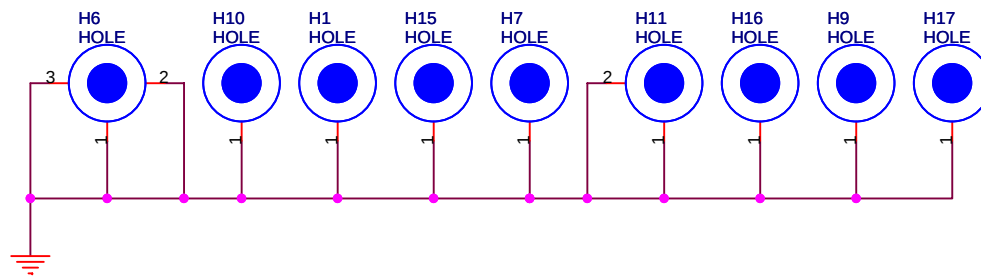
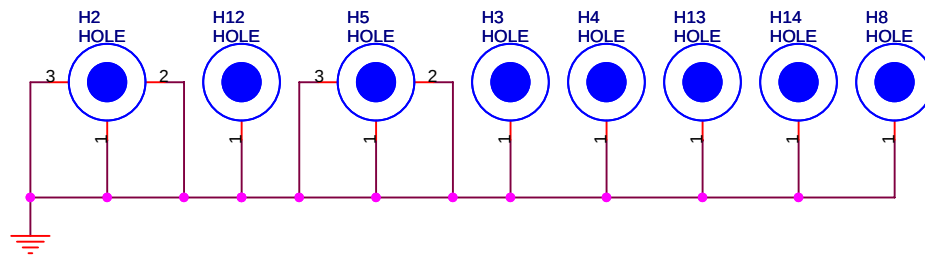
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緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

LOAD SW 1		
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	AMT	NON-AMT
R606	ASM	NO-ASM
R605	ASM	NO-ASM
R603	NO-ASM	NO-ASM
R602	ASM	NO-ASM
R600	ASM	NO-ASM
R658	NO-ASM	ASM



<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
HOLES/GND/PADS			
Size A4	Document Number Caramel-1		Rev 2
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Comparison chart for AMT function

Item	Page	Location Netname	AMT	NON_AMT	Modify	Note
1	22	U37	ICH9-M Enhanced	ICH9-M		
	22	R424	NO_ASM	ASM		
	24	U30	BOAZMAN 82567LM			
	29	SPI1	64Mbit			
	49	R411	ASM	NO_ASM		
	52	R636	ASM	NO_ASM		
	59	R603	ASM	NO_ASM		
		R605	ASM	NO_ASM		
		R606	ASM	NO_ASM		
	58	R377	NO_ASM	ASM		
		Q55	ASM	NO_ASM		
		R669	ASM	NO_ASM		
		R675	ASM	NO_ASM		
		D66	ASM	NO_ASM		
		C635	ASM	NO_ASM		
		R674	ASM	NO_ASM		
		U66	ASM	NO_ASM		
		R613	ASM	NO_ASM		
		D59	ASM	NO_ASM		
		C566	ASM	NO_ASM		
		R609	ASM	NO_ASM		

