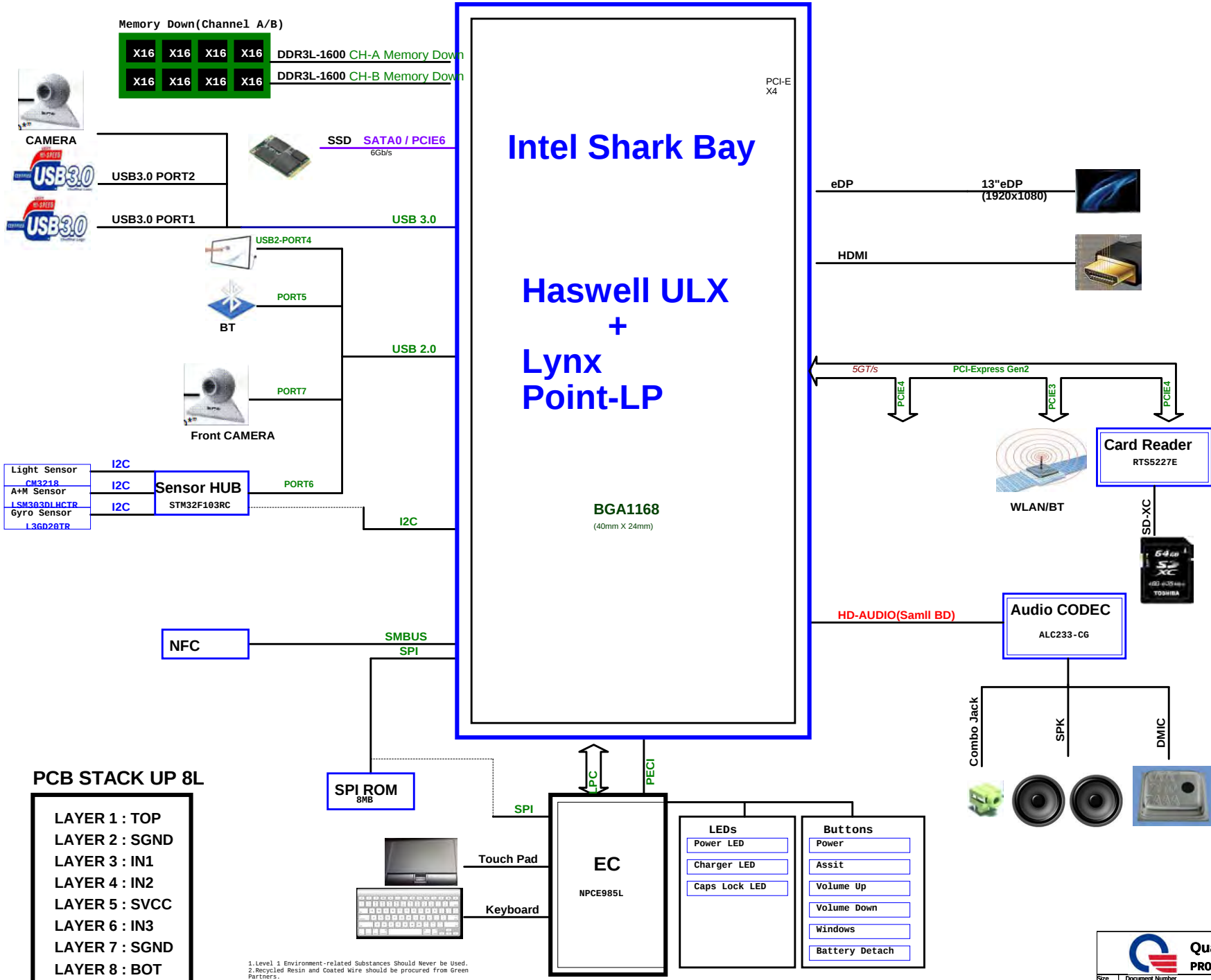




1.Level 1 Environment-related Substances Should never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Change List			03		
DVT-2013-05-05 P15 Add serial resistor RP11-RP16 on K/B matrix Reason :For ESD protection Possible Risk: no DVT-2013-05-05 P4 Change netname of GPIO 78 from EC_A20GATE to GPIO178 P16 Remove EC_A20GATE Reason : EC_A20GATE is not necessary DVT-2013-05-05 P9 PCH GPIO29 use for WLAN_PW_EN Reason :Change the power well of WLAN_PW_EN from sus to DSW. . DVT-2013-05-05 P20 Add R24460 without mount for TPS2543 . Reason :Reserve for TPS2543 . DVT-2013-05-05 P22 remove R6711,R673 for cost down . DVT-2013-05-05 P25 Change R2442 from unmount to mount Reason :Change PLTRST#_WLAN instead of PLTRST for RTD3 DVT-2013-05-05 P22 Add a AND gate U30 for WLAN power enable P16 add GPIO184 for EC_WLAN_PWR_EN DVT-2013-05-05 P22 Change WLAN_WAKE# PU power well from +3V_S5 to +3V_S5_DSW . Reason :The signal is belong to" DSW " power well DVT-2013-05-06 P15 unmount KB 220P (C184-C207) DVT-2013-05-08 P9 Add TP150 for ME DVT-2013-05-10 P23 Add U54 for SATA & PCIE Detect P23 Add +3.3V PU R24461 10Kohm DVT-2013-05-10 P12 Add C302 0.47uf capacitor for inrush current.(VCCDSW3_3) DVT-2013-05-10 P17 Add mode_lid-2# signal P16 Add KR28 PU and change EC GPIO32 from IMPV_PWRGD to MODE_LID-2# DVT-2013-05-10 P25 remove CN2 and add SW3 ,SW4 DVT-2013-05-10 P18 MR2,MR3 mount DVT-2013-05-11 P19 R200,R202 change from 2.2k to 4.7k for VEV5 7-1 DDC 130ohm test item DVT-2013-05-13 P16 Change EC GPIO55 from DRAM_VOLTAGE_SET to SIO_WAKE_GPIO127# P11 GPIO127 add SIO_WAKE_GPIO127# From EC DVT-2013-05-13 P8 Change Y1 package for Broadwell requirement DVT-2013-05-13 P8 Change Y1 package for Broadwell requirement DVT-2013-05-13 P16 Add KC16 150 pF for EC_PECI DVT-2013-05-14 P11 Add R24462 100K pull down for CR_PW_EN and unmount R297 DVT-2013-05-14 P36 Change U29 to AL000527000 and power source from +5V_WAKE to +3V_WAKE P36 Add R24470 ,R24469 to PD,R2449 mount, remove R2450 P36 Add R24471 to PD P36 Remove R2384 and mount R2451 P36 Add R24472 for SH_ALS_INT# PU DVT-2013-05-14 P21 mount L2007,L2008,L2009 DVT-2013-05-14 P18 Change MR12,MR13,MR14,MR15,MR16,MR17 from 43ohm to 20 ohm DVT-2013-05-15 P17 remove H19 DVT-2013-05-16 P17 Change U51 as AL000527000 P17 Change U51 as AL000527000 P17 remove R258,R259,R2436,R2435	DVT-2013-05-16 P11 Change R2433,R304,R310,R308,R297,R306 from mount to un-mount . DVT-2013-05-16 P4 Change the netname of GPIO177 from EC_EXT_SMI# to GPIO177 P8 Change the netname of GPIO35 from GPIO135 to EC_EXT_SMI# DVT-2013-05-16 P16 Add KD5 for EC_PWROK DVT-2013-05-16 P17 Change U52 from G5254T11U to G527ATP11U P17 mount R258 40.2k ohm and change R24456 as 100k ohm P17 Change U26 from G5254T11U to G527ATP11U P17 mount R2436 40.2k ohm and change R24454 as 100k ohm DVT-2013-05-19 P21 Change U53 from G5254T11U to G527ATP11U P21 Change R2431 to 40.2Kohm and Add R24473 as 100K ohm DVT-2013-05-19 P22 Change U27 to G547E1P81U P22 Delete R2454 DVT-2013-05-19 P4 Change R271,R272,R273,R274,R275,R276,R277 to 100Kohm DVT-2013-05-19 P7 Change R42 as 100Kohm DVT-2013-05-19 P8 R278,R279,R280,R281 change to 100Kohm, and remove R2048 DVT-2013-05-19 P9 Change R73, R282 from 10k ohm to 100k ohm DVT-2013-05-19 P10 Change R24474(no mount), R24475, R86 from 10k ohm to 100k ohm,and remove RP4 P11 Change R283, R285, R300, R315, R313, R317, R311, R296, R290, R291, R301, R303, R305, R307, R309, R312, R314, R316(no mount), R318, R319, R320, R2433(no mount), R298, R299, R304(no mount), R295, R310(no mount), R308(no mount), R284, R112, R24448, R115, R292, R287, R294, R116, R117, R118, R119, R286, R289, R297(no mount), R128(unmount),R114, R122, R306(no mount), R125, R126(unmount), R127, R129(no mount), R130, R131(no mount), R132, R133(no mount), R134, R135(unmount),R293 from 10k ohm to 100k ohm P11 Change KR2, KR4, KR19, KR26, KR27, KR28 from 10k ohm to 100k ohm P22 Change R24445,R2446 from 10k ohm to 100k ohm P4 Change R14,R15 from 10k ohm to 100k ohm DVT-2013-05-21 P38 U29 PIN1 change +3V_SENSORHUB for optional non CS design P38 R2451 change +3V_S5 for optional non CS design P36 change U29 ,C2337,R24469,R2449,R24470 to un-mount DVT-2013-05-21 P22 Add R24476 0 ohm P22 Mount R566 P22 Un-mount R2446,R24442,Q34,Q35,R2447,U28,R2442,R2443,U30,C2336 DVT-2013-05-21 P8 Change R62 as 330ohm,change CON1 P/N DVT-2013-05-22 P20 Add C2432,C2433 0.22uF CAP DVT-2013-05-22 P23 :TP function mount D8,D9,D10,D11,D12 for ESD DVT-2013-05-22 P25 :Change SW1 as DHP00XJ1301 DVT-2013-05-23 P23:change Q8 as BAW34040001 DVT-2013-05-28 P36:Mount R2359,R2361,R2364,R2365,R2367,R2369 DVT-2013-05-29 P24: Add R24477 for EMI DVT-2013-05-23 P11: Change U45 as AL000918000,U46,U47 as AL000916007 P21: Change R2449,R2447,R708,R709,R2352,R2449 DVT-2013-05-25 P11: Mount R310 DVT-2013-05-25 P17: Change COM1 FP(P/N: DFHS40FS104) DVT-2013-05-25 P23: Change SW2,SW3,SW4 FP, and reserve D45,D46,D47,D48,C2435,C2436,C2437 DVT-2013-05-25 P4:Add R24480 and Reserve R24478,R24479 DVT-2013-05-25 P6:R24481 change to 15kohm DVT-2013-05-25 P16:unmount MC21,MC24,MU2,MC23,MR28,MU3,MR22,MR23,ML4 P16:mount ML2,MR24 DVT-2013-05-27 P19: R180,R187,R189,R190,R192,R193,R195,R196 change to 470 ohm P19: R186,R187,R189,R190,R192,R193,R195,R196 change to 470 ohm DVT-2013-05-27 P19: R186,R187,R189,R190,R192,R193,R195,R196 change to 470 ohm DVT-2013-05-27 P12: Remove R256 P17: Change COM1 Q17,U28,R2442,R2443,U30,R24442,R2446,Q35,R2447,C2336,Q34,R24445,Q36 DVT-2013-05-27 P11:R315 unmount DVT-2013-05-28 P22:COM1 change to DFFC04FR066 DVT-2013-05-29 P33:add R24481 33ohm and reserve C2438 DVT-2013-05-29 P17:L30 mount DVT-2013-05-30 P23:Delete R2407,R2408 DVT-2013-05-30 P7:mount C49 680P DVT-2013-05-30 P19:unmount U53,R24473,C2415,R2431,R219 and add R24484 and R24482 DVT-2013-05-31 P19:unmount R185,R188,R189,R191,R194 DVT-2013-05-31 P17:Add U56,R24485 DVT-2013-06-01 P23: remove R342 DVT-2013-06-01 P22: Add C303,R24486 DVT-2013-06-03 P24: Change P10,P12 footprint DVT-2013-06-03 P20: Add R24488,R24489,R24490,R24491,R24492,R24493,R24494,R24495 DVT-2013-06-04 P16: Add KR29	DVT-2013-06-04 P30: Add U105,RC2,TR3,TR4,TQ2 DVT-2013-06-04 P25: Add R24496, R24497 DVT-2013-06-07 P8: Unmount R59,R60,R61,R64,R65,R66 DVT-2013-06-12 P6: Unmount KC16 DVT-2013-06-13 P19: mount R185,R188,R191,R194 DVT-2013-06-19 P25: Change Q25,Q26 PN BA001240211 DVT-2013-05-16 (Power Portion) P27 POWER 3VPCU&RVCCS(TPS51427) Removed P61,P62,+5V_WAKE-1 and +3V_WAKE-1 Reason : DVT is not necessary. P28 POWER 1.35VSUS/VTT_MEM Change PCB footprint of PU4 from 21P to 26P Reason : 21P is incorrect compoment. Removed PC75 Reason : PC75 will lead to delay DDR_PG signal. Possible : DDR_PG signal will be unstable. Removed P63 and +1.35V_SUS-1 Reason : DVT is not necessary. P29 POWER +1.05V/+1.5V/Thermal Change PC81 from CH6104K9A00 to CH6104M9901. Reason : To decrease the temperature of PQ34 and PQ35. Removed P64 and +1.05V_M-1 Reason : DVT is not necessary. P30 POWER(BAT IN / ADAIN /UL) Change "One-Shot 10ms POCHOT# For ADP/BAT" circuit to "One-Shot 10ms POCHOT# For ADP". Reason : Order to fine-tune for new 45W adapter. Change System DVP for VEV5 from "+5V_WAKE-1 ,+3V_WAKE-1, +1.35V_SUS-1 and +1.05V_M-1" To "+5V_WAKE ,+3V_WAKE ,+1.35V_SUS and +1.05V" Reason : +5V_WAKE-1 ,+3V_WAKE-1, +1.35V_SUS-1 and +1.05V_M-1 are removed P31 POWER CHARGER (ISL88732) Change PR215 from 95.3K to 140K. Change PR217 from 34.8K to 31.8K. Change charge current from 2.81A to 2.688A Reason : Modify Max Charge Current Limit from 4.5A to 3.2A. DVT-2013-05-23 (Power Portion) To change PSW P/N from DHP0T342600 to DHP00XJ1301. Reason : To change the switch component. To change P10 P/N from AL088732000 to AL088732001. Reason : IC version update. To add PR196 and to change PC141 P/N from CH21006K816 to CH5102K9B06. Reason : To reserve a RC delay time filter in ISENSE_IN route. DVT-2013-05-20 (Power Portion) To change PR23 value from 24kohm to 24.9K. Reason : To modify the Vhcore Loadline. To use PC10 1200PF and to close PC9 1000PF. Reason : To modify the Vhcore load transient. To use PC21-PC23. Reason : To modify the Vhcore load transient overshoot. To correct PU107A into PU104A. Reason : PU104A and PU104B is a group IC. DVT-2013-06-05 (Power Portion) Change PR23 from 24.9K to 23.7K Change PC170 from 470PF to 220PF Reason : modify Vhcore's Dynamic Iout accury. DVT-2013-06-06 (Power Portion) Update F11 Power portion DVT 0606 schematics To add PR145 430K and PR141 in parallel. Change PR141 from 470K NTC to 680K NTC Change PR146 from 6.98K to 0 ohm. Reason : The minimum temp of PQ36 trigger point will be increased from 86C to 98C DVT-2013-06-07 (Power Portion) F11 power portion 0607 update(for DVT building 80W) Change PR141,PR146 and PR145 from DVT value to EVT value. Reason : PR141 680K lack parts. PVT-2013-07-18 (Power Portion) P26,P27,P29 Change to Short pad PR7,PR15,PR25,PR27,PR838,PR54,PR45,PR53,PR55,PR37,PR231,PR96,PR116,PR193 Reason : PVT stage do not need to reserve 0 ohm. P27 POWER 3VPCU&RVCCS(TPS51427) Change PC44,PC46 from CH7221MT6001 to CH7221MT603 Reason : NEO assign cap P/N. Add +1.35V_SUS discharge circuit (PR64,PR65,PR67,PR69,PQ16,PQ17) Reason : +1.35V_SUS discharge circuit P29 POWER +1.05V/+1.5V/Thermal Add PC 151 parallel with the PR220 P30 POWER(BAT IN / ADAIN /UL) Change P65 PCB foot print from short pad jumper to RC 0603 P31 POWER CHARGER (ISL88732) Add PC54 parallel with the P161 Reason : Avoid BAT over voltage. PVT-2013-07-18 P8: reserve Q46 , R24498 for no power issue P16:EC GPIO085 add EC_RTC_RST for workaround solution P12:R138,R139 change to short pad. P13:DR1,DR2 change to short pad. P14:DR50,DR53 change to short pad. P16 KL1,KR25,KR18 change to short pad. P17 add U58,R24499,R24501,R24500,C2444, remove F6 P22 R519 change to short pad. PVT-2013-07-19 P17 change F2 footprint PVT-2013-07-19 P22 Add R24507,R24508 and unmount R24486 PVT-2013-07-19 (Power Portion) modified 1.35V_SUS discharge circuit Reason: change PQ17 from dual package to single package. PVT-2013-07-30 P36 Change U20 AR000000000(FW26) PVT-2013-07-31 P17 R24512 change to 0 ohm PVT-2013-08-01 P17 change C2331,C2332,C2333 to 2.2u PVT-2013-08-01 P19 change R208 to 57.6k,R212 to 23.7k P19 change R206 to 100k,R211 to 42.2k	MP-2013-08-09 P21: Change C932 as 1u P21: add D49,R24517 , C2446 MP-2013-08-10 P10: Add R24518,R24519,R24520,R24521,R24522 P23: Change C330,C317,C319 and reserve C2447 MP-2013-08-11 P19: remove R185,R188,R188,R191,R194 as 330 ohm, add C2448,C2449,C2450,C2451 MP-2013-08-16 P21: R084, R2146 change to 0 ohm P17: Change F2 PVT-2013-07-21 P10 change R95,R96,R97,R98 to shortpad P17 change R329 to shortpad P36 change R2374 to shortpad P21 delete L2008,L2009 PVT-2013-07-22 (Power Portion) P31 POWER CHARGER Added EC176 0.1u/25V/Y5V_4 Reason: EMI suggestion P30 POWER(BATIN/ADAIN/UL) Added EC11 0.1u/50V/K7R_6 Reason: EMI suggestion PVT-2013-07-22 P4 change R24480 to shortpad P17 change R2437 to shortpad P17 unmount R24455,R24454 ,C294,C295 P21 change R219 to shortpad P11 mount R308,R306 P25 Change R226 as 120 ohm,R264 as 300 ohm; P25 as 330ohm,F19 unmount P25 change R2450,R24510 and Q47 PVT-2013-07-23 P17 reserve D24 PVT-2013-07-27 P17 change the R185,R188,R191,R194 as 330 ohm PVT-2013-07-29 P17 unmount R24455,R24454 ,C294,C295,R2438,R2448, mount R24512,R24511	Quanta Computer Inc. PROJECT : F11 Change List Size Document Number Date: Tuesday, August 20, 2013 Rev 3 of 40	

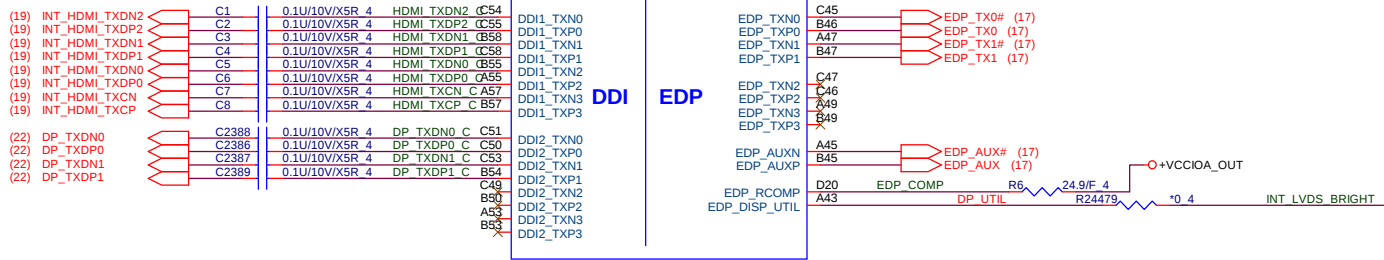
Haswell ULT (DISPLAY)

4

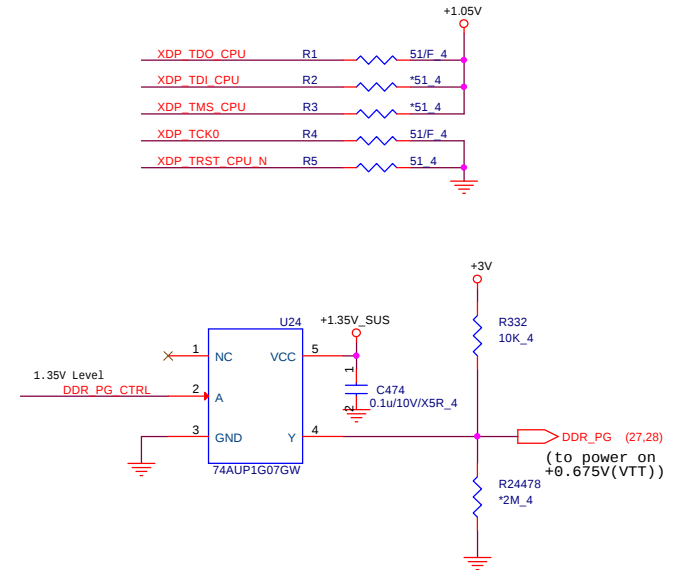
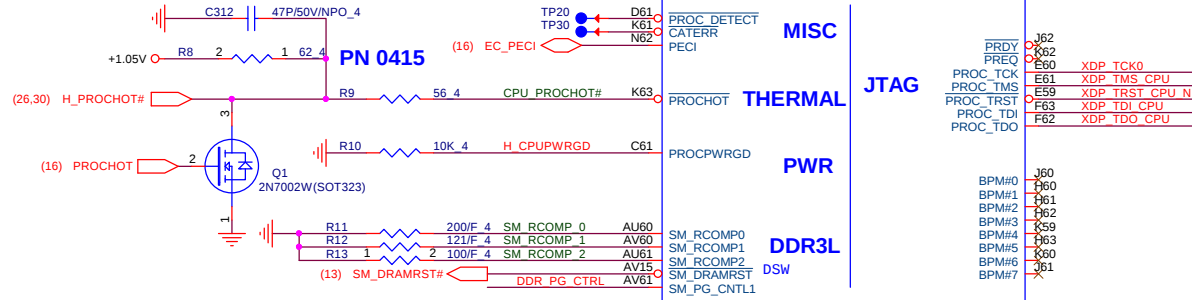
INT. HDMI

WiGig

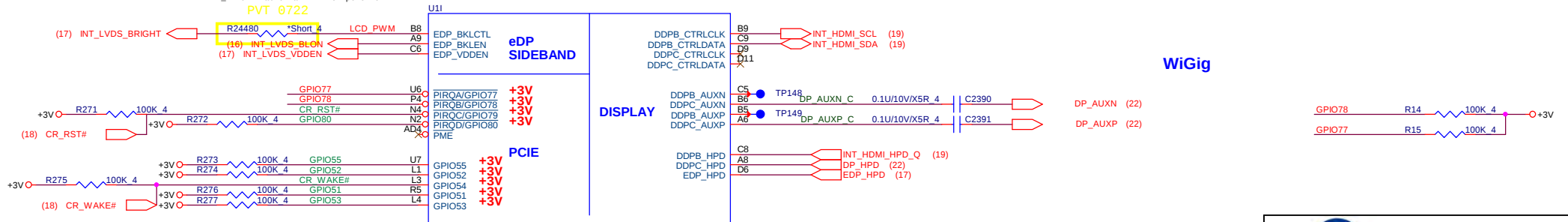
U1A



U1B



EDP_BKLCTL: abnormal 2V when power on.
PVT 0722



WiGig

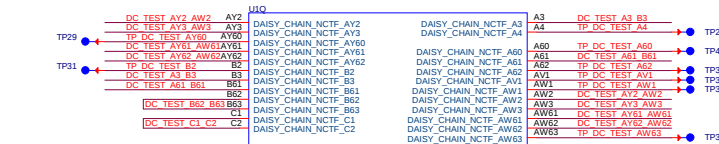


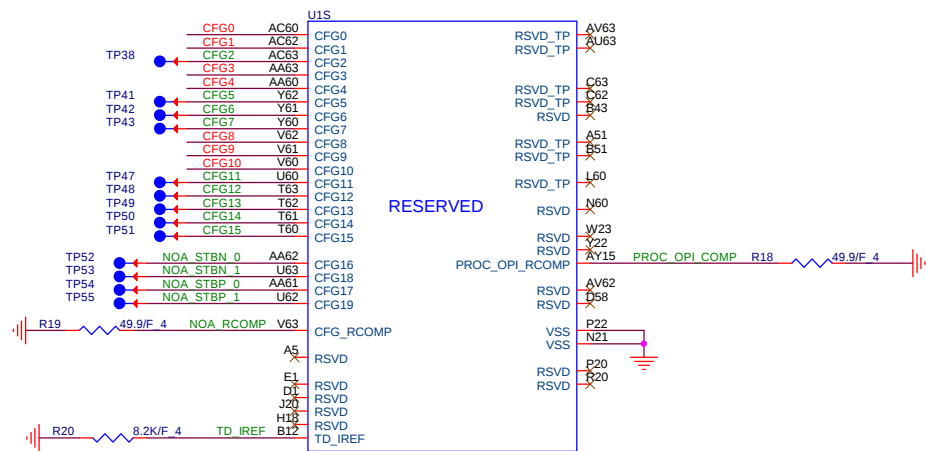
Quanta Computer Inc.
PROJECT : FI1

Size	Document Number	Rev
1A	HSW MCP(Display/eDP)	1A
Date:	Monday, August 19, 2013	Sheet 4 of 40

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

5

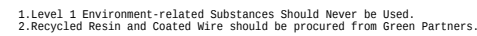
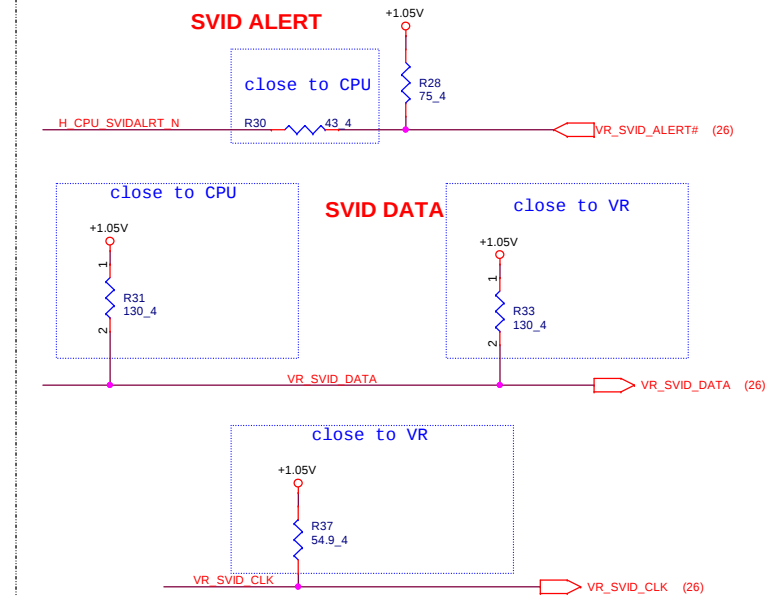




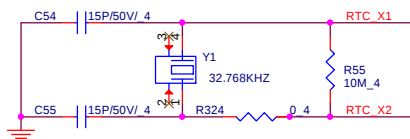
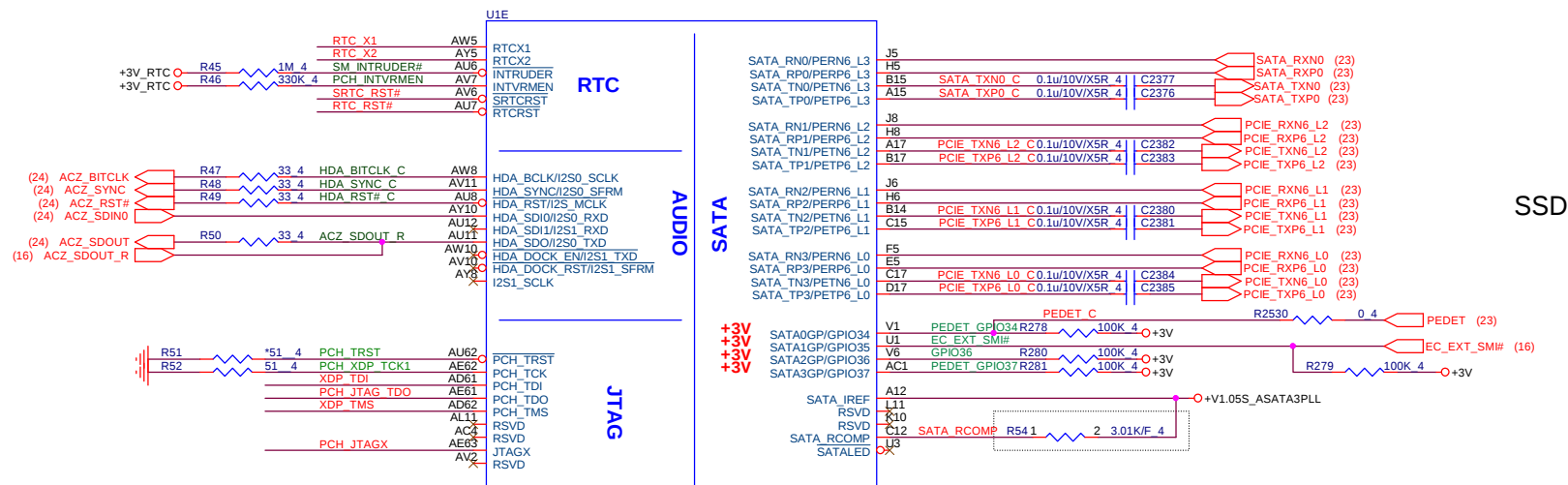
Processor Strapping

	1	0	
CFG0 EAR-STALL/NOT STALL RESET SEQUENCE AFTER PCU PLL IS LOCKED	(DEFAULT) NORMAL OPERATION; NO STALL	STALL	
CFG1 PCH/ PCH LESS MODE SELECTION	(DEFAULT) NORMAL OPERATION	PCH-LESS MODE	
CFG3 PHYSICAL_DEBUG_ENABLED (DFX PRIVACY)	DISABLED	ENABLED SET DFX ENABLED BIT IN DEBUG INTERFACE MSR	
CFG4 DISPLAY PORT PRESENCE STRAP	DISABLED NO PHYSICAL DISPLAY PORT ATTACHED TO EMBEDDED DISPLAY PORT	ENABLED; NOA WILL BE AVAILABLE REGARDLESS OF THE LOCKING OF THE UNIT	
CFG 8 ALLOW THE USE OF NOA ON LOCKED UNITS	DISABLED(DEFAULT); IN THIS CASE, NOA WILL BE DISABLED IN LOCKED UNITS AND ENABLED IN UN-LOCKED UNITS	ENABLED AN EXTERNAL DISPLAY PORT DEVICE IS CONNECTED TO THE EMBEDDED DISPLAY PORT	
CFG9 NO SVID PROTOCOL CAPABLE VR CONNECTED	VRS SUPPORTING SVID PROTOCOL ARE PRESENT	NO VR SUPPORTING SVID IS PRESENT. THE CHIP WILL NOT GENERATE (OR RESPOND TO) SVID ACTIVITY	
CFG10 SAFE MODE BOOT	POWER FEATURES ACTIVATED DURING RESET	POWER FEATURES (ESPECIALLY CLOCK GATINE ARE NOT ACTIVATED	

7

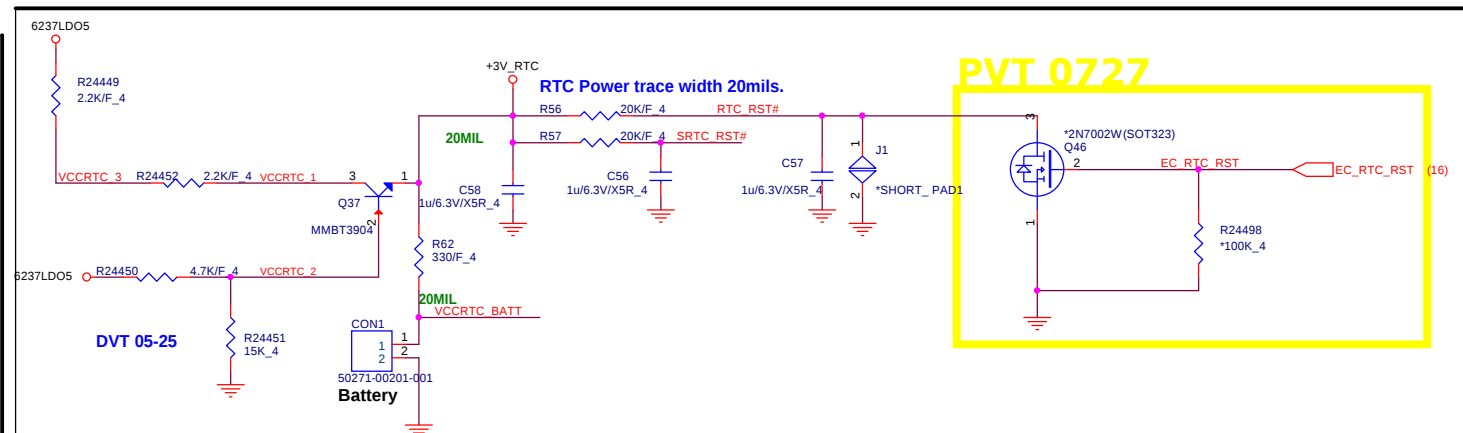
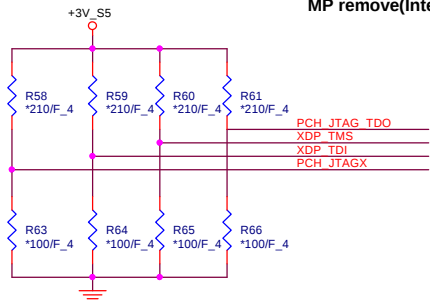


Haswell ULT (RTC, HDA, JTAG, SATA)



PCH JTAG Debug (CLG)

MP remove(Intel)

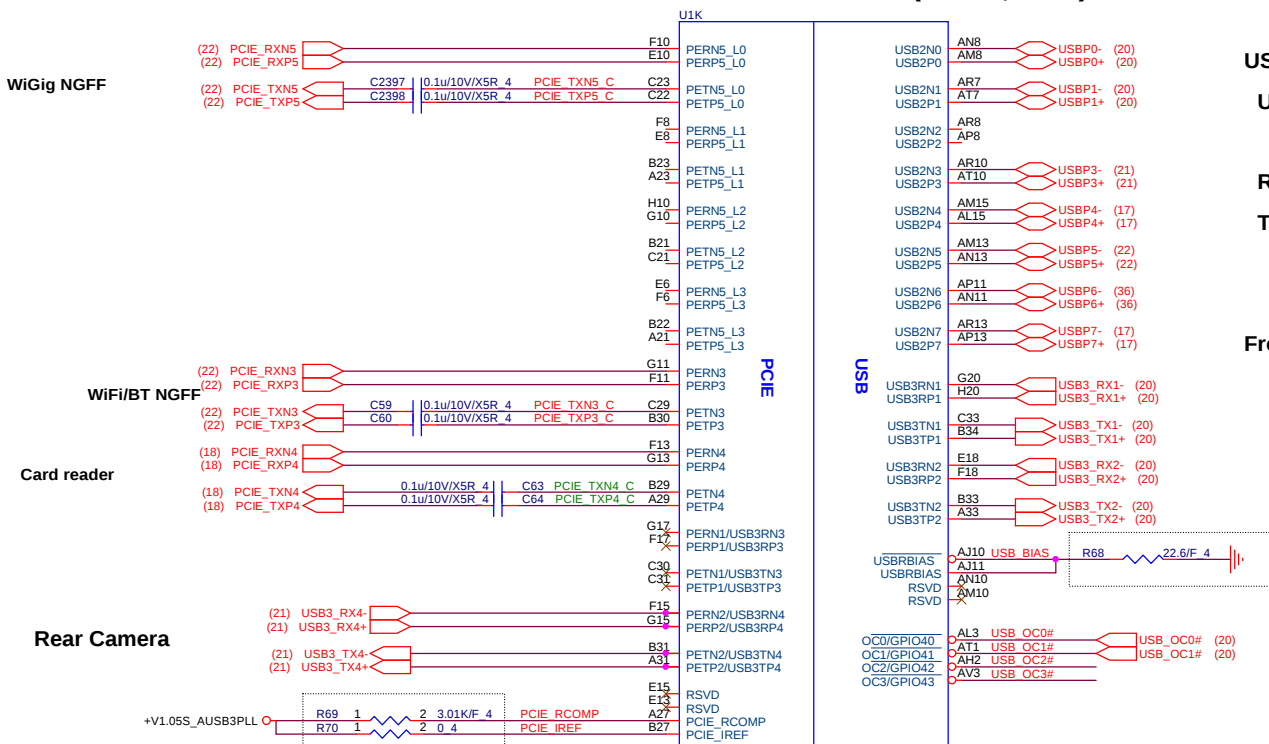


PCH Strap Table

Pin Name	Strap description	Sampled	Configuration	note
SPKR	No reboot mode setting	PWROK	0 = Default (weak pull-down 20K) 1 = Setting to No-Reboot mode	
HDA_SDO	Flash Descriptor Security Override / Intel ME Debug Mode	PWROK	0 = Security Effect (Int PD) 1 = Can be Override	
INTVRMEN	Integrated 1.05V VRM enable	ALWAYS	Should be always pull-up	



Haswell ULT (PCIE, USB)



USB3.0 Port 1 with Charge

USB3.0 Port 2

Rear CAMERA

Touch Screen (Full Speed)

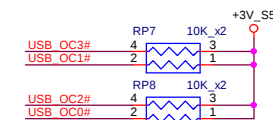
BT (Combo)

Sensor Hub

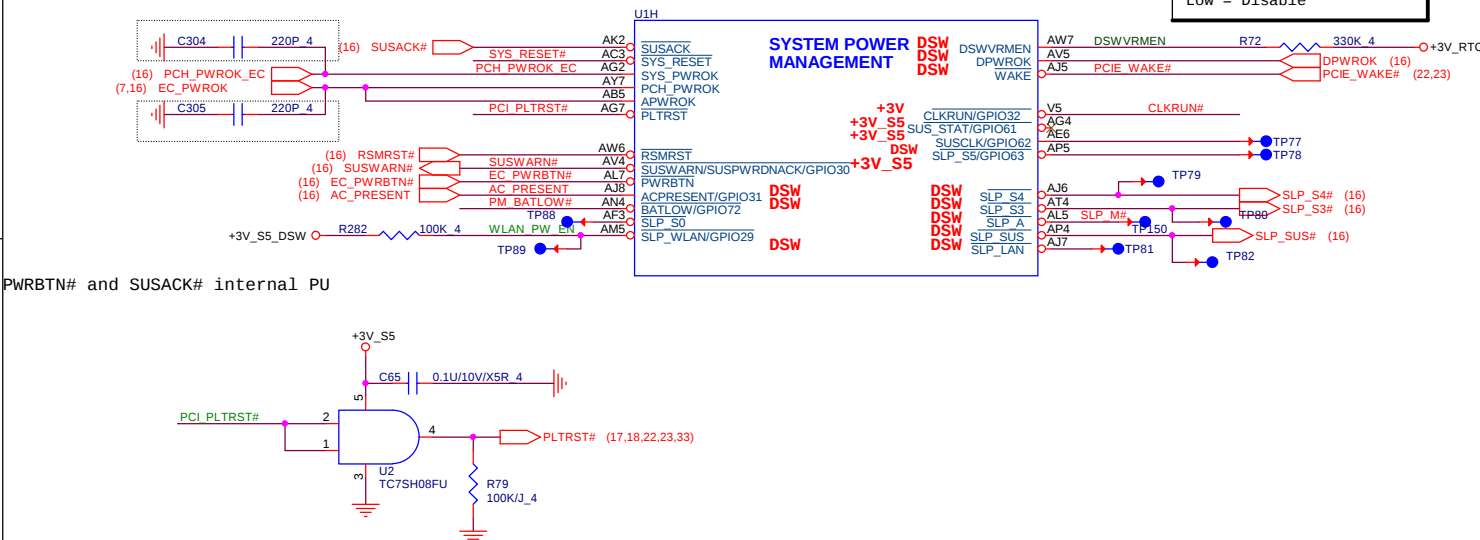
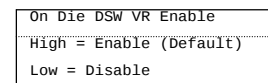
Front CAMERA

USB3.0 Port 1 W/Charge

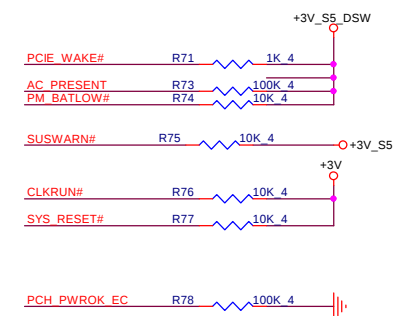
USB3.0 Port 2



Haswell ULT (SYSTEM POWER MANAGEMENT)



PCH Pull-high/low(CLG)



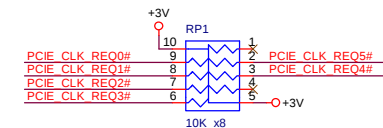
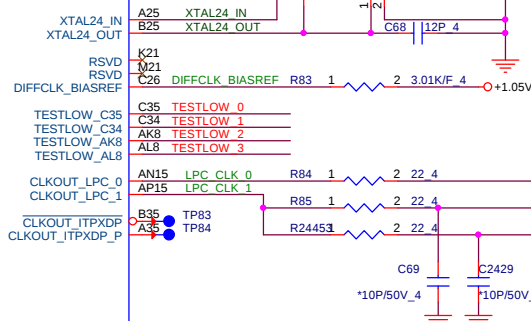
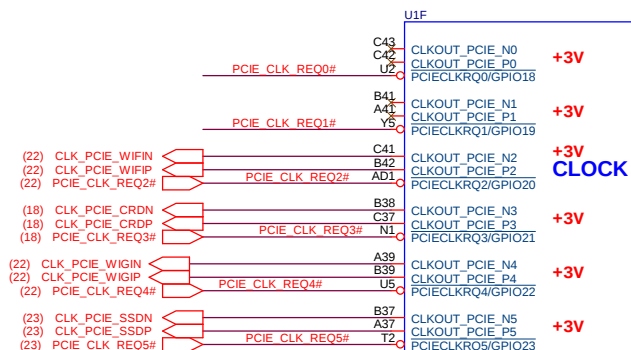
Haswell ULT (CLK)

WiFi/BT(NGFF)

Card Reader

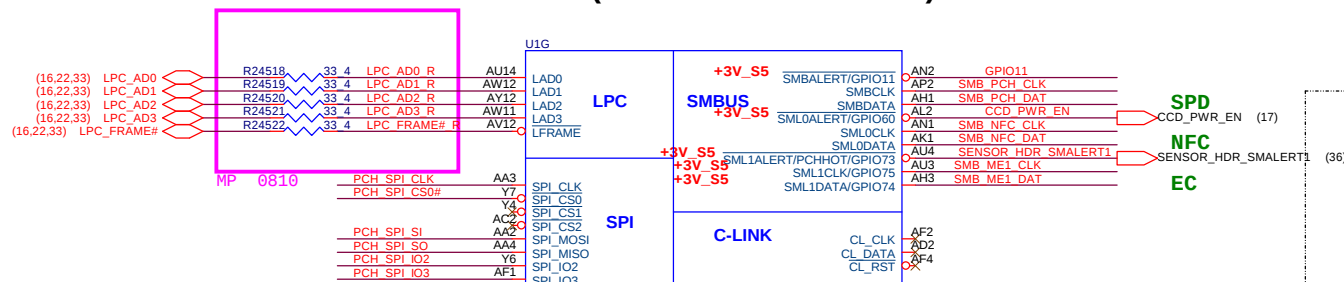
Wigig(NGFF)

SSD

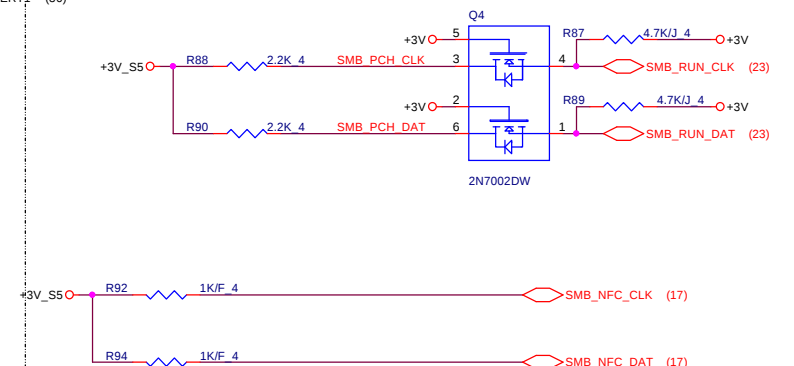


Do not short the testlow pins together.

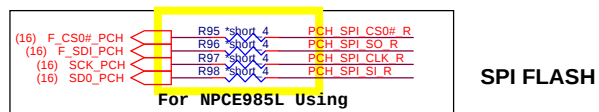
Haswell ULT (LPC/SPI/SMB/CLINK)



SMBus/Pull-up(CLG)

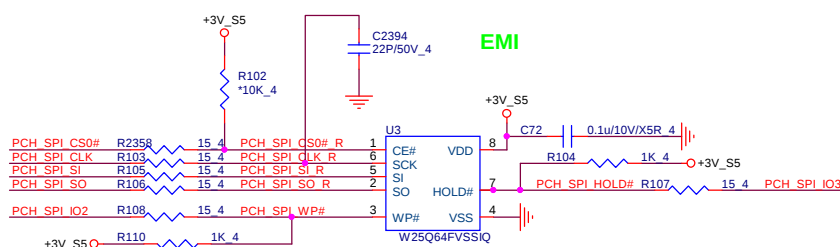


PVT 0721



SPI FLASH

For NPCE985L Using



Quanta Computer Inc.
PROJECT : FI1

GPIO27

With Intel LAN:
Connect to LANWAKE# pin on the LAN
Without Intel LAN:
Used to wake event from DSW

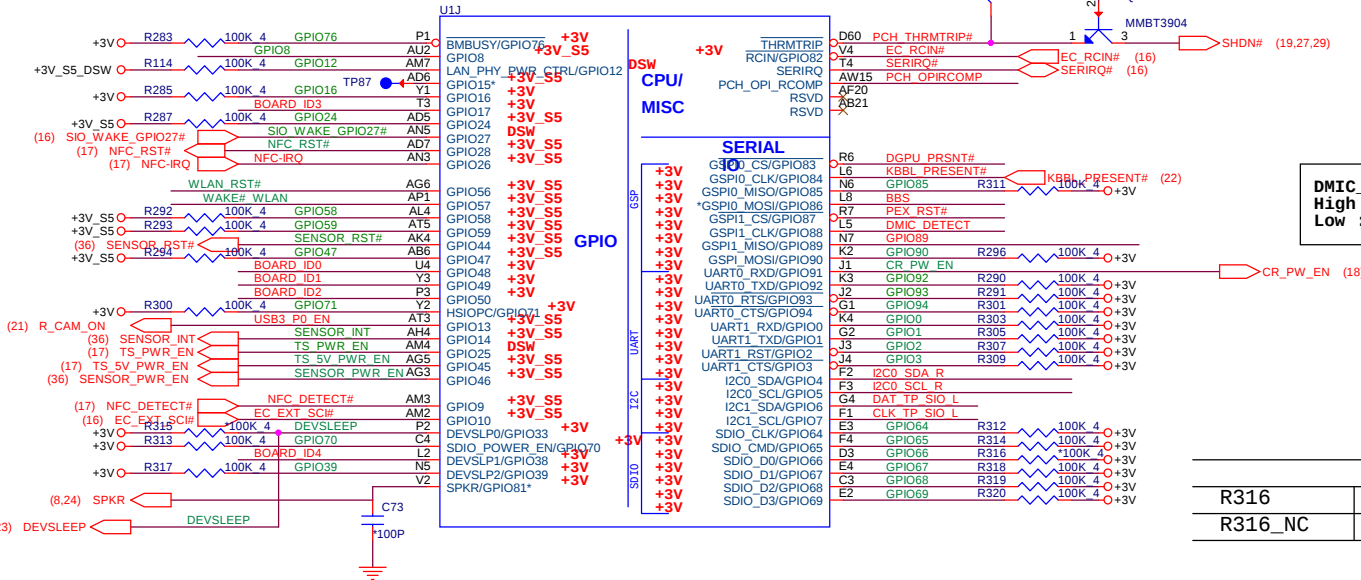
Hasswell ULT(GPIO, LPIO, MISC)

+V1.05S_VCCST

GPIO Pull-up/Pull-down(CLG)

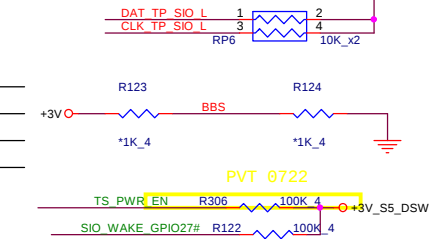
PVT 0722

11

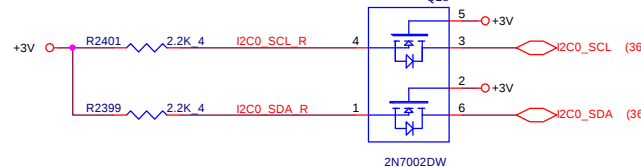


DMIC_DETECT:
High : Single DMIC
Low : Dual DMIC

GPIO66	
R316	ENABLE
R316_NC	DISABLE(Default)



Vendor	Vendor Part Number	BOARD_ID2	BOARD_ID3	BOARD_ID4
Elpida(4G)	EDJ4216EFBG-GN-F	0	0	0
Samsung(4G)	K4B4G1646B-HYK0	0	0	1
Hynix(4G)	H5TC4G63AFR-PBA	0	1	0
Elpida(8G)	EDJ8416E6MB-GN-F	0	1	1
Samsung(8G)	K4B8G1646B-MYK0	1	0	0
Hynix(8G)	H5TC8G63AMR-PBA	1	0	1
		1	1	0
		1	1	1



R127(Low)
R128(High)

R125(Low)
R126(High)

	Board ID1	Board ID0
Mule FI1	0	0
HuronSHA1 FI2	0	1
HuronSHB1 FI3_UMA	1	0
HuronSHB1 FI3_DGPU	1	1

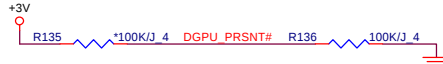
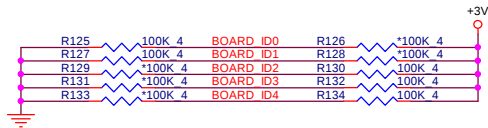
GPIO86	
PU	LPC
PD	SPI (Default IPD)

No Reboot Strap(GPIO81)

NC	Default
PU	EN

TLS CONFIDENTIALITY STRAP(GPIO15)

NC	Default
PU	EN



PCBA SKU	Discrete	UMA
R135(Pull High)	Stuff	No Stuff
R136(Pull Low)	No Stuff	Stuff



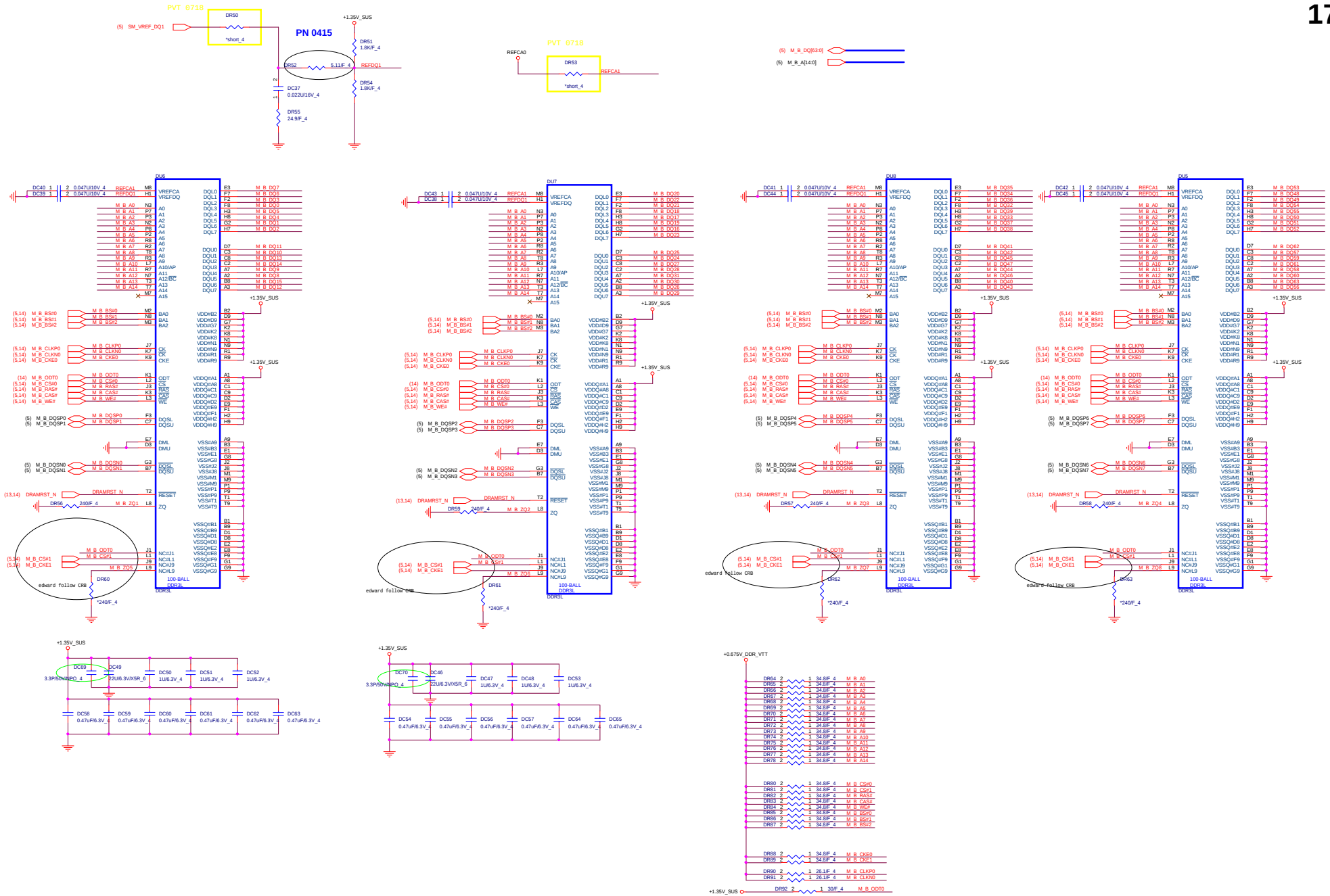
Quanta Computer Inc.
PROJECT : FI1

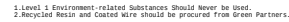
Size	Document Number	Rev
		1A
HSW PCH(GPIO/MISC)		
Date:	Monday, August 19, 2013	Sheet 11 of 40

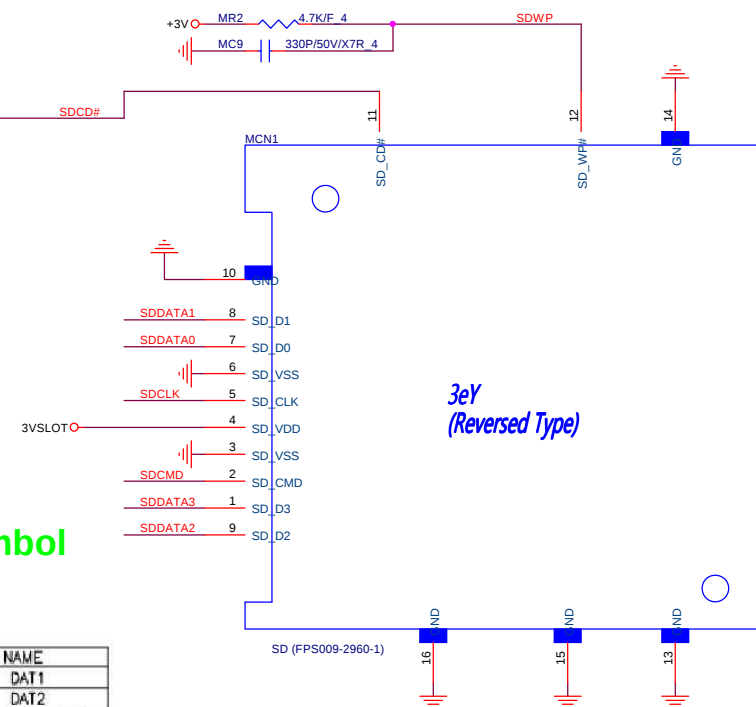
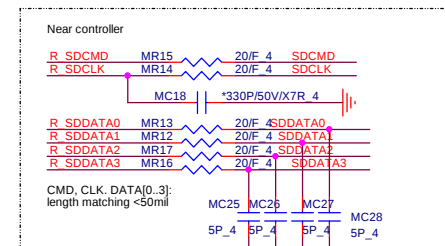
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.











check symbol

3eY
(Reversed Type)

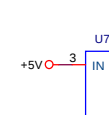
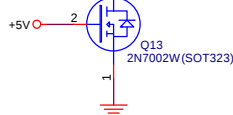
Pin No.	NAME	Pin No.	NAME
1	CD/DAT3	8	DAT1
2	CMD	9	DAT2
3	Vss1	10	CD/ WP (GND) Pin
4	VDD	11	CD Pin
5	CLK	12	WP Pin
6	Vss2	13	GND (Shell)
7	DAT0	14	GND (Shell)
		15	GND (Shell)
		16	GND (Shell)



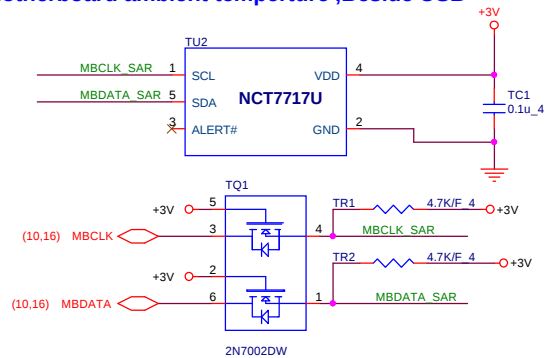
Quanta Computer Inc.
PROJECT : FI1

Size	Document Number	Rev
	CARD	1A
Date:	Monday, August 19, 2013	Sheet 18 of 40

- 1.Level 1 Environment-related Substances Should Never be Used.
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H/W Thermal Protect



$$R_{SET}(k\Omega) = 0.0012T^2 - 0.9308T + 96.147$$

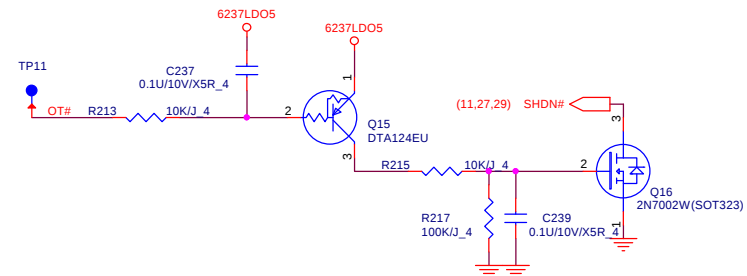
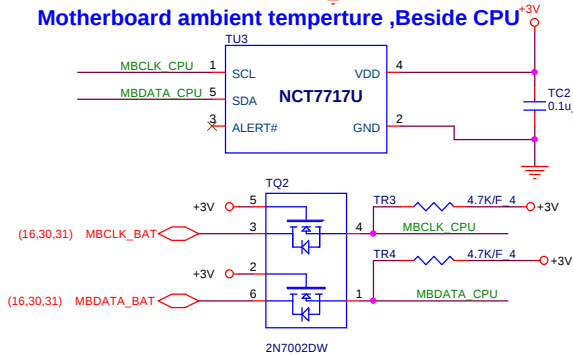
95	18.5K
100	15K
107	10.3K
110	8.2K

DIS SKU

Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	70	R208=36.87K	36.5K	71	70
Near GFX sensor temp	65	R146=40.72K	40.2K	66.3	65.1
Near AUDIO sensor temp	60	R345=44.62K	44.2K	61.2	60

UMA SKU

Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	81	R208=28.63K	28K	82.3	81.4
Near AUDIO sensor temp	58	R345=46.2K	46.4K	58.4	57.1



PROJECT : FI1

HDMI/Thermal IC

Size	Document Number
	HDMI/T
Date:	Monday, August 19, 2013

Rev
1A

- 1.Level 1 Environment-related Substances Should Never be Used.
- 2.Recycled Resin and Coated Wire should be procured from Green Partners.

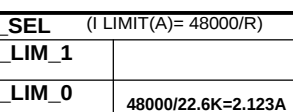


Mode CDP OFF DCP

VBUS

VBUS stop time 1Sec

USB 3.0 PORT0



System State	USB Battery Charging Setting			
	Disable	C(1 2 3)	Enable	C(1 2 3)
S0	SDP	(X 1 0)	CDP	(1 1 1)
S3	SDP	(X 1 0)	DCP BC	(1 0 0)
DS3	Charger OFF	(0 0 0)	DCP BC	(1 0 0)
S4	Charger OFF	(0 0 0)	DCP BC	(1 0 0)
S5	Charger OFF	(0 0 0)	DCP BC	(1 0 0)

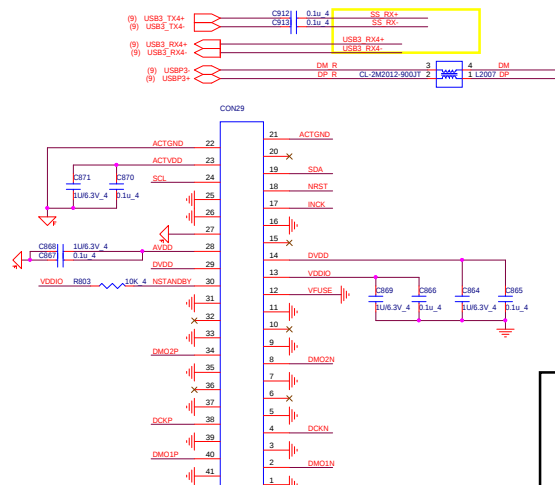
- 1.Level 1 Environment-Related Substances should never be Used.
- 2.Recycled Resin and Coated Wire should be procured from Green Partners.

Realtek PC Camera RTS5825

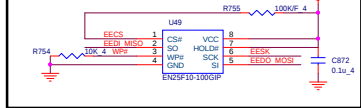
To Rear Camera(8M)

Check

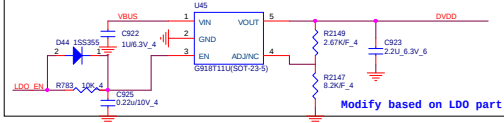
PVT 0721



SPI Interface

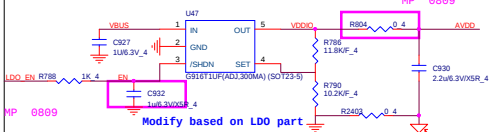


1.05V LDO for sensor core power



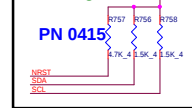
Modify based on LDO part

2.7V LDO for sensor I/O analog power



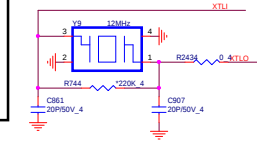
Modify based on LDO part

Pull High



PN 0415

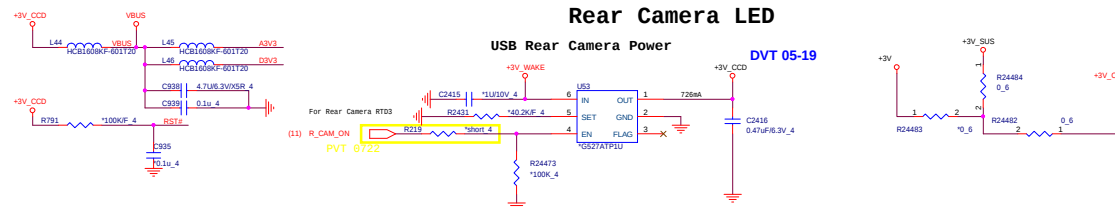
PN 0415



Rear Camera LED

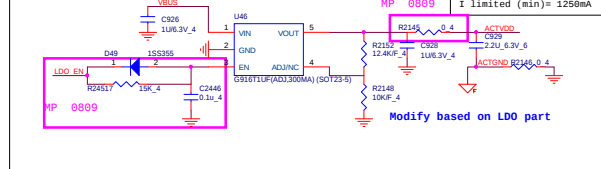
USB Rear Camera Power

DVT 05-19



To Front/Rear Camera(1M/8M)

2.8V LDO for sensor ACTVDD



Modify based on LDO part

1. Level 1 Environment-related Substances Should Never be Used.
2. Recycled Resin and Coated Wire should be procured from Green Partners.

NGFF Wifi/BT connector

WLAN/WIMAX/WIGig Need Type A

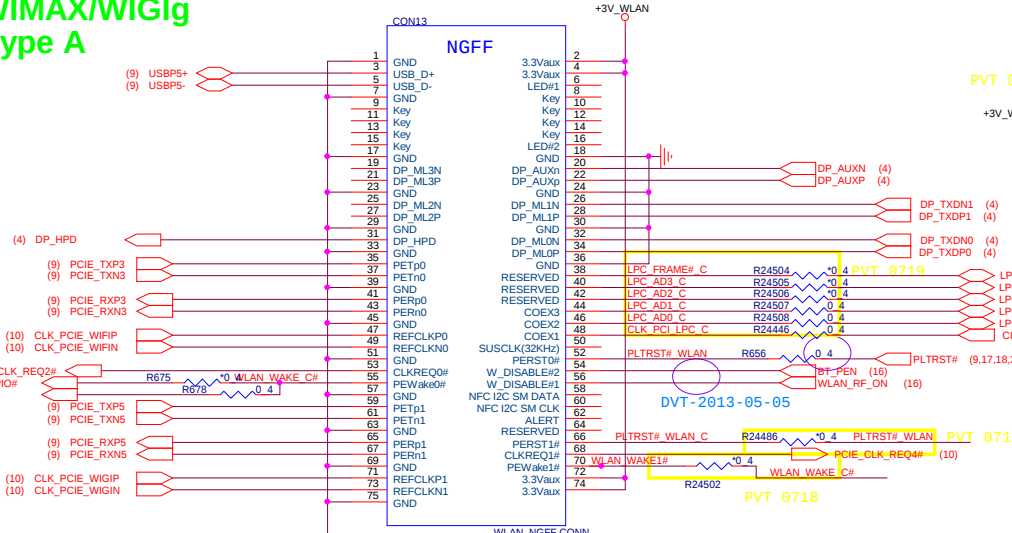
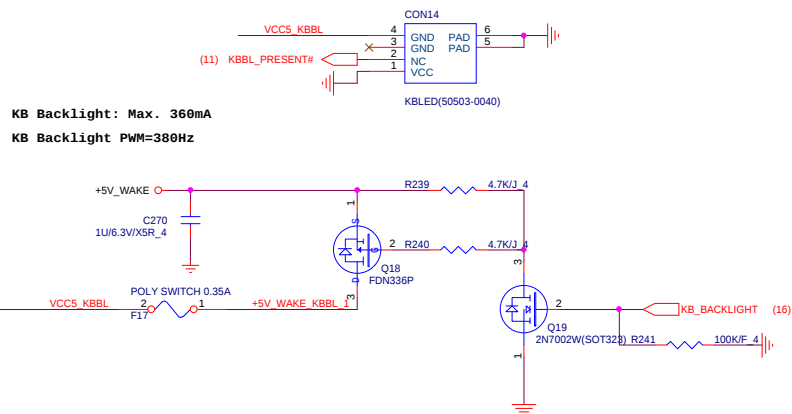
22

WiFi

WiGig

Check symbol

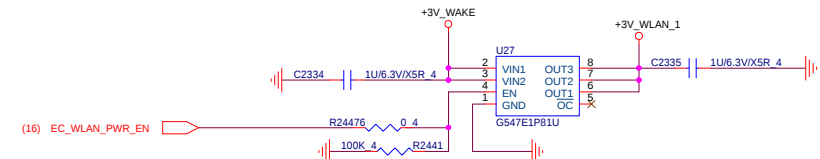
KB BACKLIGHT



+3.3V_NGFF_WLAN
Max Current : 1000mA

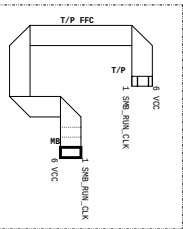
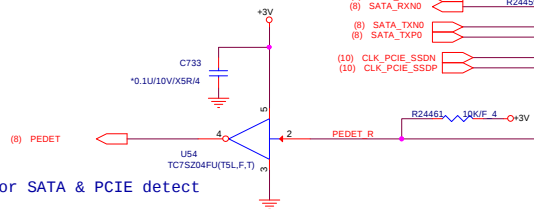
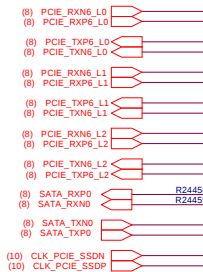
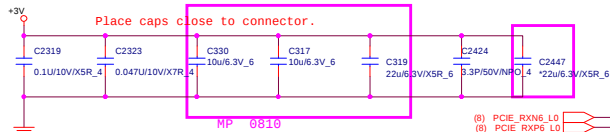
Debug Port

AC Mode : Support Wake on WLAN
DC Mode : Don't support wake on WLAN



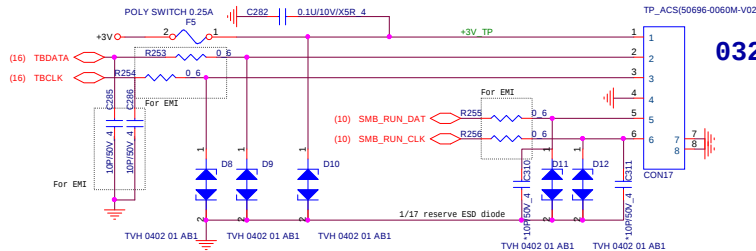
USE NGFF Type M

Peak Current : 3.0A,120mil

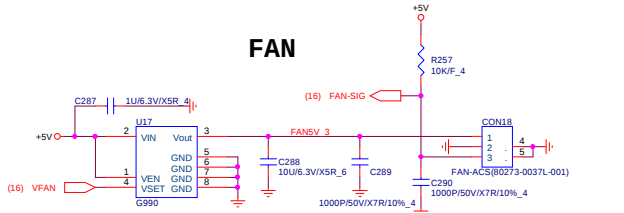


T/P Board to T/P

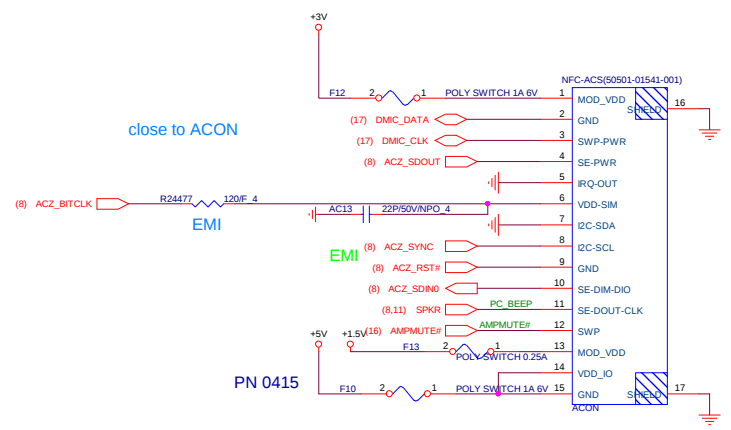
0327reverse



FAN

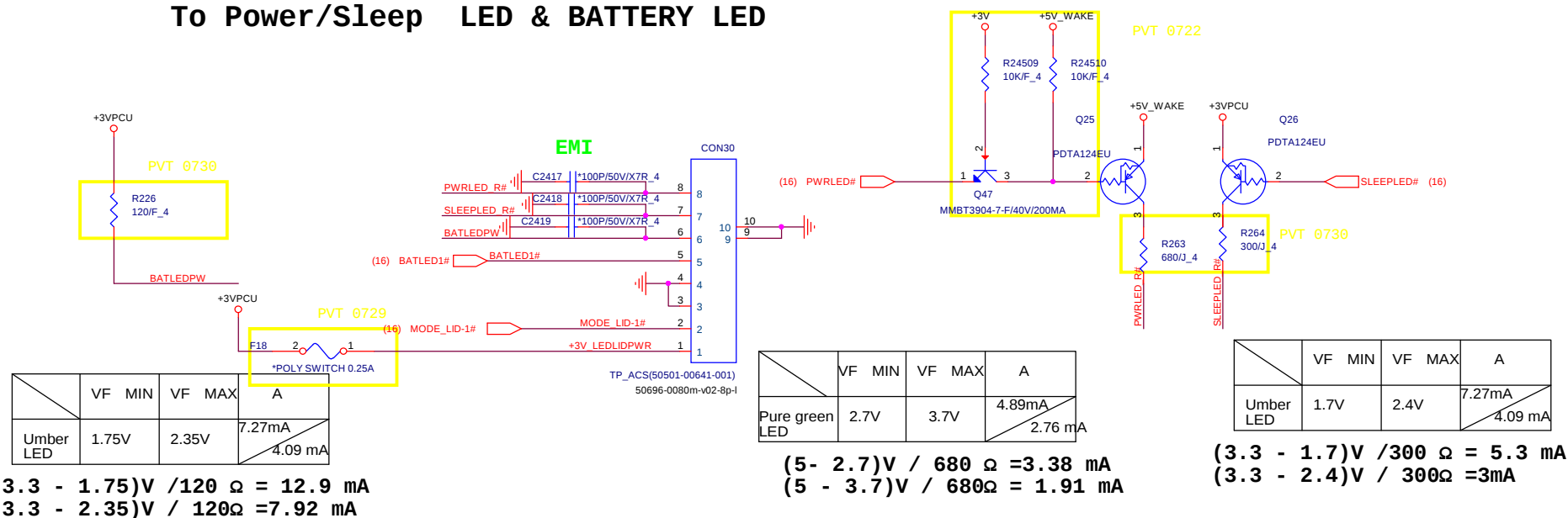


1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured From Green Partners.



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

To Power/Sleep LED & BATTERY LED



Power & Assist SW Follow FI3

D48, C2437 near SW1

D47, C299 near SW2

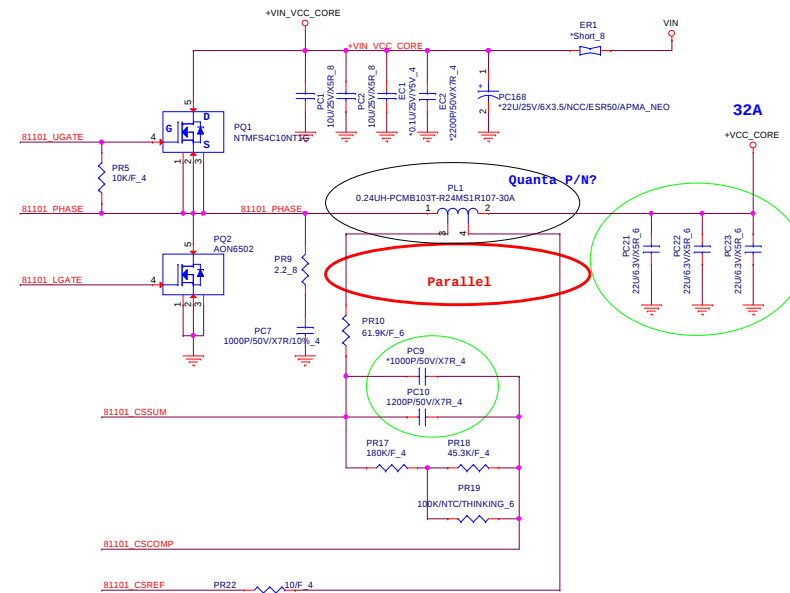
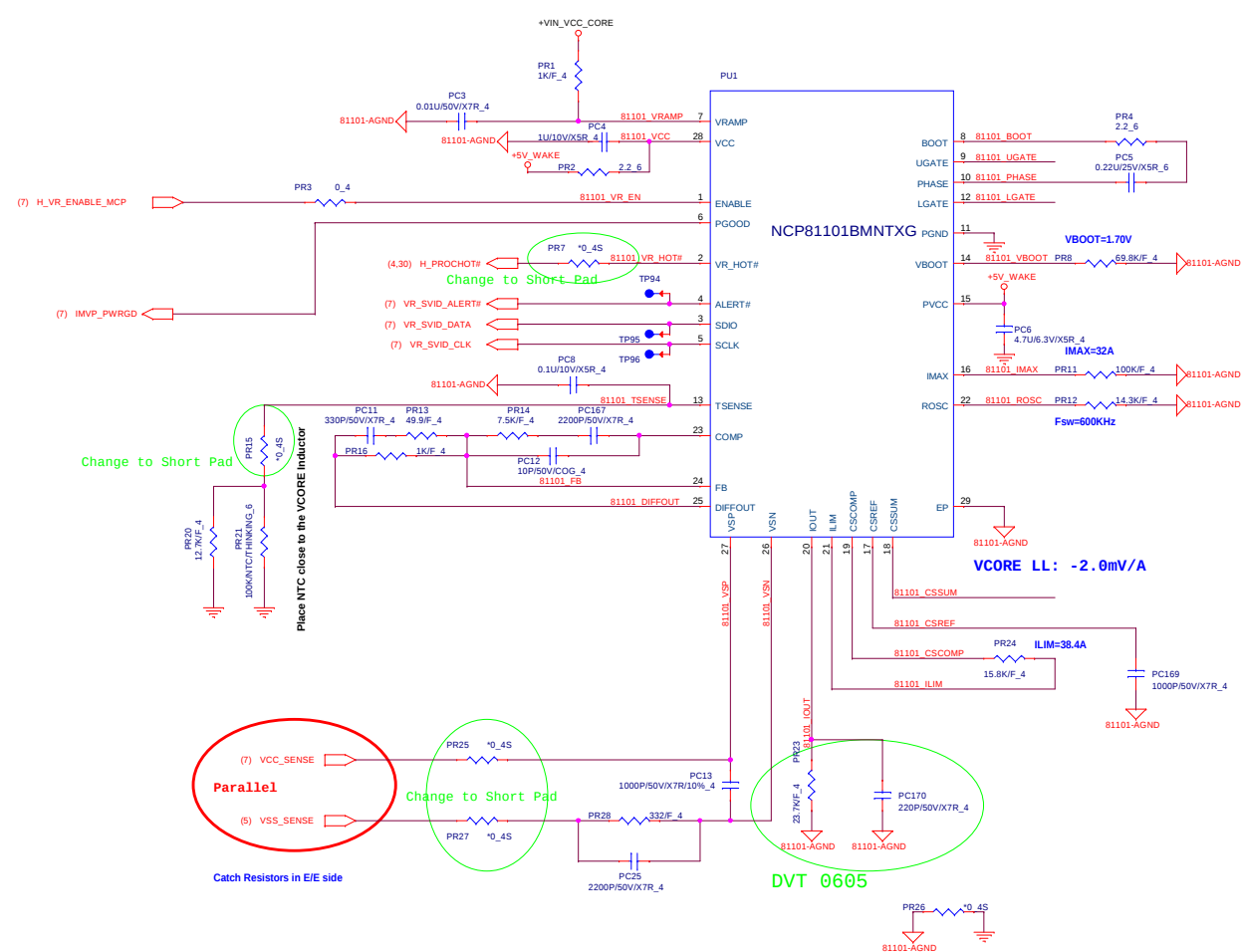
D45, C2435 near SW3

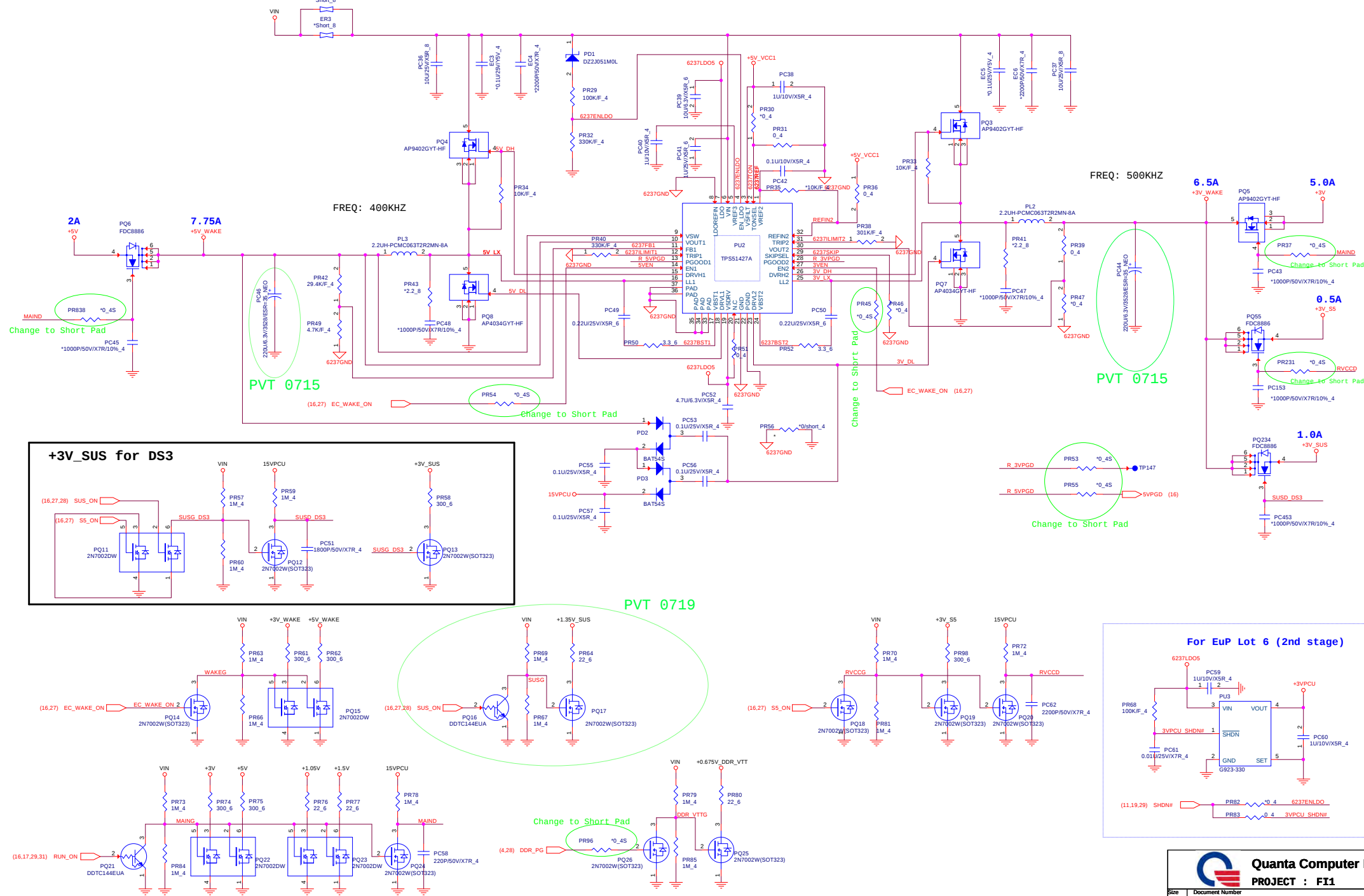
D46, C2436 near SW4



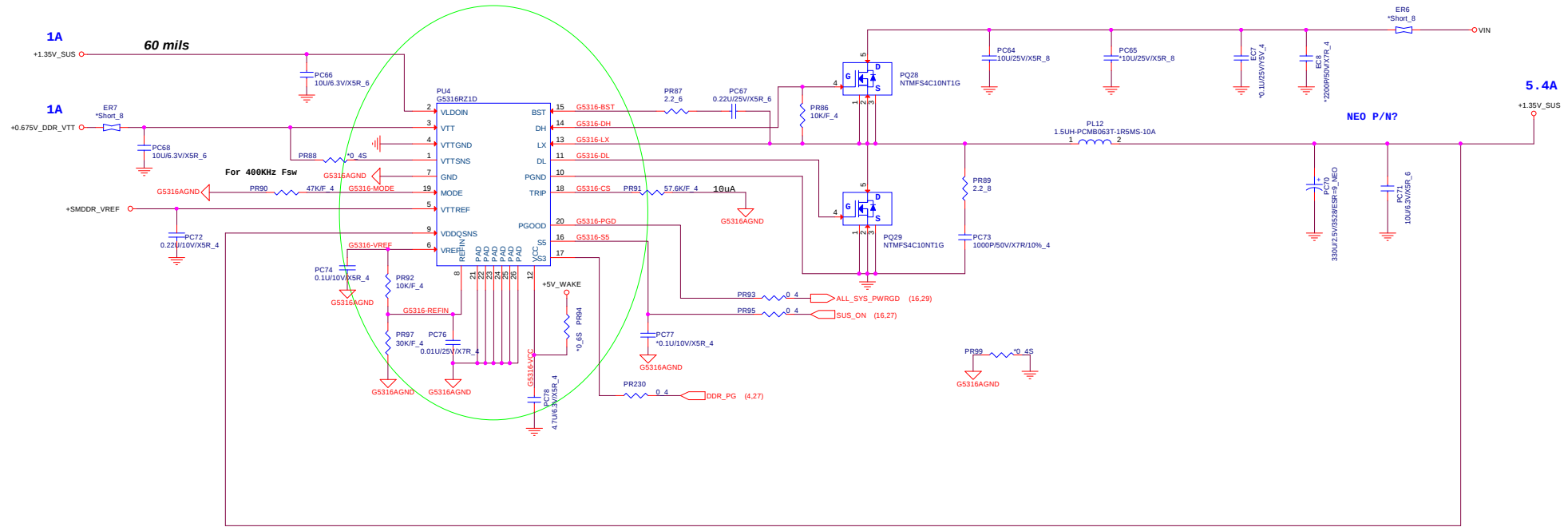
Quanta Computer Inc.
PROJECT : FI1

Size	Document Number	Rev
	LED/RF/KB/PS	1A
Date:	Monday, August 19, 2013	Sheet 25 of 40



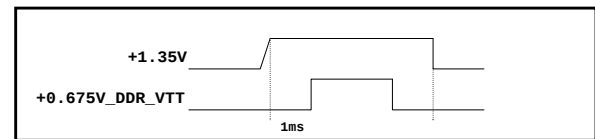


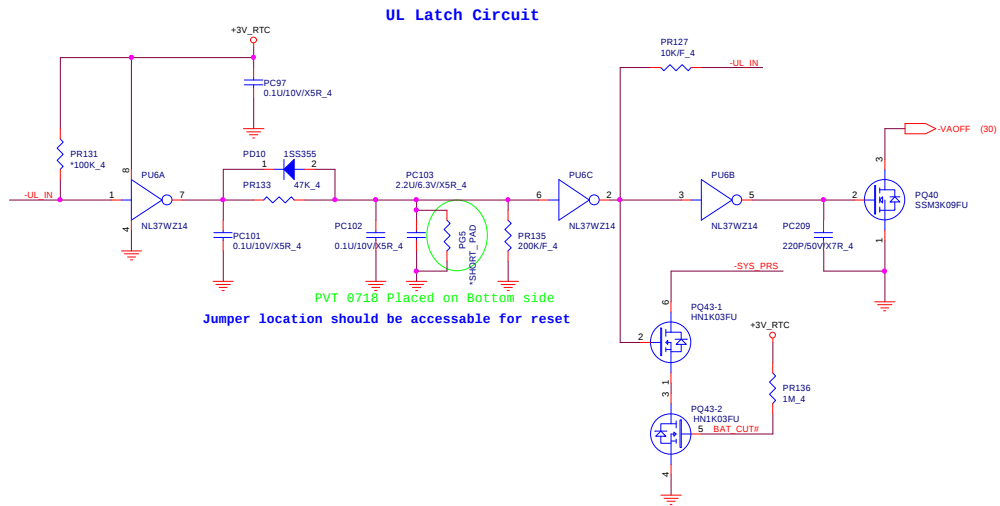
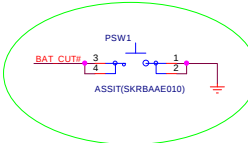
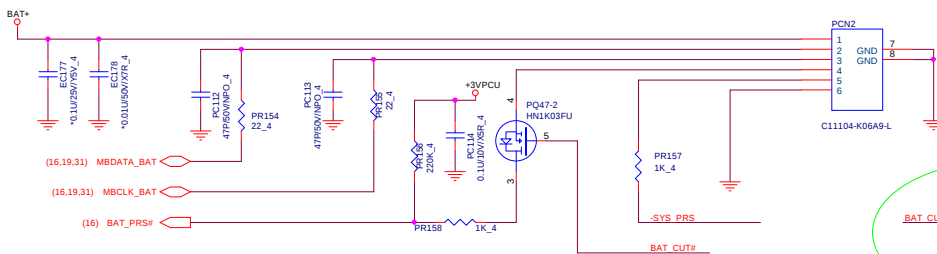
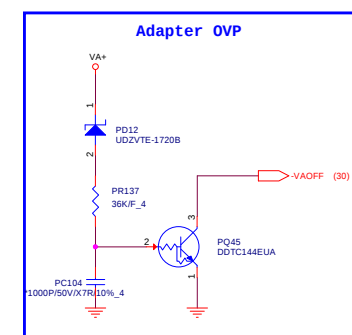
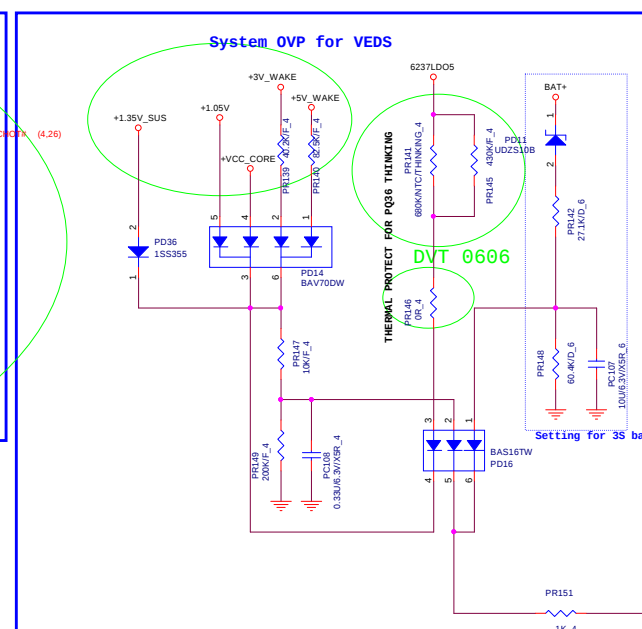
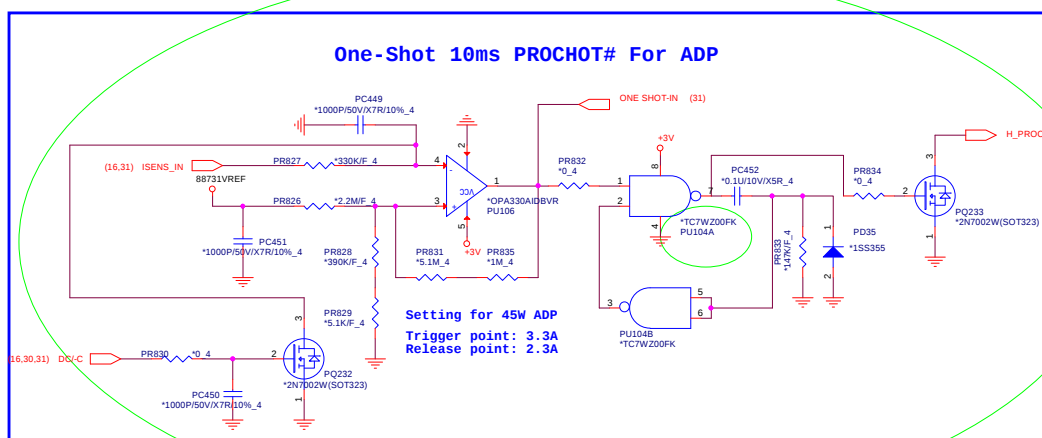
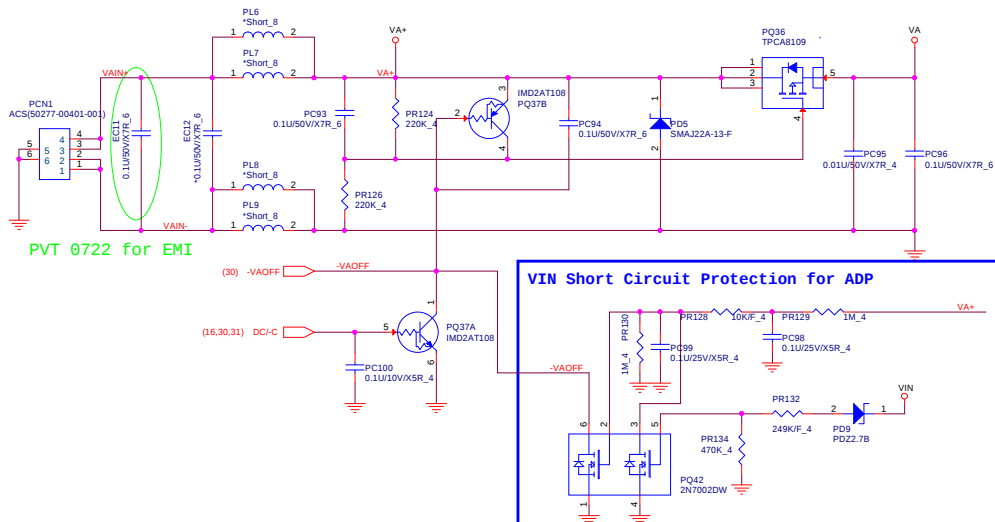
1.35VSUS & VTT_MEM

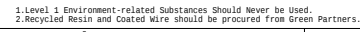


MODE	Resistor on Mode	Fsw	Discharge Mode
3	200Kohm	400KHZ	Tracking discharge
2	100Kohm	300KHZ	
1	60Kohm	300KHZ	Non-tracking discharge
0	47Kohm	400KHZ	

STATE	S3	S5	1.5VSUS	VTTREF	VTT
S0	1	1	On	On	On
S3	0	1	On	On	Off/High Z
S4/S5	0	0	Off	Off	Off







D

D

C

C

B

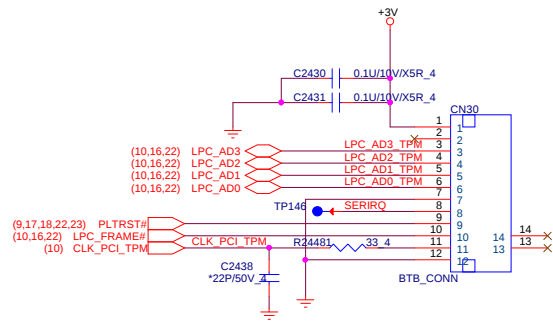
B

A

A

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

		Quanta Computer Inc.	
		PROJECT : FI1	
Size	Document Number	Rev	
		1A	
Debug con			
Date:	Monday, August 19, 2013	Sheet	32 of 40



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

D

D

C

C

B

B

A

A

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

		Quanta Computer Inc.	
		PROJECT : FI1	
Size	Document Number		Rev
	VRAM 3/4		1A
Date:	Monday, August 19, 2013		Sheet 34 of 40

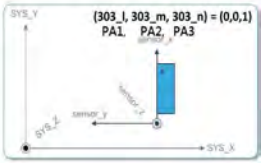


1.Level 1 Environment-related Substances Should Never be Used.
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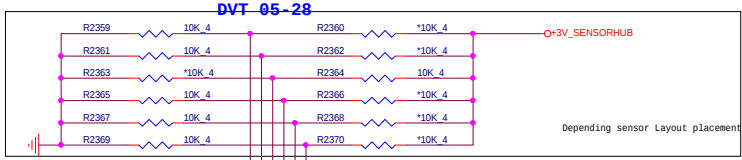
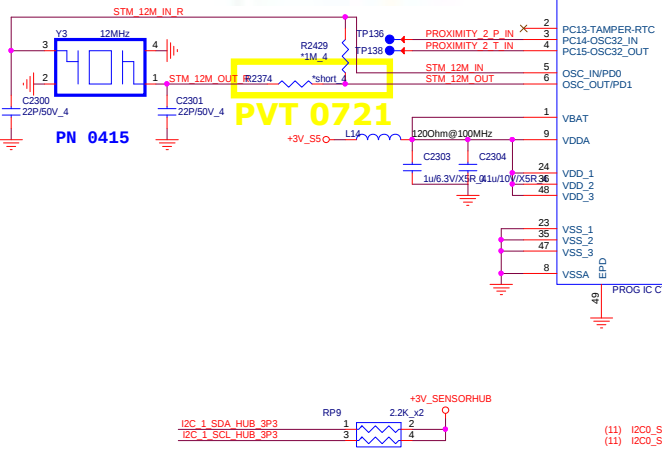
		Quanta Computer Inc.	
		PROJECT : FI1	
Size	Document Number		Rev
	VRAM 4/4		1A
Date:	Monday, August 19, 2013	Sheet	35 of 40

Sensor HUB

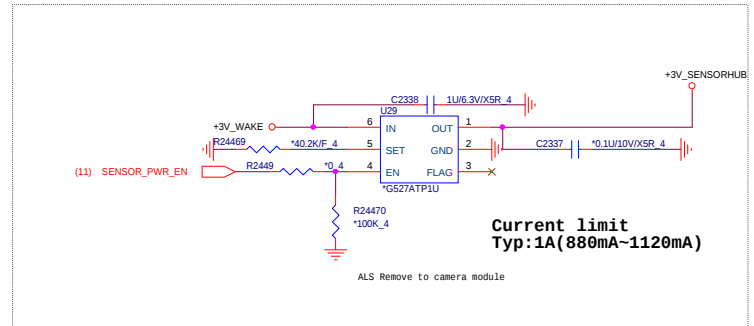
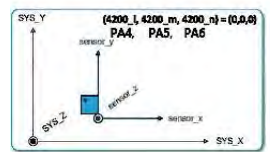
LSM303DLHC Orientation Setting (Top)



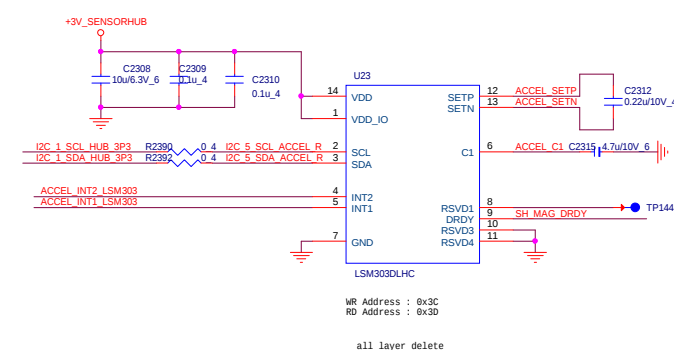
PVT 0730



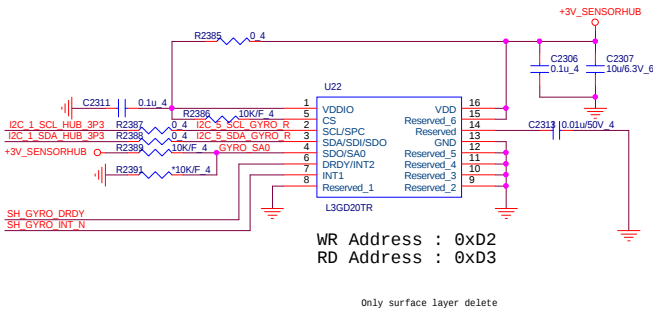
L3GD20TR Orientation Setting (Top)



G-sensor/E-compass/Magnetometer



Gyroscope



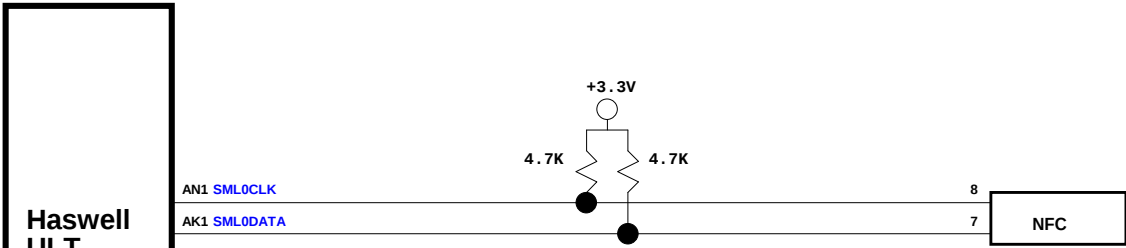
1.Level 1 Environment-related Substances Should Never be Used.
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- 1.Level 1 Environment-related Substances Should Never be Used.
- 2.Recycled Resin and Coated Wire should be procured from Green Partners.

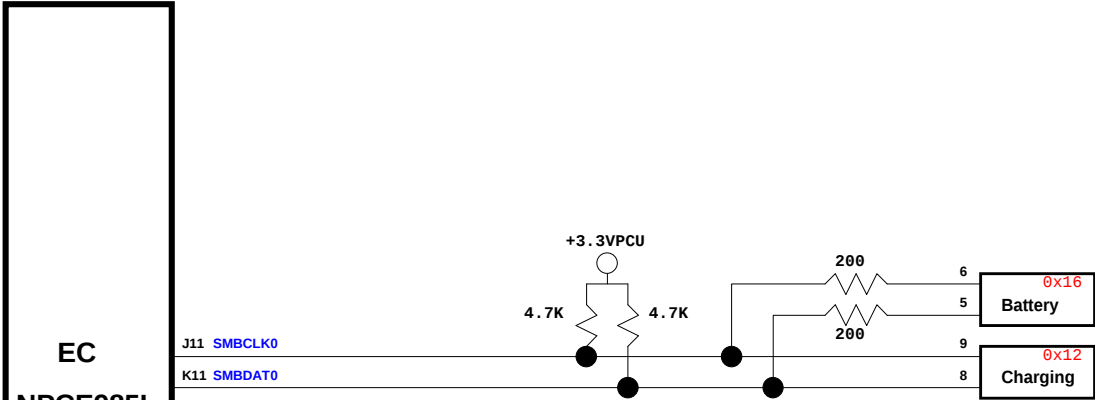


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Size	Document Number	Rev
	Blank	1A
Date:	Monday, August 19, 2013	Sheet 37 of 40



Function	IC	SMBus Address
Charge IC	ISL88731CHRTZ	0X12
Battery	Battery	0X16
NFC		



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Power	Voltage	S0	S3	DS3	S4 (/W RTC Wake up)	S4 (/W0 RTC Wake up)	S4 (/W0 CHANGE)	S4 (/W CHANGE)	S5 (/W0 CHANGE)	S5 (/W CHANGE)	Ctl Signal
+3VPCU	3.3V	V	V	V	V	V	V	V	V	V	Adapter/BAT in
+3.3V_DSW	3.3V	V	V	V	V	V	V	V	V	V	DEEP_EC_EN
+3V_WAKE	3.3V	V	V	V	V			V		V	EC_WAKE_ON
+5V_WAKE	5V	V	V	V	V			V		V	EC_WAKE_ON
+5V_S5	5V	V	V		V						S5_ON
+3V_S5	3.3V	V	V		V						S5_ON
+1.35V_SUS	1.35V	V	V	V							SUS_ON
+3V	3.3V	V									RUN_ON
+5V	5V	V									RUN_ON
+0.675V_DDR_VTT	0.675V	V									RUN_ON
+1.05V	1.05V	V									RUN_ON
+1.5V	1.5V	V									RUN_ON
+VCC_CORE	By VID	V									RUN_ON



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PROJECT : FI1

Size	Document Number	Rev
	Power Table	1A
Date:	Monday, August 19, 2013	Sheet 39 of 40

