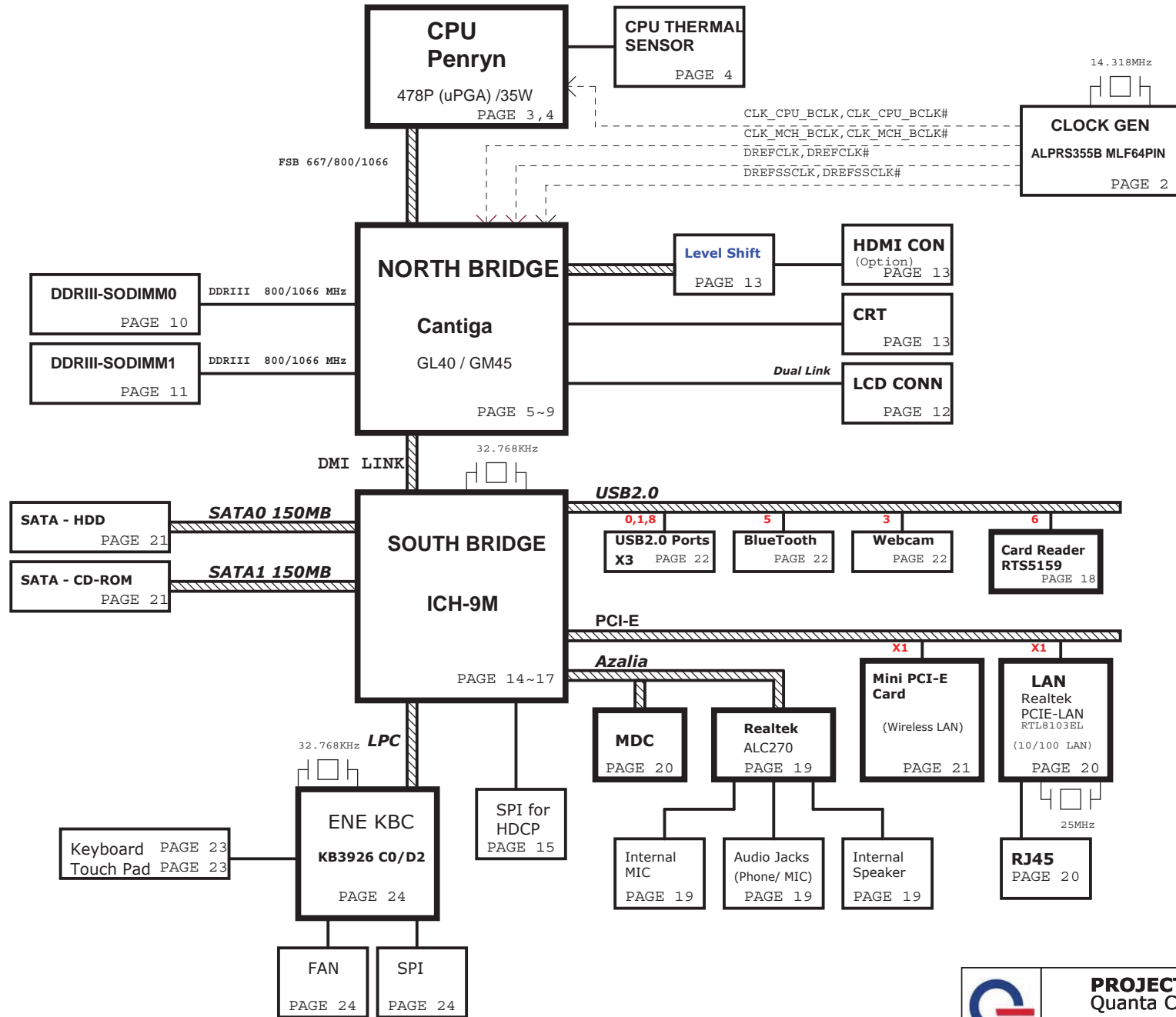


# Wimbledon AX3/5 BLOCK DIAGRAM

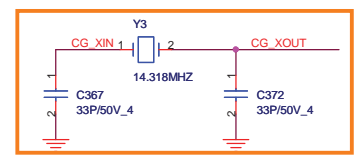
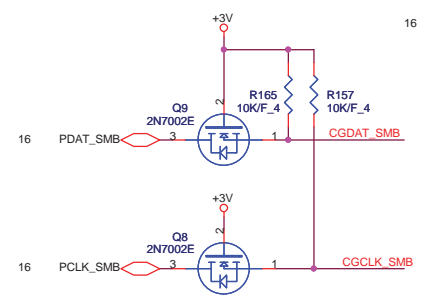
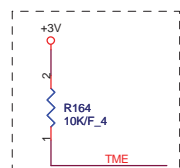
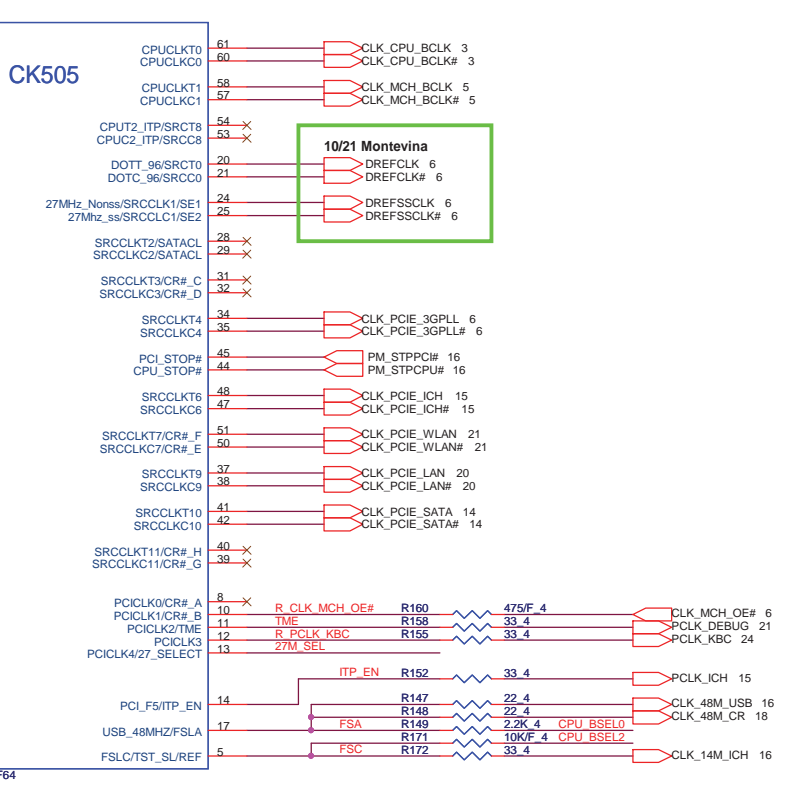
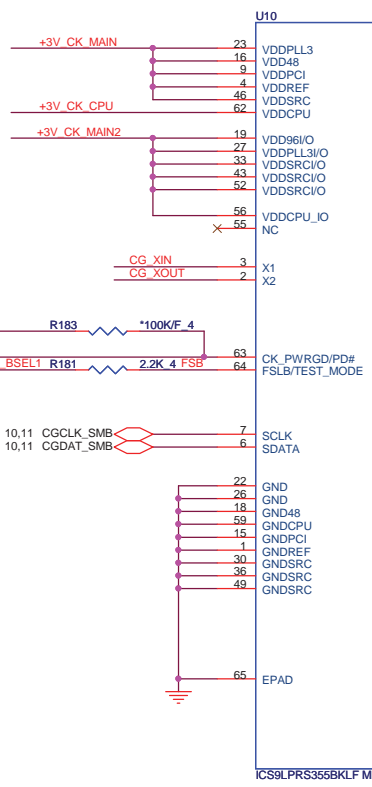
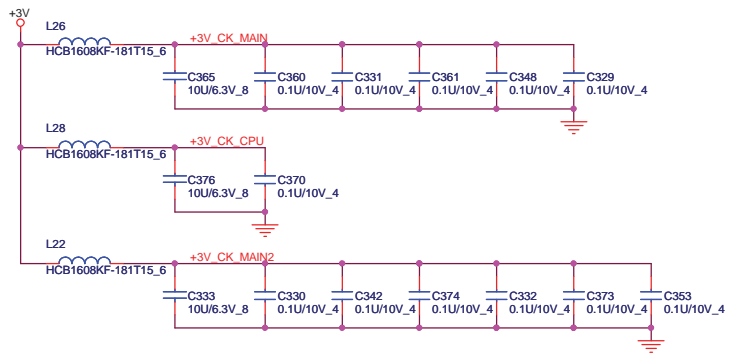
PCB STACK UP  
6L UMA CO-LAY DIS

- LAYER 1 : TOP
- LAYER 2 : SGND
- LAYER 3 : IN1
- LAYER 4 : IN2
- LAYER 5 : VCC
- LAYER 6 : BOT



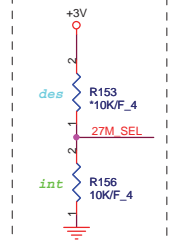
- SYSTEM CHARGER (ISL6251AHAZ-T) PAGE 25
- SYSTEM POWER (RT8206BGQW) PAGE 26
- VCCP +1.05V (RT8209AGQW) & +1.8VSUS (RT9043GB) PAGE 27
- CPU CORE (ISL6262ACRZ-T) PAGE 28
- DDR III SMD DR +1.5VSUS (RT8207AGQW) PAGE 29
- SYSTEM DISCHARGER PAGE 30

3,4,5,6,8,9,14,17,27,28 +1.05V  
4,6,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,28,30 +3V

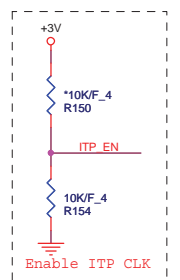


DB:Change from 27P to 33P(TXC suggestion)

0=overclocking of CPU and SRC Allowed  
1 = overclocking of CPU and SRC not Allowed



0=UMA  
1 = External VGA

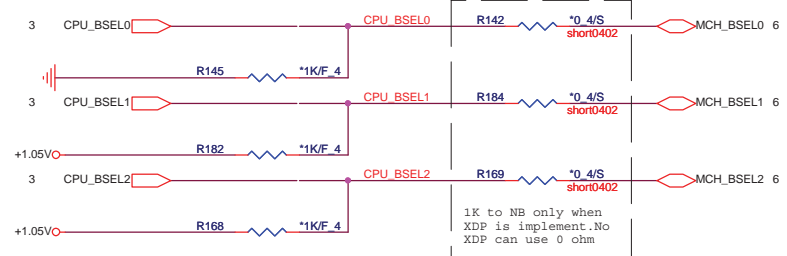


27M_SEL PIN13	PIN20	PIN21	PIN24	PIN25
0=UMA	DOT96T	DOT96C	SRCT1/LCDT_100	SRCT1/LCDT_100
1 = External VGA	SRCT0	SRCC0	27Mout-NSS	27Mout-SS

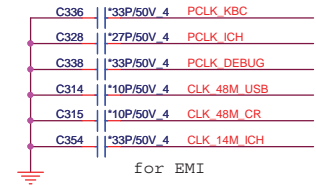
**CK505 QFN64**  
Silego SLG8SP513VTR AL8SP513000  
Realtek RTM875N-606-VD-GR AL000875000  
ICS ICS9LPRS355BKLF ALPRS355000



**CPU Clock select**



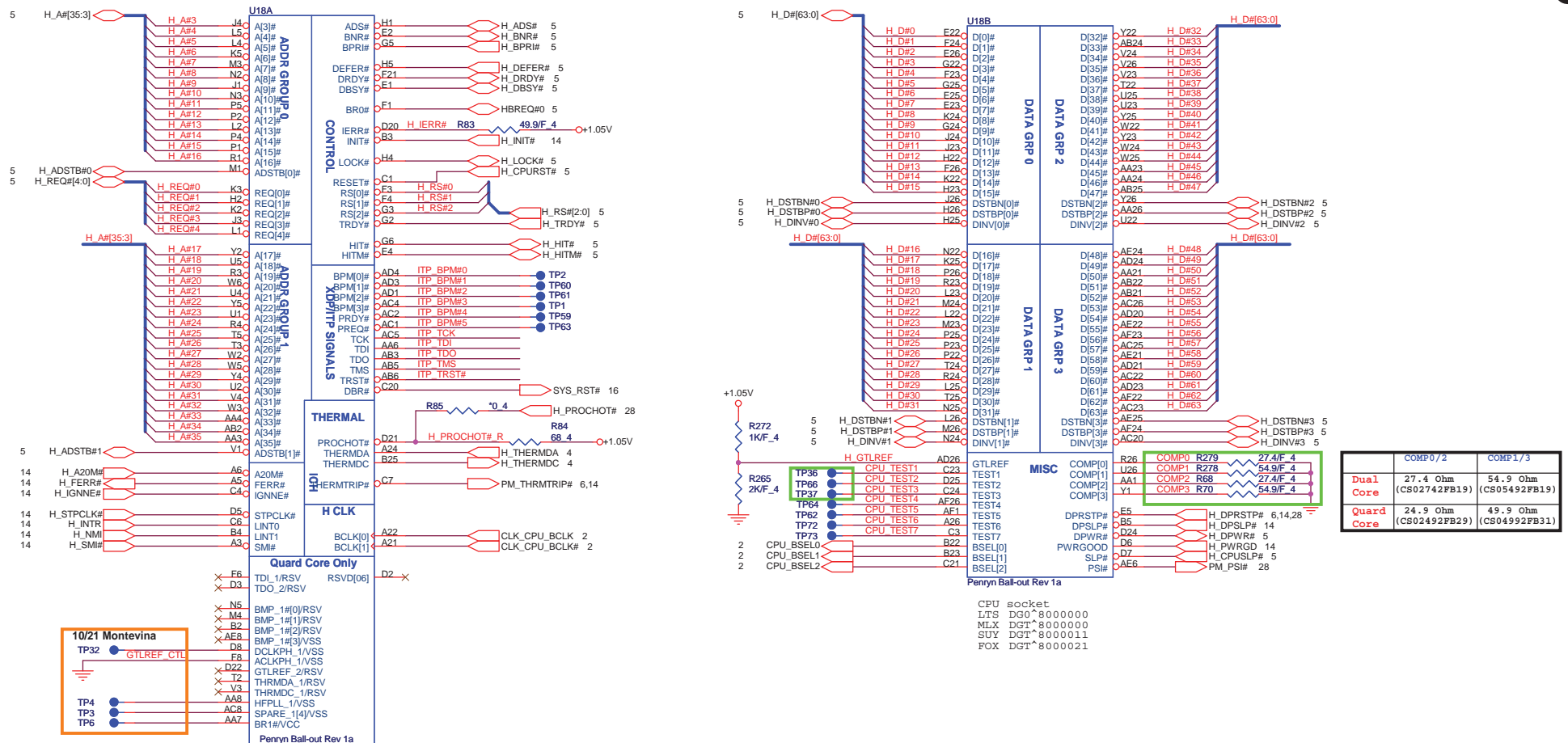
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33



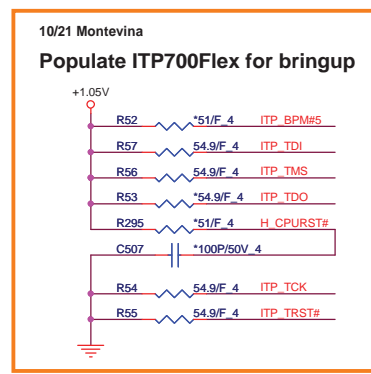
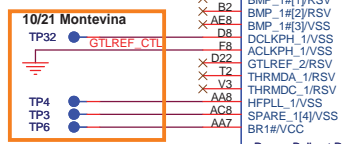
**PROJECT :AX3**  
Quanta Computer Inc.

**NBS5**

Size Custom Document Number **Clock Generator** Rev 1A  
Date: Friday, November 27, 2009 Sheet 2 of 30



	COMP0/2	COMP1/3
Dual Core	27.4 Ohm (CS02742FB19)	54.9 Ohm (CS05492FB19)
Quad Core	24.9 Ohm (CS02492FB29)	49.9 Ohm (CS04992FB31)



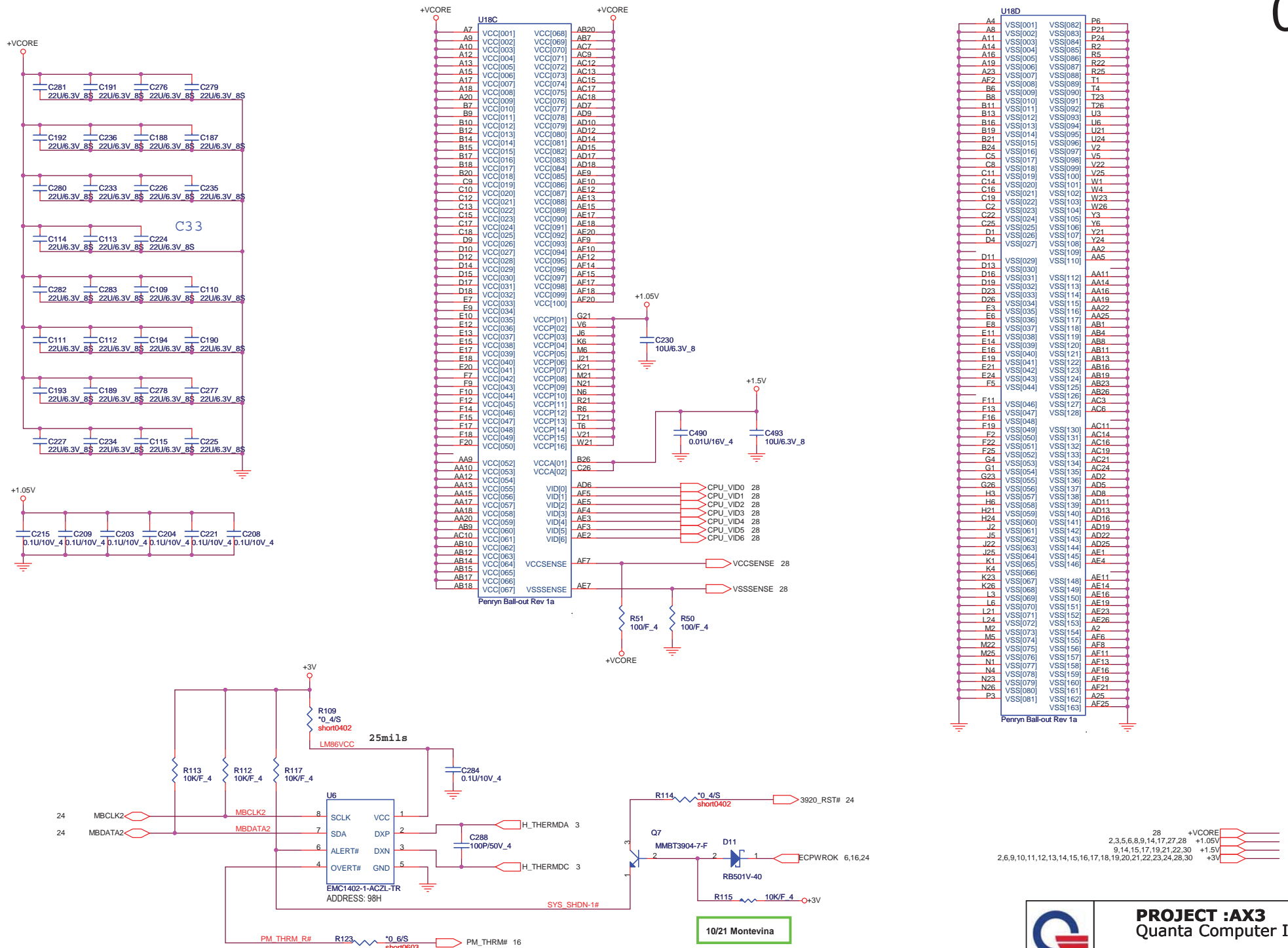
2,4,5,6,8,9,14,17,27,28 +1.05V

**PROJECT :AX3**  
Quanta Computer Inc.

**NBS5**

Size Custom Document Number Penryn Host 1/2 Rev 1A

Date: Friday, November 27, 2009 Sheet 3 of 30



U18D	VSS	P
A4	VSS[001]	P6
A8	VSS[002]	P21
A14	VSS[003]	P24
A16	VSS[004]	VSS[085]
A19	VSS[006]	VSS[086]
A23	VSS[007]	VSS[087]
A6	VSS[008]	VSS[088]
B8	VSS[009]	VSS[089]
B11	VSS[010]	VSS[090]
B13	VSS[012]	VSS[091]
B16	VSS[013]	VSS[092]
B19	VSS[014]	VSS[093]
B21	VSS[015]	VSS[094]
B24	VSS[016]	VSS[095]
C5	VSS[017]	VSS[096]
C8	VSS[018]	VSS[097]
C11	VSS[019]	VSS[098]
C14	VSS[020]	VSS[099]
C16	VSS[021]	VSS[100]
C19	VSS[022]	VSS[101]
C2	VSS[023]	VSS[102]
C22	VSS[024]	VSS[103]
C25	VSS[025]	VSS[104]
D1	VSS[026]	VSS[105]
D4	VSS[027]	VSS[106]
D11	VSS[029]	VSS[107]
D13	VSS[030]	VSS[108]
D16	VSS[031]	VSS[109]
D19	VSS[032]	VSS[110]
D23	VSS[033]	VSS[111]
D26	VSS[034]	VSS[112]
E3	VSS[035]	VSS[113]
E6	VSS[036]	VSS[114]
E8	VSS[037]	VSS[115]
E11	VSS[038]	VSS[116]
E14	VSS[039]	VSS[117]
E16	VSS[040]	VSS[118]
E19	VSS[041]	VSS[119]
E21	VSS[042]	VSS[120]
E24	VSS[043]	VSS[121]
F5	VSS[044]	VSS[122]
F11	VSS[046]	VSS[123]
F13	VSS[047]	VSS[124]
F16	VSS[048]	VSS[125]
F19	VSS[049]	VSS[126]
F2	VSS[050]	VSS[127]
F22	VSS[051]	VSS[128]
F25	VSS[052]	VSS[129]
G4	VSS[053]	VSS[130]
G1	VSS[054]	VSS[131]
G23	VSS[055]	VSS[132]
G26	VSS[056]	VSS[133]
H3	VSS[057]	VSS[134]
H6	VSS[058]	VSS[135]
H21	VSS[059]	VSS[136]
H24	VSS[060]	VSS[137]
J6	VSS[061]	VSS[138]
J22	VSS[062]	VSS[139]
J25	VSS[063]	VSS[140]
K1	VSS[064]	VSS[141]
K4	VSS[065]	VSS[142]
K23	VSS[066]	VSS[143]
K26	VSS[067]	VSS[144]
L3	VSS[068]	VSS[145]
L6	VSS[070]	VSS[146]
L21	VSS[071]	VSS[147]
L24	VSS[072]	VSS[148]
M2	VSS[073]	VSS[149]
M5	VSS[074]	VSS[150]
M22	VSS[075]	VSS[151]
M25	VSS[076]	VSS[152]
N1	VSS[077]	VSS[153]
N4	VSS[078]	VSS[154]
N23	VSS[079]	VSS[155]
N26	VSS[080]	VSS[156]
P3	VSS[081]	VSS[157]
		VSS[158]
		VSS[159]
		VSS[160]
		VSS[161]
		VSS[162]
		VSS[163]

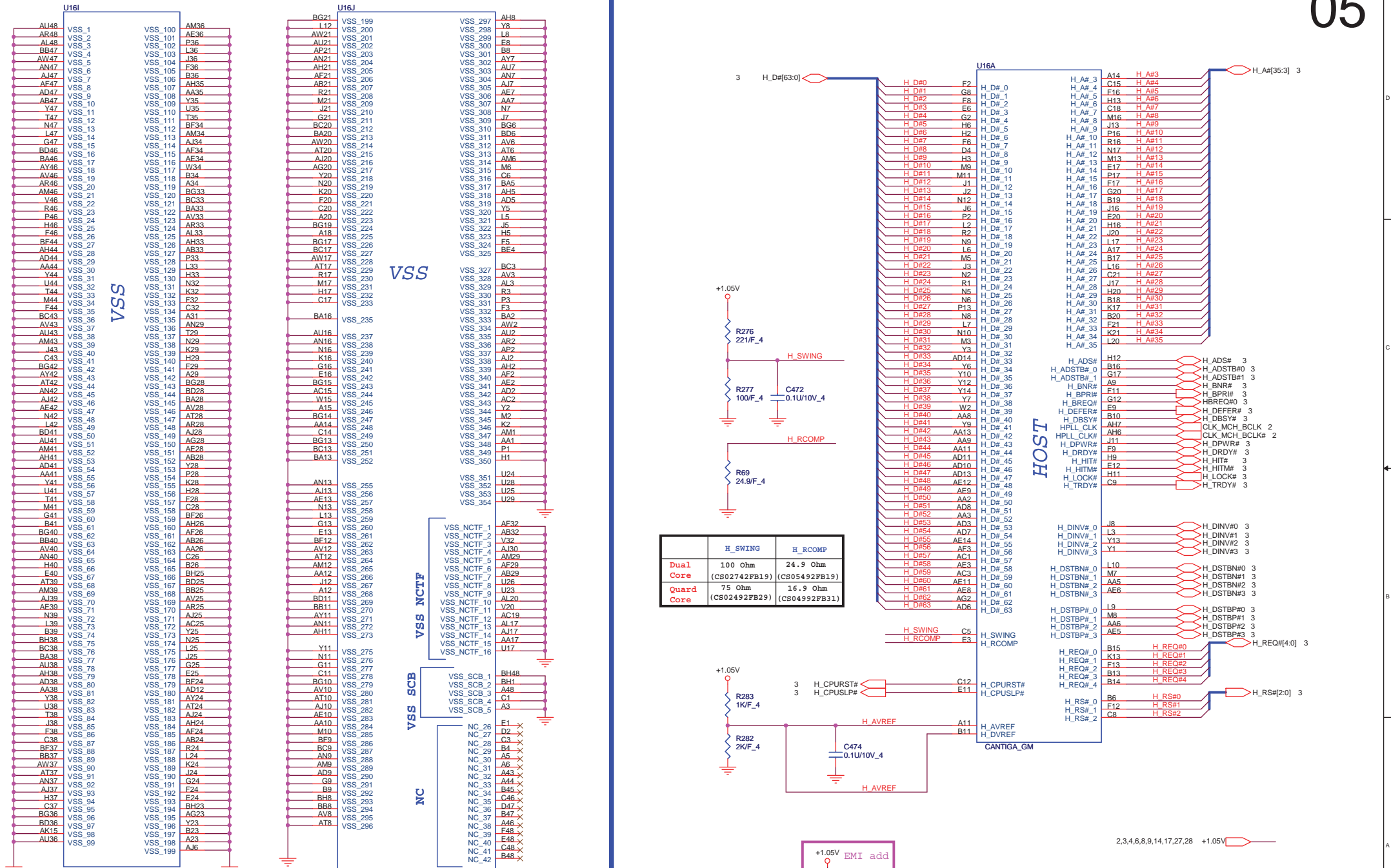
28 +VCCORE  
 2,3,5,6,8,9,14,17,27,28 +1.05V  
 9,14,15,17,19,21,22,30 +1.5V  
 2,6,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,26,30 +3V

**PROJECT :AX3**  
 Quanta Computer Inc.

Size Custom Document Number Penryn & TH Monitor 2/2 Rev 1A

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**PROJECT :AX3**  
**Quanta Computer Inc.**

Size Custom Document Number  
**Cantiga Host & VSS 1/5**

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**NBS5** Rev 1A



10 M\_A\_DQ[63:0]

M A D00	AJ38	SA DO 0
M A D01	AJ41	SA DO 1
M A D02	AN38	SA DO 2
M A D03	AM38	SA DO 3
M A D04	AJ36	SA DO 4
M A D05	AM40	SA DO 5
M A D06	AM44	SA DO 6
M A D07	AM42	SA DO 7
M A D08	AN43	SA DO 8
M A D09	AN44	SA DO 9
M A D10	AL40	SA DO 10
M A D11	AT38	SA DO 11
M A D12	AN41	SA DO 12
M A D13	AN39	SA DO 13
M A D14	AL44	SA DO 14
M A D15	AV39	SA DO 15
M A D16	AY44	SA DO 16
M A D17	BA40	SA DO 17
M A D18	BD43	SA DO 18
M A D19	AV41	SA DO 19
M A D20	AT38	SA DO 20
M A D21	AY43	SA DO 21
M A D22	BB41	SA DO 22
M A D23	BC40	SA DO 23
M A D24	AY37	SA DO 24
M A D25	BD38	SA DO 25
M A D26	AV37	SA DO 26
M A D27	AT36	SA DO 27
M A D28	AY38	SA DO 28
M A D29	BB38	SA DO 29
M A D30	AV36	SA DO 30
M A D31	AW36	SA DO 31
M A D32	BD13	SA DO 32
M A D33	AU11	SA DO 33
M A D34	BC11	SA DO 34
M A D35	BA12	SA DO 35
M A D36	AU13	SA DO 36
M A D37	AV13	SA DO 37
M A D38	BD12	SA DO 38
M A D39	BC12	SA DO 39
M A D40	BB9	SA DO 40
M A D41	BA9	SA DO 41
M A D42	AL10	SA DO 42
M A D43	AV9	SA DO 43
M A D44	BA11	SA DO 44
M A D45	BD9	SA DO 45
M A D46	AV8	SA DO 46
M A D47	BA6	SA DO 47
M A D48	AV5	SA DO 48
M A D49	AV7	SA DO 49
M A D50	AT9	SA DO 50
M A D51	AN8	SA DO 51
M A D52	AU5	SA DO 52
M A D53	AU6	SA DO 53
M A D54	AT5	SA DO 54
M A D55	AN10	SA DO 55
M A D56	AM11	SA DO 56
M A D57	AM5	SA DO 57
M A D58	AJ9	SA DO 58
M A D59	AJ8	SA DO 59
M A D60	AN12	SA DO 60
M A D61	AM13	SA DO 61
M A D62	AJ11	SA DO 62
M A D63	AJ12	SA DO 63

DDR SYSTEM MEMORY A

SA_BS_0	BD21	M A BS#0 10
SA_BS_1	BG18	M A BS#1 10
SA_BS_2	AT25	M A BS#2 10
SA_RAS#	BB20	M A RAS# 10
SA_CAS#	RD20	M A CAS# 10
SA_WE#	AY20	M A WE# 10
SA_DM_0	AM37	M A DM0
SA_DM_1	AT41	M A DM1
SA_DM_2	AY41	M A DM2
SA_DM_3	AU39	M A DM3
SA_DM_4	BB12	M A DM4
SA_DM_5	AY6	M A DM5
SA_DM_6	AT7	M A DM6
SA_DM_7	AJ5	M A DM7
SA_DQS_0	AJ44	M A DQS0
SA_DQS_1	AT44	M A DQS1
SA_DQS_2	BA43	M A DQS2
SA_DQS_3	BC37	M A DQS3
SA_DQS_4	AW12	M A DQS4
SA_DQS_5	BC2	M A DQS5
SA_DQS_6	AU8	M A DQS6
SA_DQS_7	AM7	M A DQS7
SA_DQS#_0	AJ43	M A DQS#0
SA_DQS#_1	AT43	M A DQS#1
SA_DQS#_2	BA44	M A DQS#2
SA_DQS#_3	BD37	M A DQS#3
SA_DQS#_4	AY12	M A DQS#4
SA_DQS#_5	BD8	M A DQS#5
SA_DQS#_6	AU9	M A DQS#6
SA_DQS#_7	AM8	M A DQS#7
SA_MA_0	BA21	M A A0
SA_MA_1	BC24	M A A1
SA_MA_2	BG24	M A A2
SA_MA_3	BH24	M A A3
SA_MA_4	BC25	M A A4
SA_MA_5	BA24	M A A5
SA_MA_6	BD24	M A A6
SA_MA_7	BG27	M A A7
SA_MA_8	BF25	M A A8
SA_MA_9	AW24	M A A9
SA_MA_10	BC21	M A A10
SA_MA_11	BG26	M A A11
SA_MA_12	BH26	M A A12
SA_MA_13	BH17	M A A13
SA_MA_14	AY25	M A A14

CANTIGA\_GM

11 M\_B\_DQ[63:0]

M B D00	AK47	SB DO 0
M B D01	AH46	SB DO 1
M B D02	AP47	SB DO 2
M B D03	AP46	SB DO 3
M B D04	AJ46	SB DO 4
M B D05	AJ48	SB DO 5
M B D06	AM48	SB DO 6
M B D07	AP48	SB DO 7
M B D08	AL47	SB DO 8
M B D09	AJ46	SB DO 9
M B D10	BA48	SB DO 10
M B D11	AY48	SB DO 11
M B D12	AT47	SB DO 12
M B D13	AR47	SB DO 13
M B D14	BA47	SB DO 14
M B D15	BC47	SB DO 15
M B D16	BC46	SB DO 16
M B D17	BC44	SB DO 17
M B D18	BG43	SB DO 18
M B D19	BF43	SB DO 19
M B D20	BE45	SB DO 20
M B D21	BC41	SB DO 21
M B D22	BF40	SB DO 22
M B D23	BF41	SB DO 23
M B D24	BC38	SB DO 24
M B D25	BF38	SB DO 25
M B D26	BH35	SB DO 26
M B D27	BG35	SB DO 27
M B D28	BH40	SB DO 28
M B D29	BG39	SB DO 29
M B D30	BH34	SB DO 30
M B D31	BH14	SB DO 31
M B D32	BG12	SB DO 32
M B D33	BH11	SB DO 33
M B D34	BG8	SB DO 34
M B D35	BH12	SB DO 35
M B D36	BF11	SB DO 36
M B D37	BF8	SB DO 37
M B D38	BG7	SB DO 38
M B D39	BC5	SB DO 39
M B D40	BC6	SB DO 40
M B D41	AY3	SB DO 41
M B D42	AY1	SB DO 42
M B D43	BF6	SB DO 43
M B D44	BF5	SB DO 44
M B D45	BA1	SB DO 45
M B D46	BD3	SB DO 46
M B D47	AV2	SB DO 47
M B D48	AU3	SB DO 48
M B D49	AR3	SB DO 49
M B D50	AN2	SB DO 50
M B D51	AY2	SB DO 51
M B D52	AV1	SB DO 52
M B D53	AP3	SB DO 53
M B D54	AR1	SB DO 54
M B D55	AL1	SB DO 55
M B D56	AL2	SB DO 56
M B D57	AL1	SB DO 57
M B D58	AH1	SB DO 58
M B D59	AM2	SB DO 59
M B D60	AM3	SB DO 60
M B D61	AH3	SB DO 61
M B D62	AH3	SB DO 62
M B D63	AJ3	SB DO 63

DDR SYSTEM MEMORY B

CANTIGA\_GM

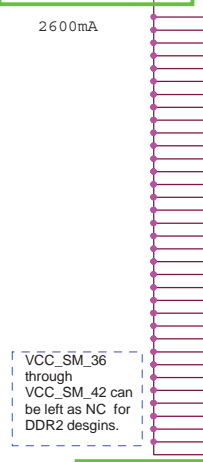
SB_BS_0	BC16	M B BS#0 11
SB_BS_1	BB17	M B BS#1 11
SB_BS_2	BB33	M B BS#2 11
SB_RAS#	AU17	M B RAS# 11
SB_CAS#	BG16	M B CAS# 11
SB_WE#	BF14	M B WE# 11
SB_DM_0	AM47	M B DM0
SB_DM_1	AY47	M B DM1
SB_DM_2	BD40	M B DM2
SB_DM_3	BF35	M B DM3
SB_DM_4	BG11	M B DM4
SB_DM_5	BA3	M B DM5
SB_DM_6	AP1	M B DM6
SB_DM_7	AK2	M B DM7
SB_DQS_0	AL47	M B DQS0
SB_DQS_1	AV48	M B DQS1
SB_DQS_2	BG41	M B DQS2
SB_DQS_3	BG37	M B DQS3
SB_DQS_4	BH9	M B DQS4
SB_DQS_5	BB2	M B DQS5
SB_DQS_6	AU1	M B DQS6
SB_DQS_7	AN6	M B DQS7
SB_DQS#_0	AL46	M B DQS#0
SB_DQS#_1	AV47	M B DQS#1
SB_DQS#_2	BH41	M B DQS#2
SB_DQS#_3	BH37	M B DQS#3
SB_DQS#_4	BG9	M B DQS#4
SB_DQS#_5	BC2	M B DQS#5
SB_DQS#_6	AT2	M B DQS#6
SB_DQS#_7	AN5	M B DQS#7
SB_MA_0	AV17	M B A0
SB_MA_1	BA25	M B A1
SB_MA_2	BC25	M B A2
SB_MA_3	AU25	M B A3
SB_MA_4	AW25	M B A4
SB_MA_5	BB28	M B A5
SB_MA_6	AU28	M B A6
SB_MA_7	AW28	M B A7
SB_MA_8	AT33	M B A8
SB_MA_9	BD33	M B A9
SB_MA_10	BB16	M B A10
SB_MA_11	AW33	M B A11
SB_MA_12	AY33	M B A12
SB_MA_13	BH15	M B A13
SB_MA_14	AU33	M B A14

**PROJECT :AX3**  
Quanta Computer Inc.

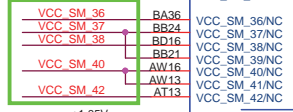
Size Custom Document Number  
NB5 Cantiga DDR3 3/5 Rev 1A

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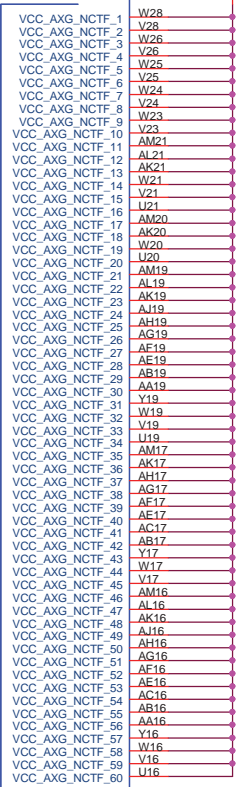
10/21 Montevina +1.5VSUS



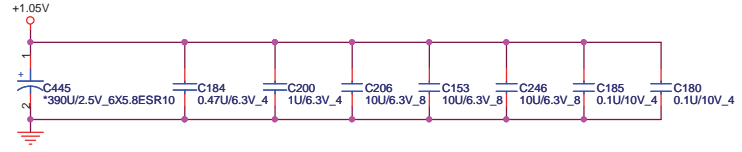
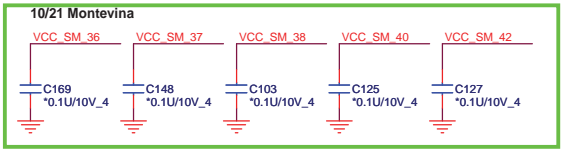
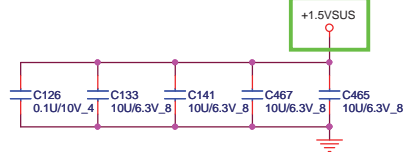
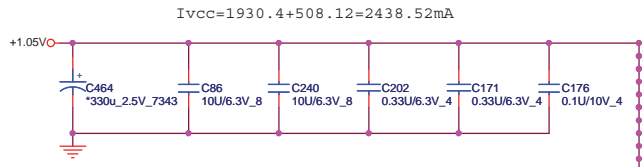
VCC\_SM\_36 through VCC\_SM\_42 can be left as NC for DDR2 designs.



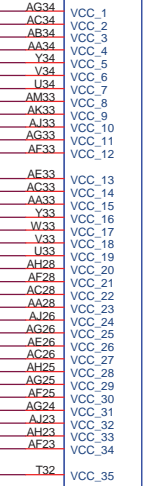
U16G



Ivcc\_axg=6326.84mA



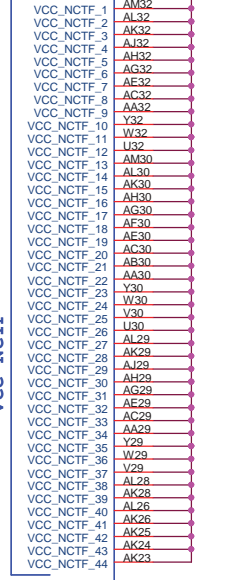
U16F



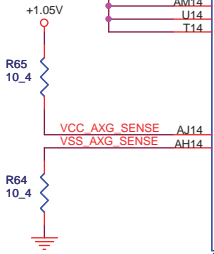
POWER

VCC CORE

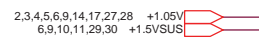
VCC NCTF



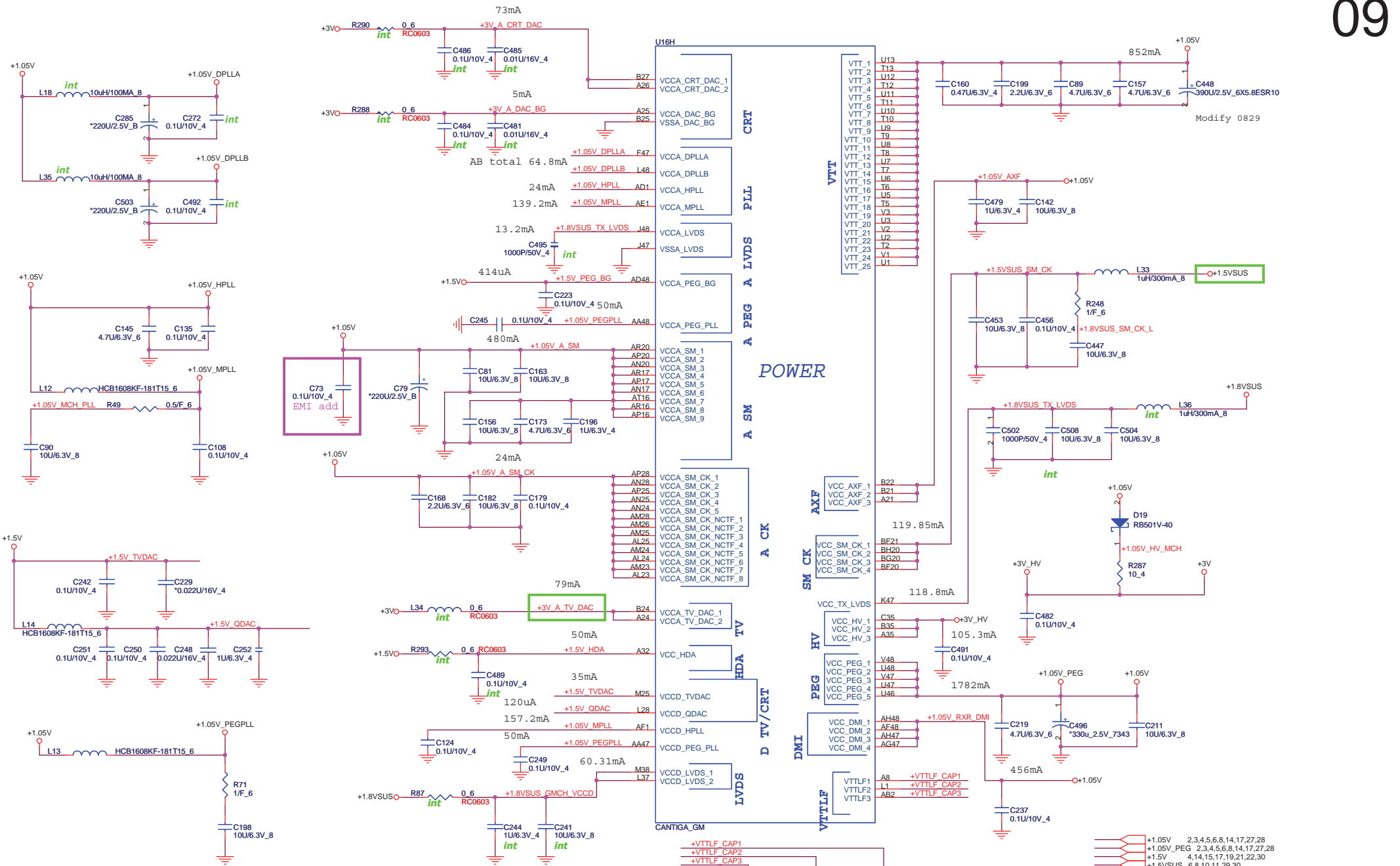
CANTIGA\_GM



CANTIGA\_GM



PROJECT :AX3  
Quanta Computer Inc.  
NB5  
Document Number  
Cantiga VCC 4/5  
Rev 1A  
Date: Friday, November 27, 2009 | Sheet 8 of 30

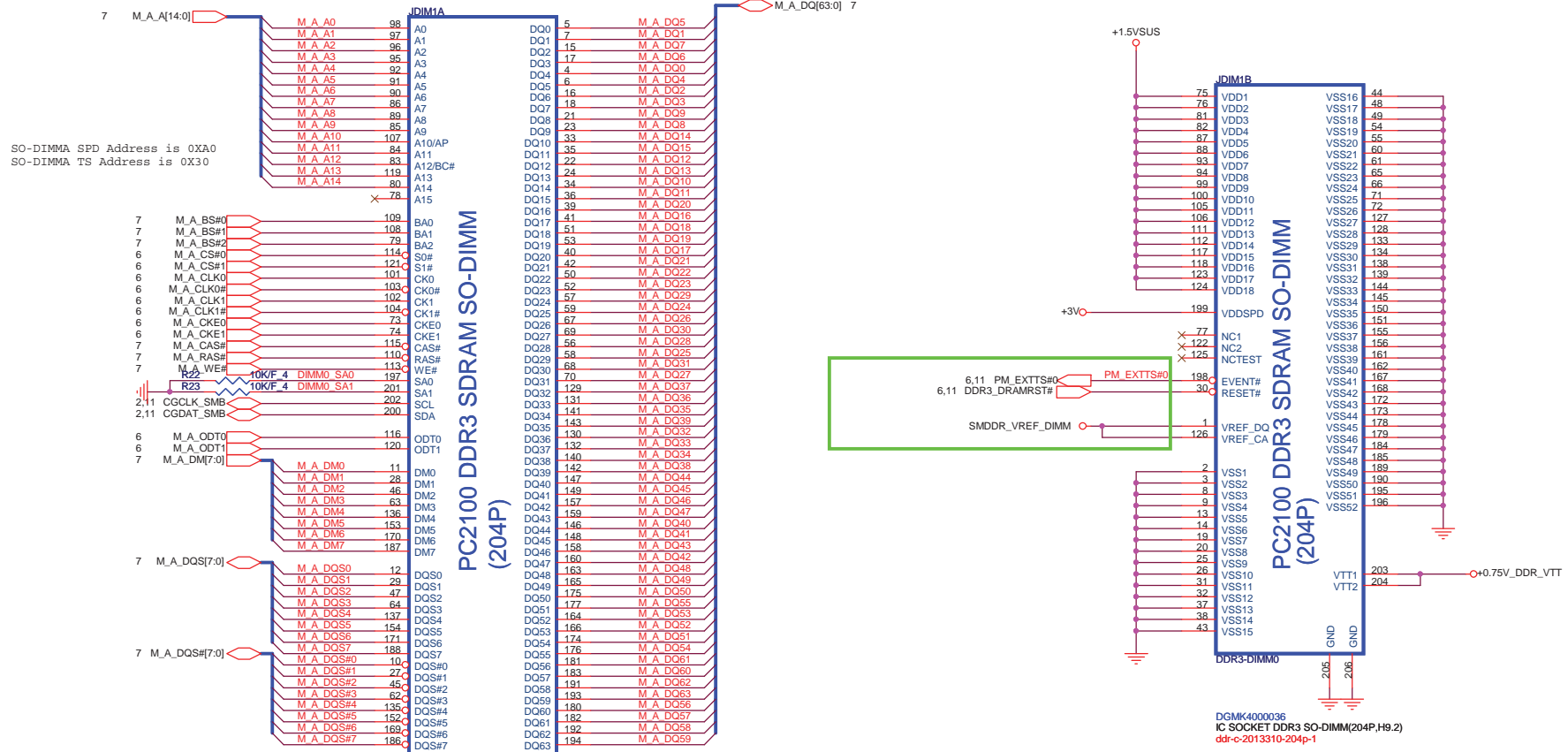


Modify to U40#AF1 power to +1.05V\_MPLL as CRB change

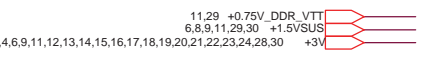
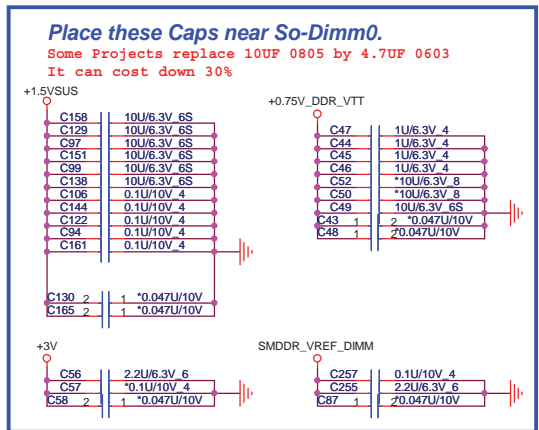
**PROJECT :AX3**  
Quanta Computer Inc.

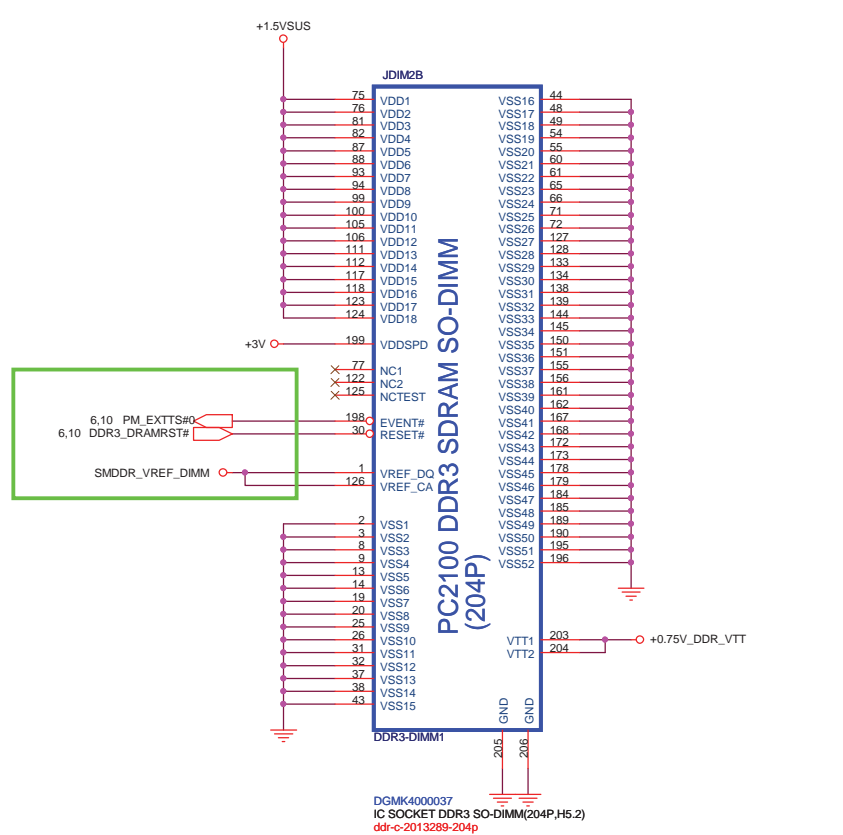
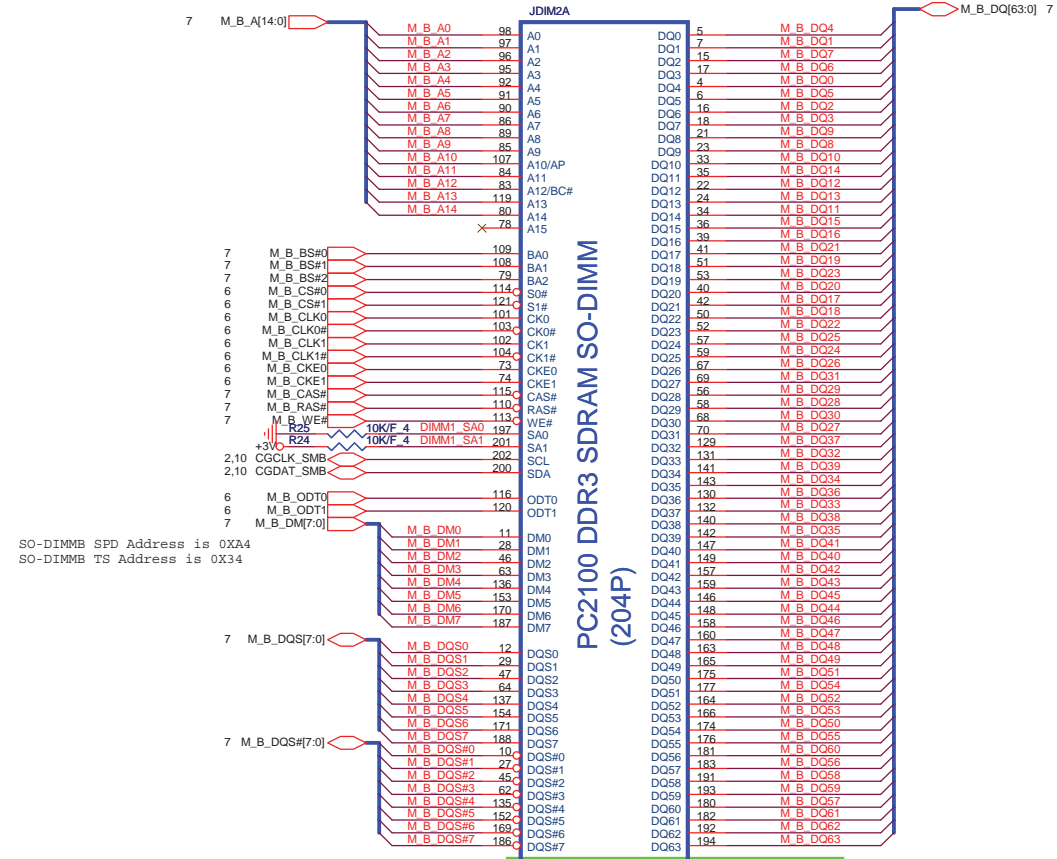
**NBS5**

Size Custom	Document Number <b>Quanta Power 5/5</b>	Rev 1A
Date: Friday, November 27, 2009		Sheet 9 of 30

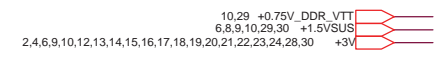
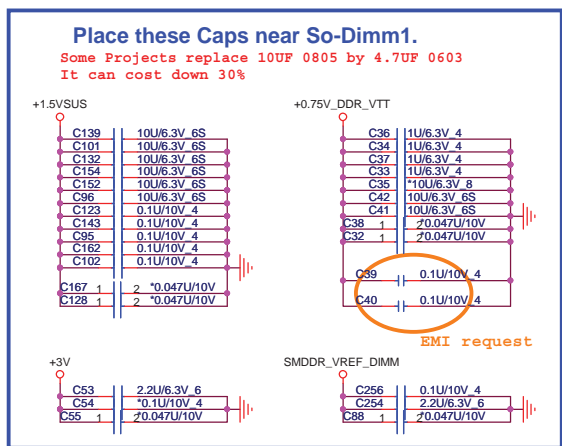
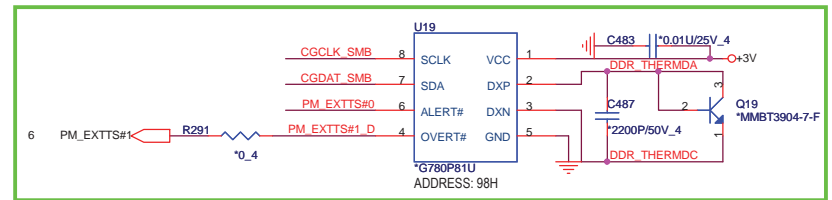


DDR3-DIMM0  
 DGMK4000036  
 IC SOCKET DDR3 SO-DIMM(204P,H9.2)  
 ddr-c-2013310-204p-1  
**Reverse type change to Standard type.**

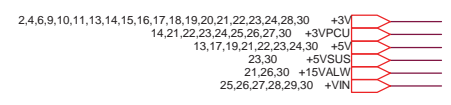
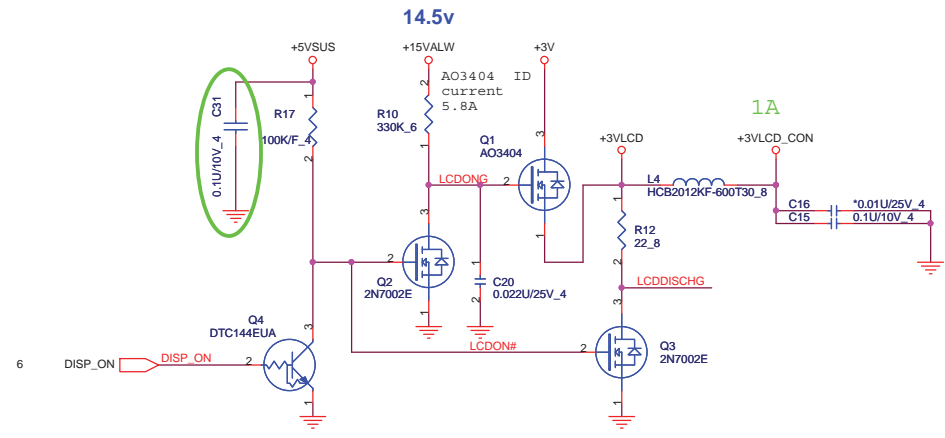
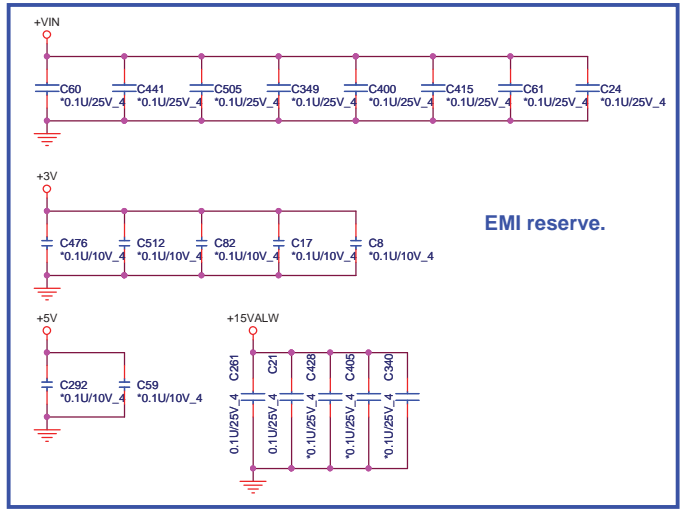
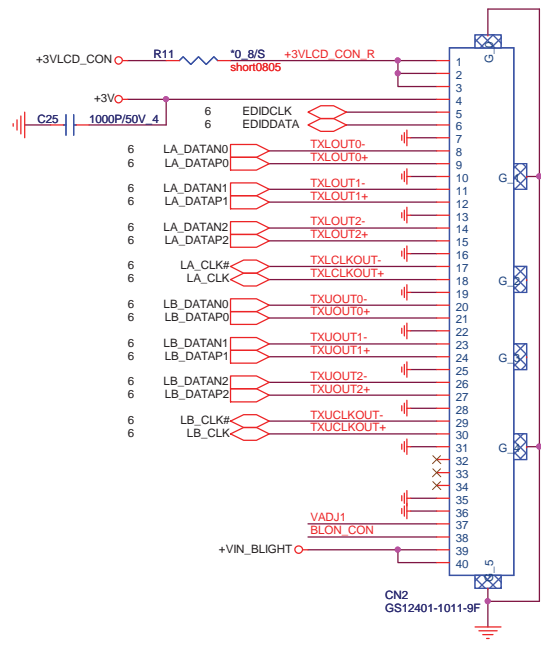
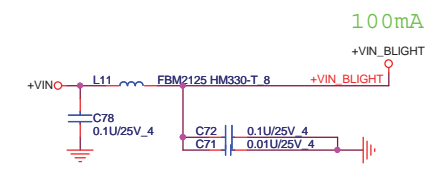
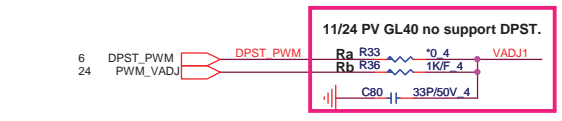
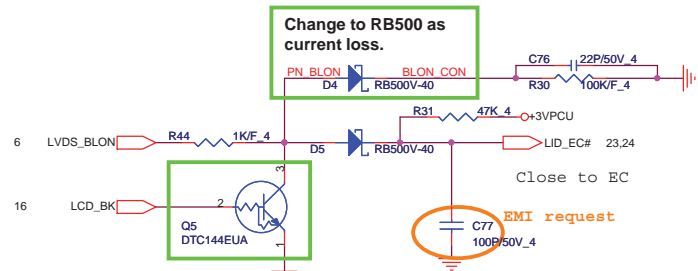




DDR3-DIMM1  
 DGMK4000037  
 IC SOCKET DDR3 SO-DIMM(204P,H5.2)  
 ddr-c-2013289-204p  
**Reverse type change to Standard type.**



# LID Switch

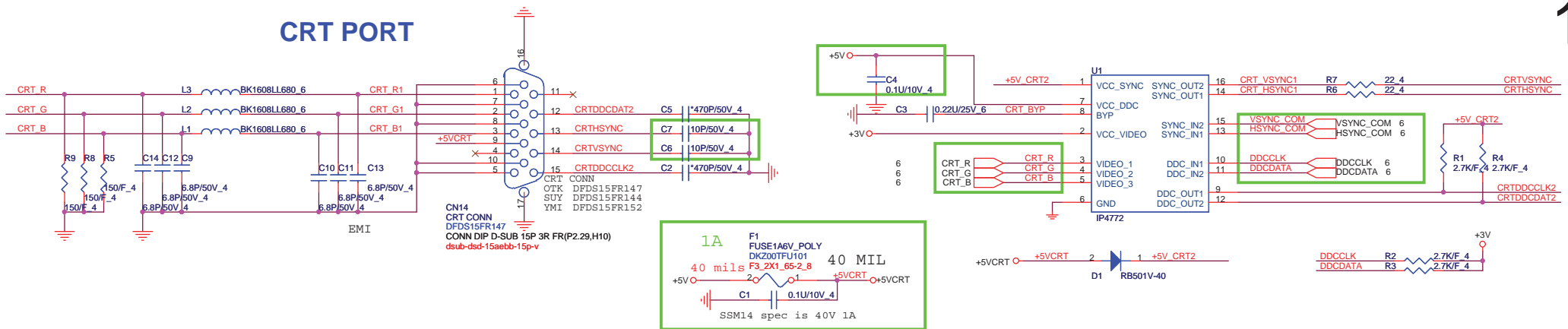


**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom Document Number **LCD CONN/LID** Rev 1A

Date: Friday, November 27, 2009 Sheet 12 of 30

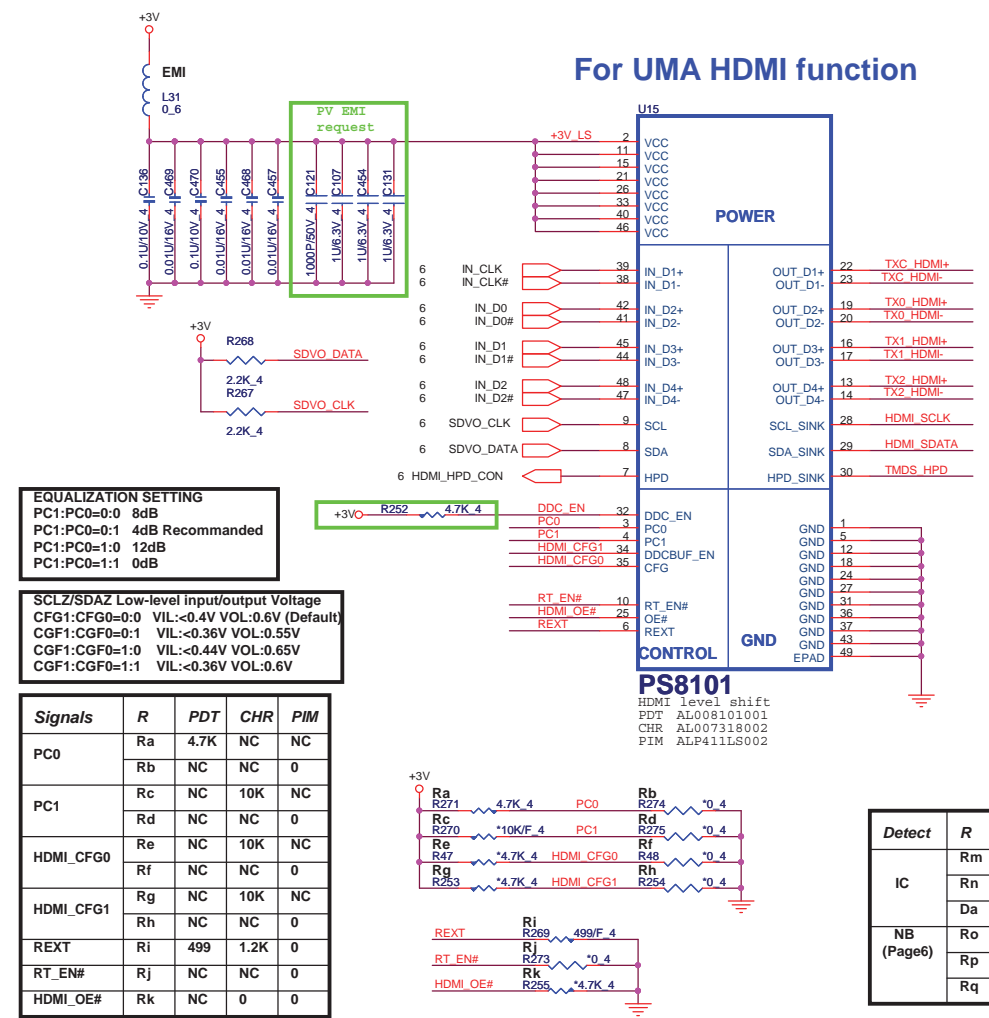
CRT PORT



For UMA HDMI function

9/16 : PIM: need use ALP411LS000 or ALP411LS004 for capella  
CHR : need Na R182, add R1027 for capella

Vendor: PDT P/N:AL008101001  
Vendor: CHR P/N:AL007318002  
Vendor: PIM P/N:ALP411LS002



**EQUALIZATION SETTING**

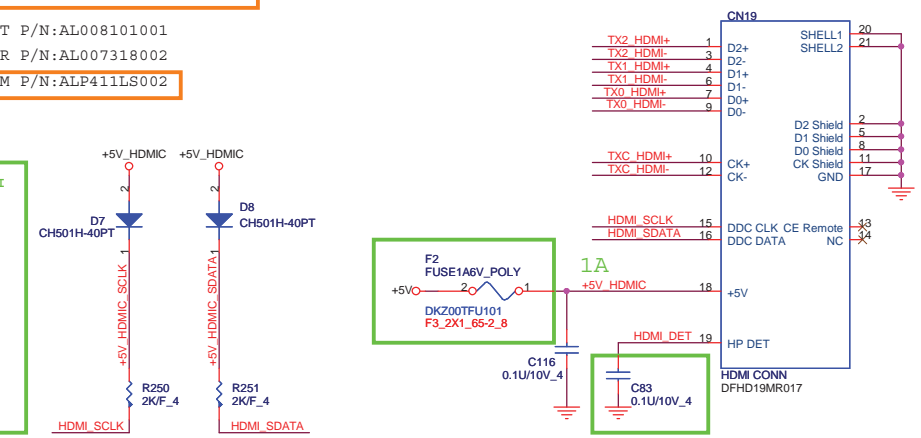
PC1:PC0=0:0	8dB
PC1:PC0=0:1	4dB Recommended
PC1:PC0=1:0	12dB
PC1:PC0=1:1	0dB

**SCLZ/SDAZ Low-level input/output Voltage**

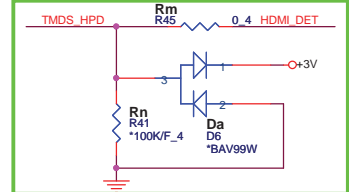
CFG1:CFG0=0:0	VIL:<-0.4V VOL:0.6V (Default)
CGF1:CGF0=0:1	VIL:<-0.36V VOL:0.55V
CGF1:CGF0=1:0	VIL:<-0.44V VOL:0.65V
CGF1:CGF0=1:1	VIL:<-0.36V VOL:0.6V

Signals	R	PDT	CHR	PIM
PC0	Ra	4.7K	NC	NC
	Rb	NC	NC	0
PC1	Rc	NC	10K	NC
	Rd	NC	NC	0
HDMI_CFG0	Re	NC	10K	NC
	Rf	NC	NC	0
HDMI_CFG1	Rg	NC	10K	NC
	Rh	NC	NC	0
REXT	Ri	499	1.2K	0
RT_EN#	Rj	NC	NC	0
HDMI_OE#	Rk	NC	0	0

Detect	R	PDT	CHR	PIM
IC	Rm	0	20K	0
	Rn	NC	47K	NC
NB (Page6)	Ro	NC	20K	20K
	Rp	NC	7.5K	7.5K
Rq	0	0	0	0

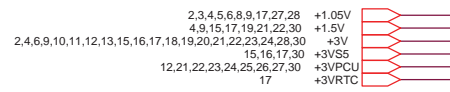
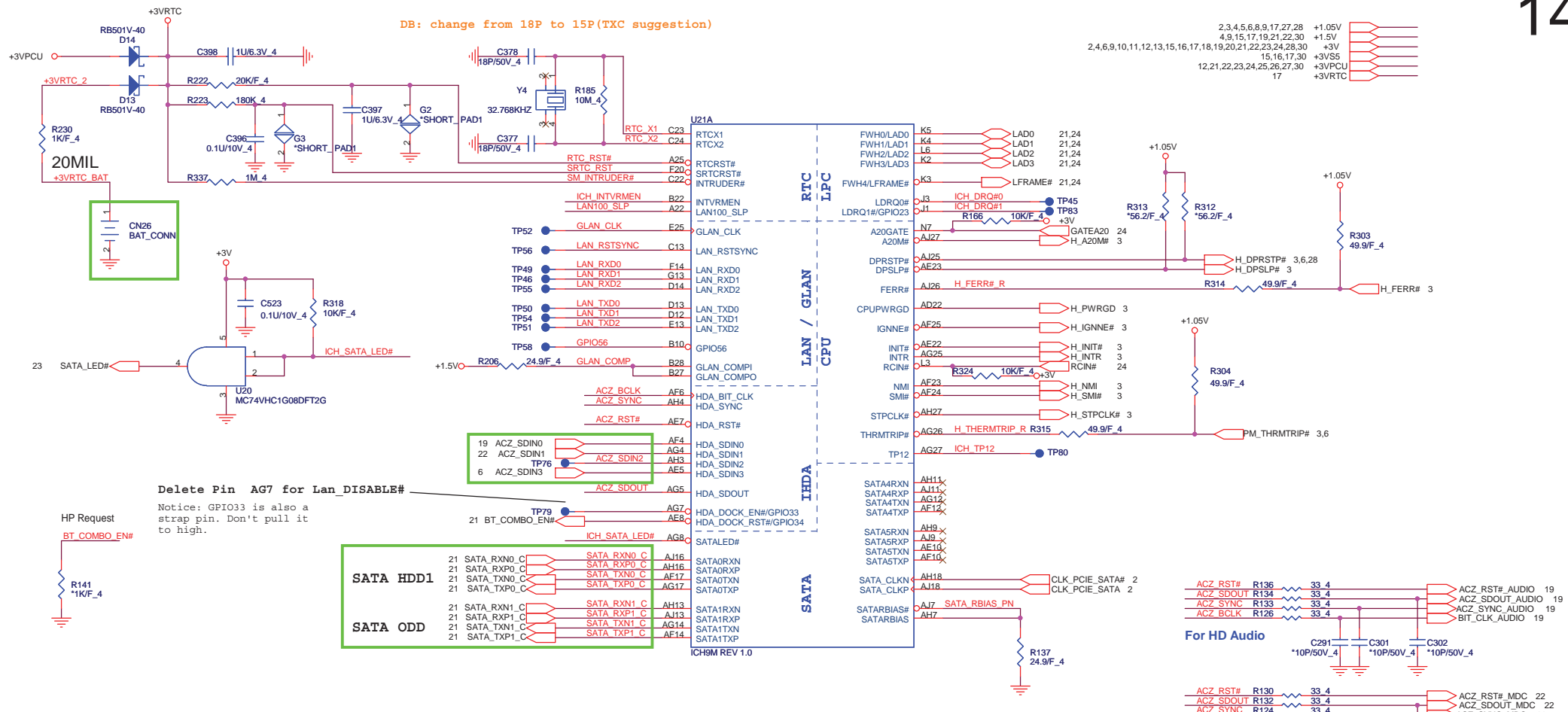


TXC HDMI+	R260	*100F_4	TXC HDMI-
TX0 HDMI+	R63	*100F_4	TX0 HDMI-
TX1 HDMI+	R264	*100F_4	TX1 HDMI-
TX2 HDMI+	R266	*100F_4	TX2 HDMI-



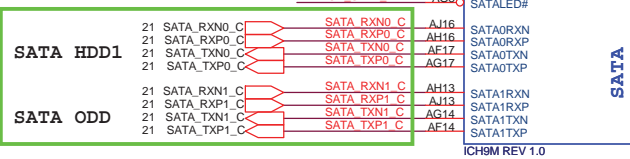
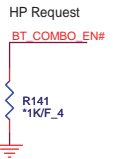
2,4,6,9,10,11,12,14,15,16,17,18,19,20,21,22,23,24,28,30 +3V  
12,17,19,21,22,23,24,30 +5V

DB: change from 18P to 15P(TXC suggestion)

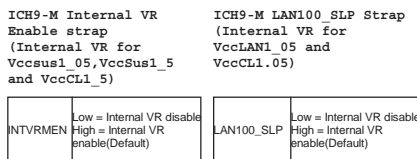


Delete Pin AG7 for Lan\_DISABLE#

Notice: GPIO33 is also a strap pin. Don't pull it to high.



SB Strap



**XOR Chain Entrance Strap**

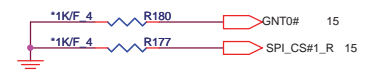
ICH_TP3	HDA_SDOULT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal opration(Default)
1	1	Set PCIe port config bit 1

**ICH9 Boot BIOS select**

STRAP	PCI_GNT0#	SPI_CS#1	(default)
SPI	0	1	
PCI	1	0	
LPC	1	1	

**No Reboot Strap**

ACZ_SPKR	Low: Default Hi: No reboot
----------	-------------------------------

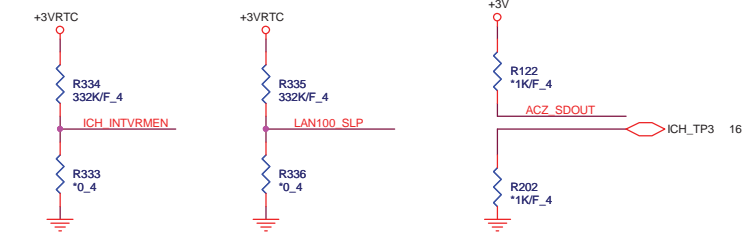
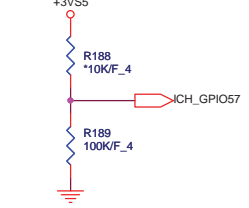


**A16 swap override strap**

PCI_GNT#3	Low = A16 swap override enabled Hi = Default
-----------	---

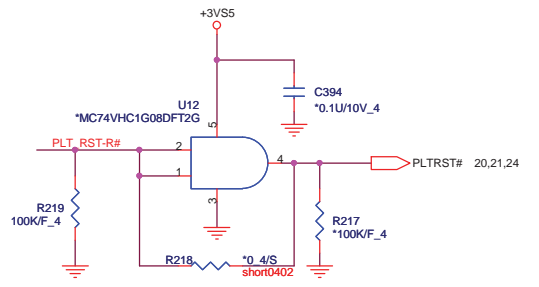
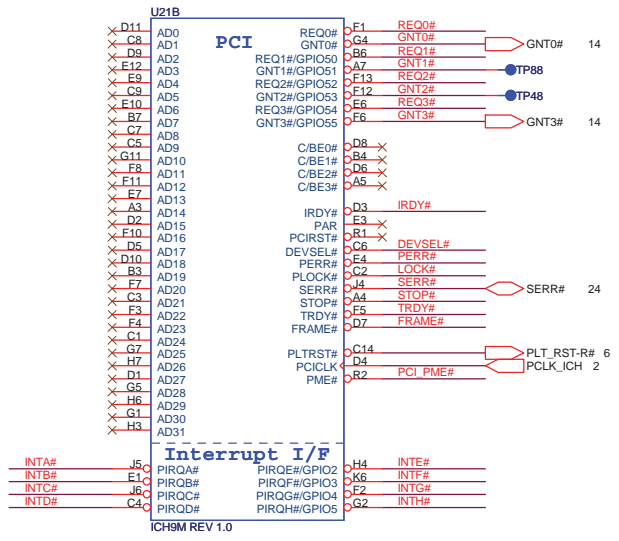
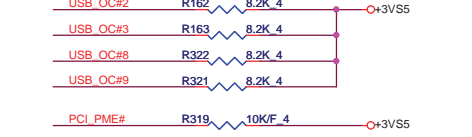
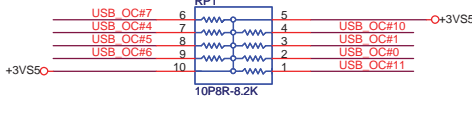
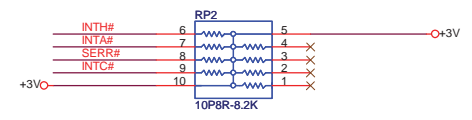
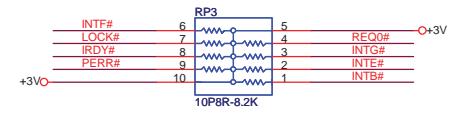
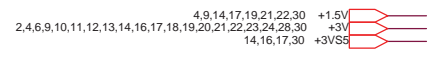
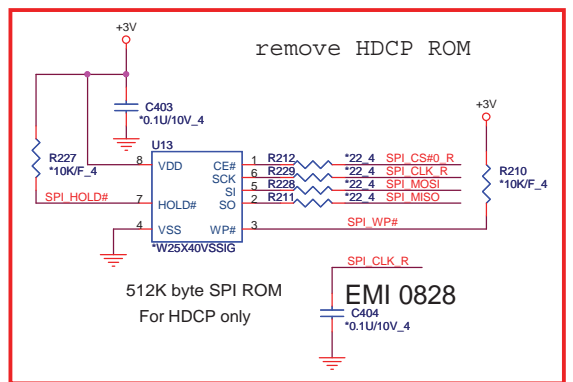
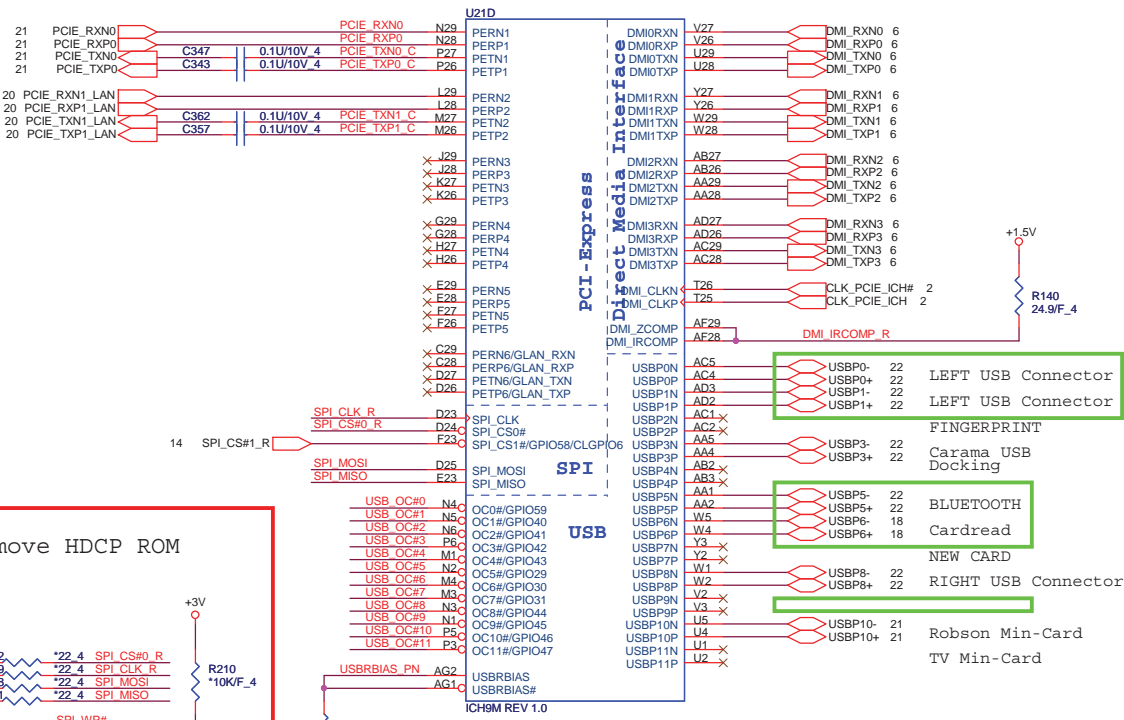
**TPM physical presence**

ICH_GPIO57	Low: Default
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MINI CARD PCI-E (WLAN)

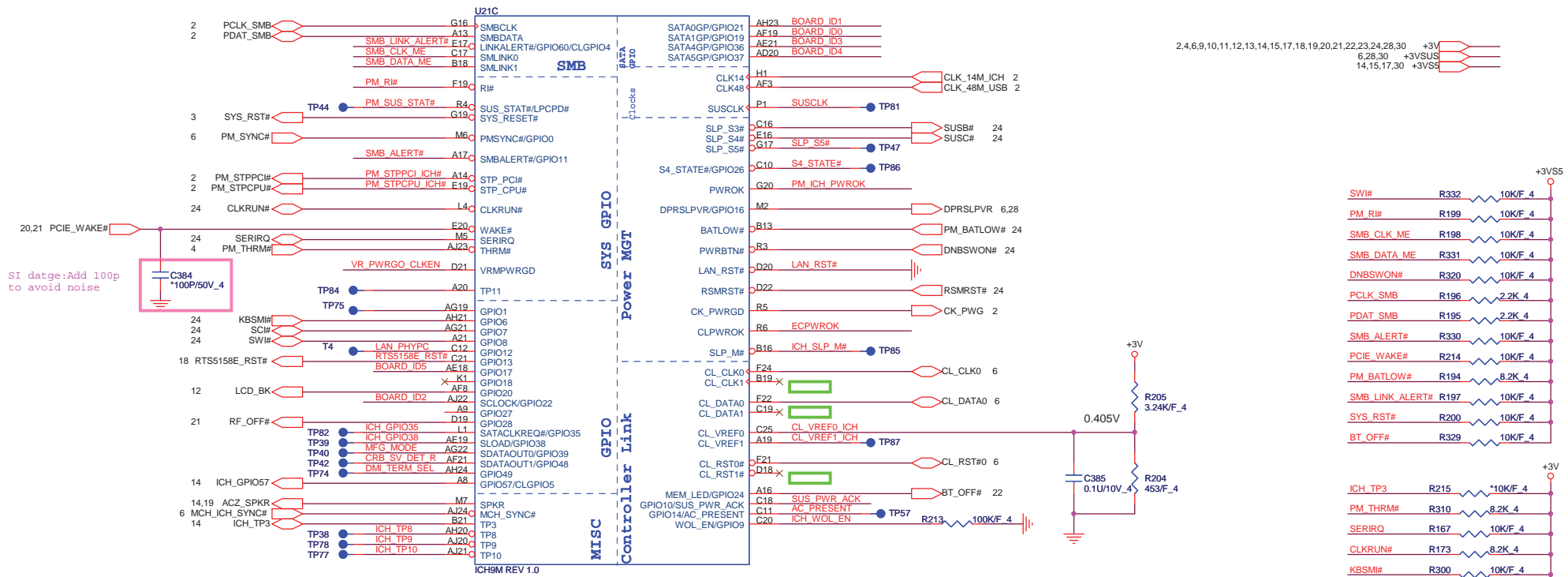
PCI-E-LAN



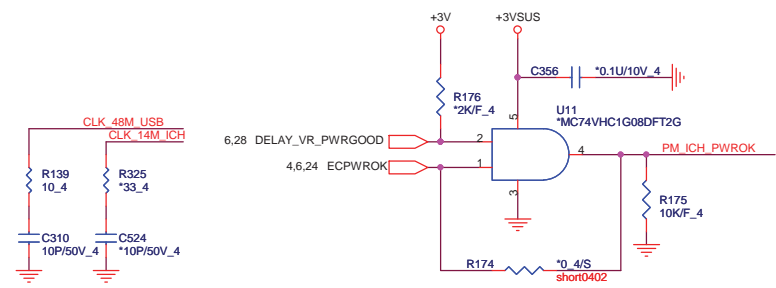
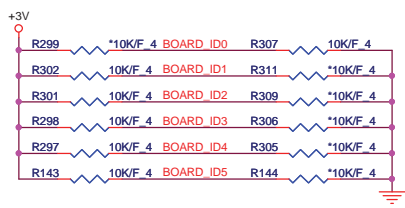
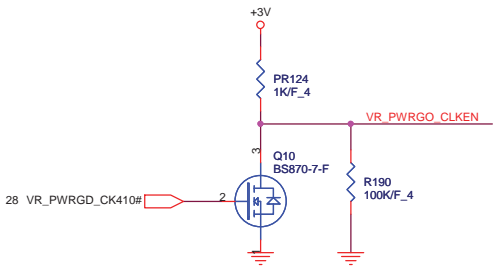
**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom Document Number **IC9M-M DMI/PCIe/USB 2/4** Rev 1A

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SI datge: Add 100p to avoid noise



Board ID	ID0	ID1	ID2	ID3	ID4	ID5
	GPIO19	GPIO21	GPIO22	GPIO36	GPIO37	GPIO17
UMA/DIS	0=UMA 1=Dis.					
CardReader		0=No 1=Yes				
HDMI			0=No 1=Yes			
Bluetooth				0=No 1=Yes		
Camera					0=No 1=Yes	
Modem						0=No 1=Yes

AX3 MB P/N	ID0	ID1	ID2	ID3	ID4	ID5
31AX6MB0000	0	1	1	1	1	1
31AX6MB0010	0	1	1	1	1	1
31AX6MB0020	0	1	1	1	1	1

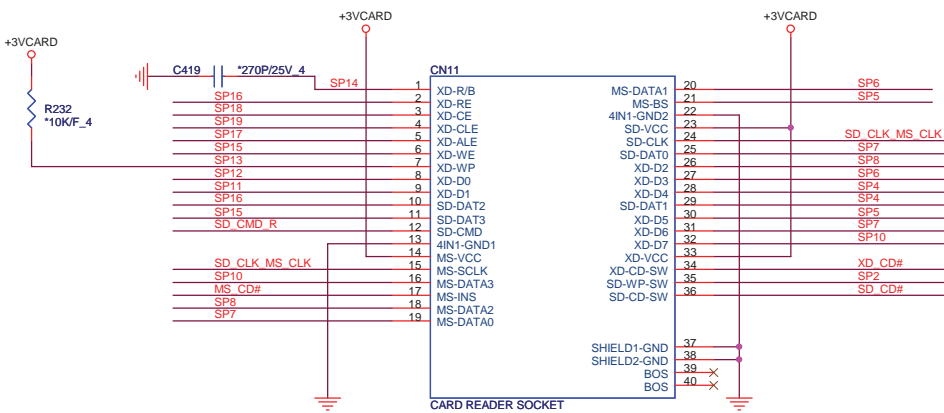
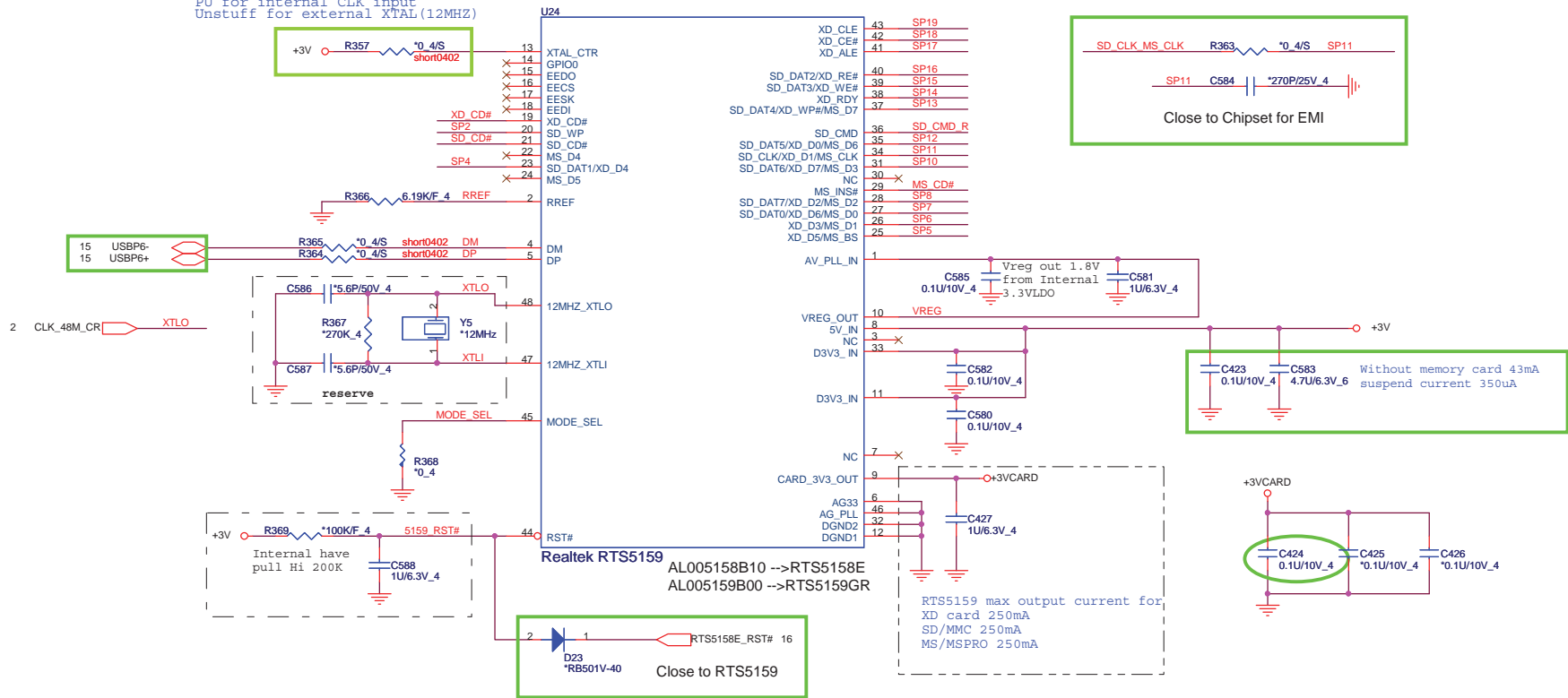
**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom Document Number **IC9-M GPIO/Board ID 3/4** Rev 1A

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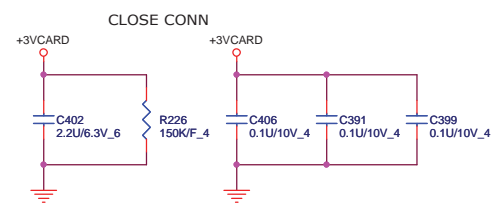


PU for internal CLK input  
Unstuff for external XTAL(12MHZ)



**5 IN1 CARD-READER (PUSH-PUSH)**

Support SD/SD PRO/MMC/MS/MS PRO/xD Cards



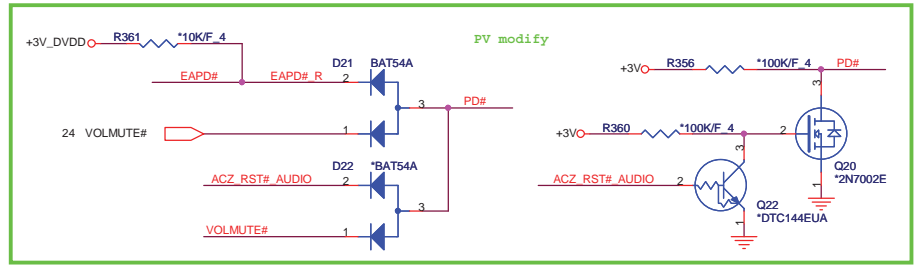
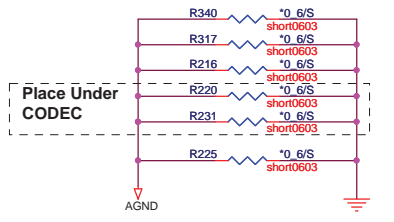
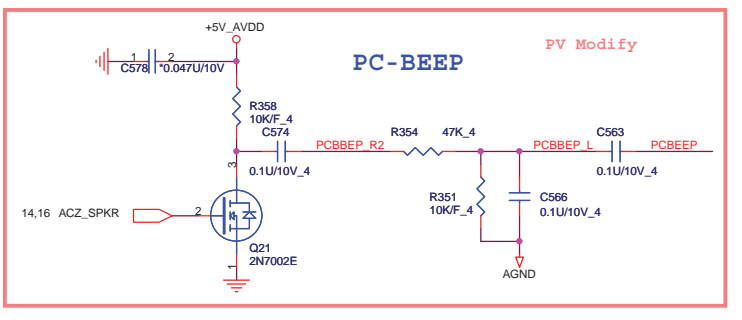
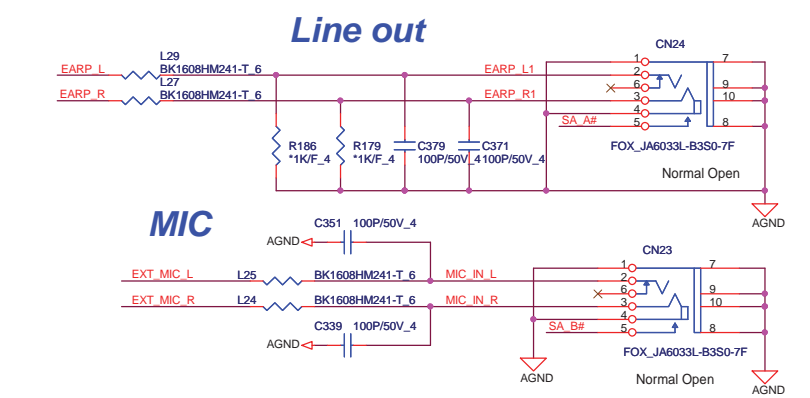
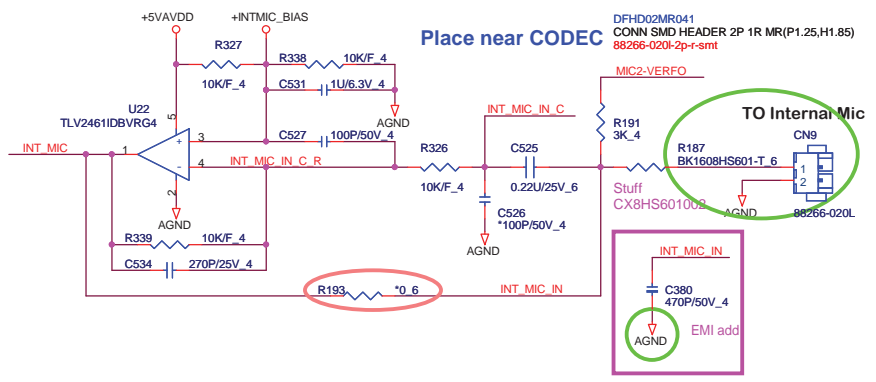
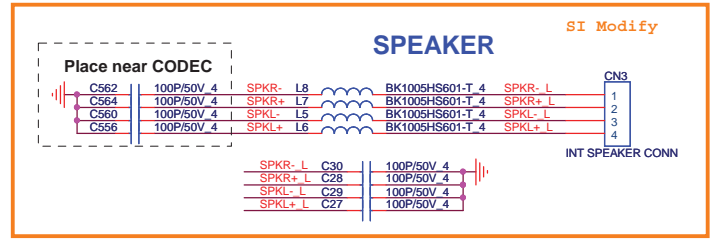
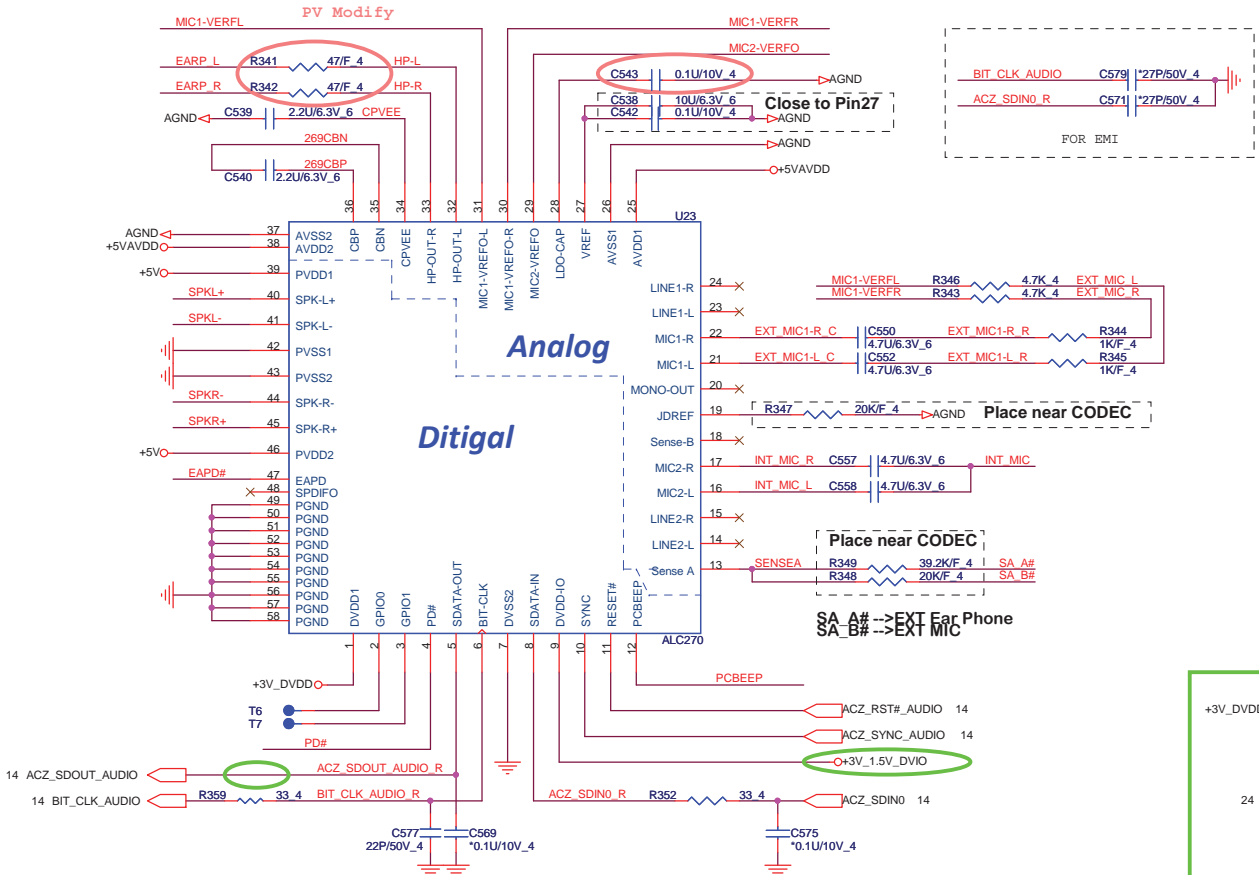
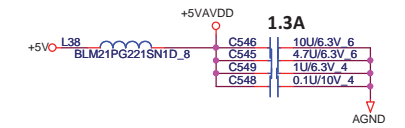
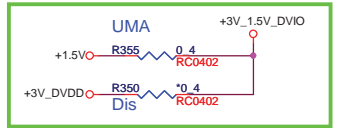
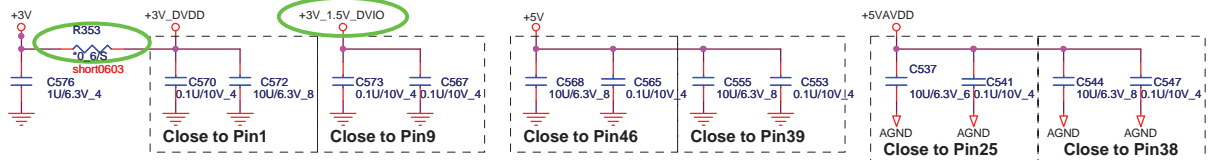
**Note:**

SP0	SD/MMC	MS	XD
SP1	SD_WP		XD_CD#
SP2	SD_WP		
SP3	SD_CD#		
SP4	SD_DAT1		XD_D4
SP5		MS_BS	XD_D5
SP6		MS_D1	XD_D3
SP7	SD_DAT0	MS_D0	XD_D6
SP8	SD_DAT7	MS_D2	XD_D2
SP9		MS_INS#	
SP10	SD_DAT6	MS_D3	XD_D7
SP11	SD_CLK	MS_SCLK	XD_D1
SP12	SD_DAT5		XD_D0
SP13	SD_DAT4		XD_D4
SP14			XD_R/#
SP15	SD_DAT3		XD_WE#
SP16	SD_DAT2		XD_RE#
SP17			XD_ALE
SP18			XD_CE#
SP19			XD_CLE

2,4,6,9,10,11,12,13,14,15,16,17,19,20,21,22,23,24,28,30 +3V

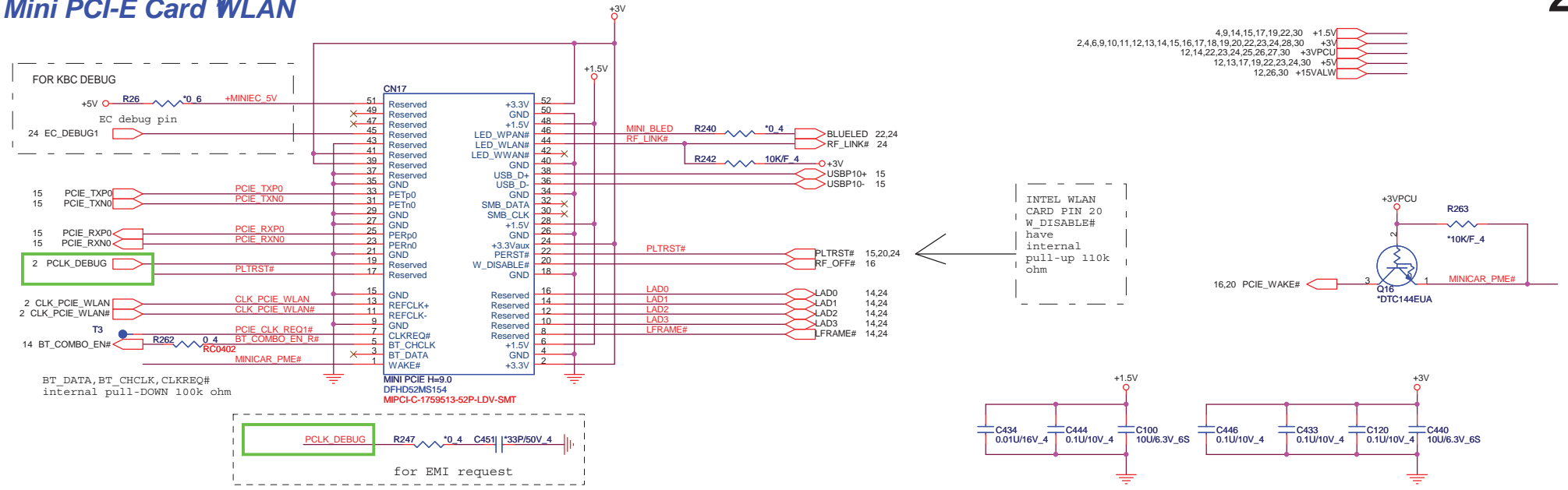
**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom	Document Number <b>RTS5159/CR Socket</b>	Rev 1A
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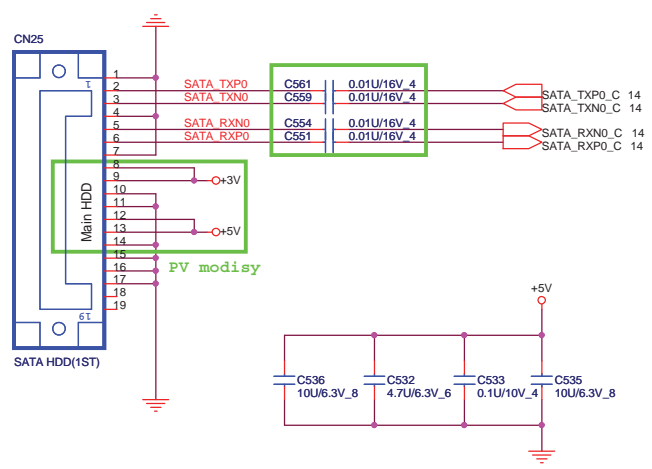




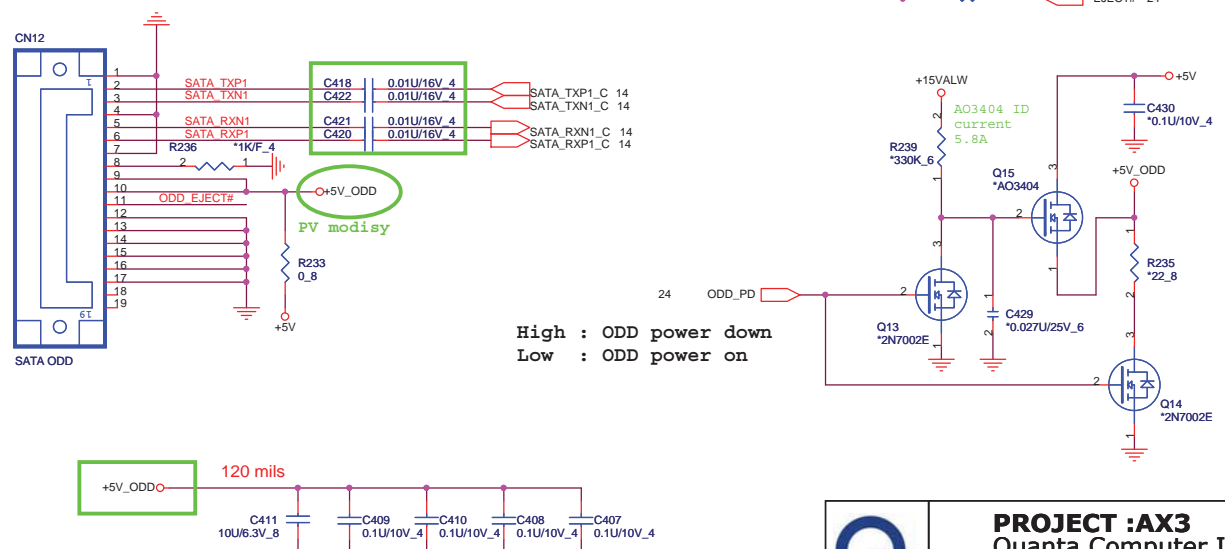
### Mini PCI-E Card WLAN



### SATA HDD CONNECTOR



### SATA ODD CONNECTOR

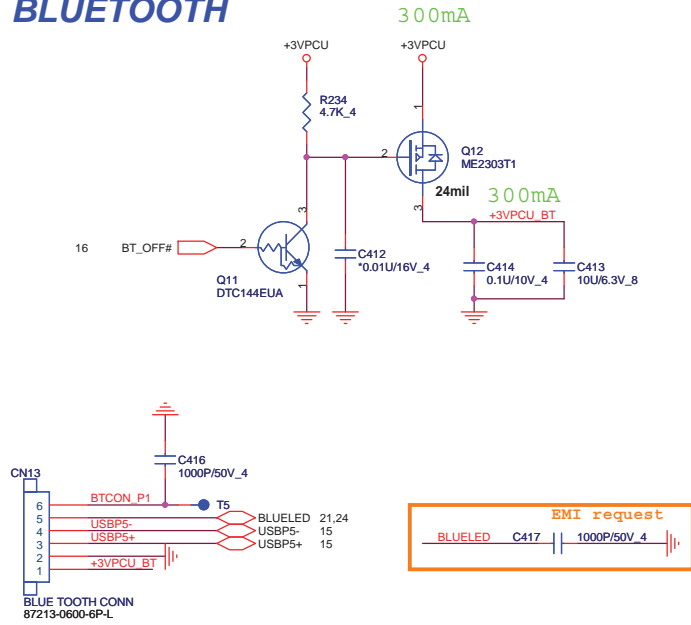


**PROJECT :AX3**  
 Quanta Computer Inc.

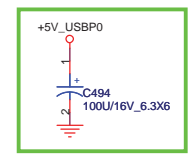
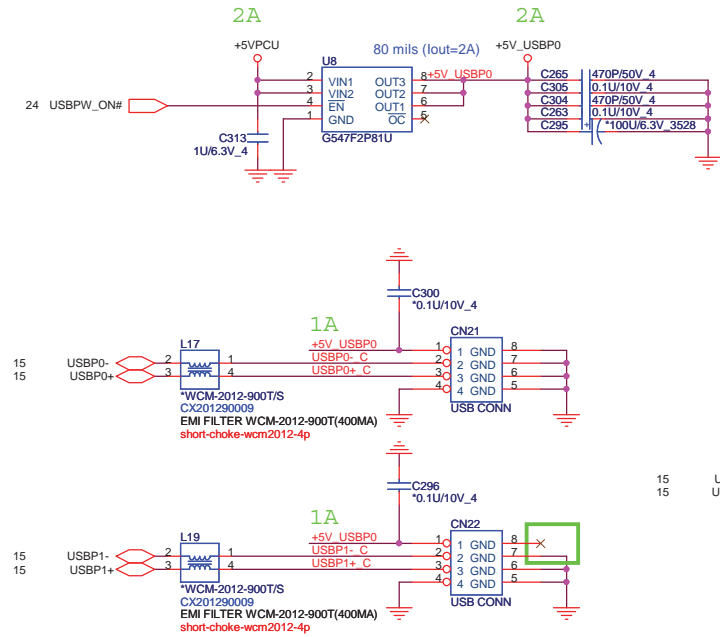
**NBS5**

Size Custom	Document Number <b>HDD/ODD/FAN</b>	Rev 1A
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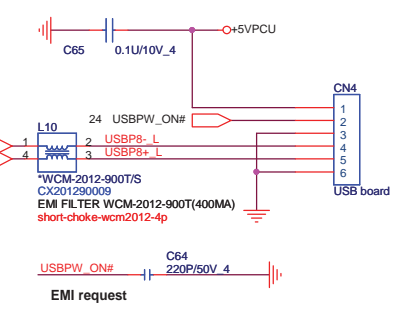
### BLUETOOTH



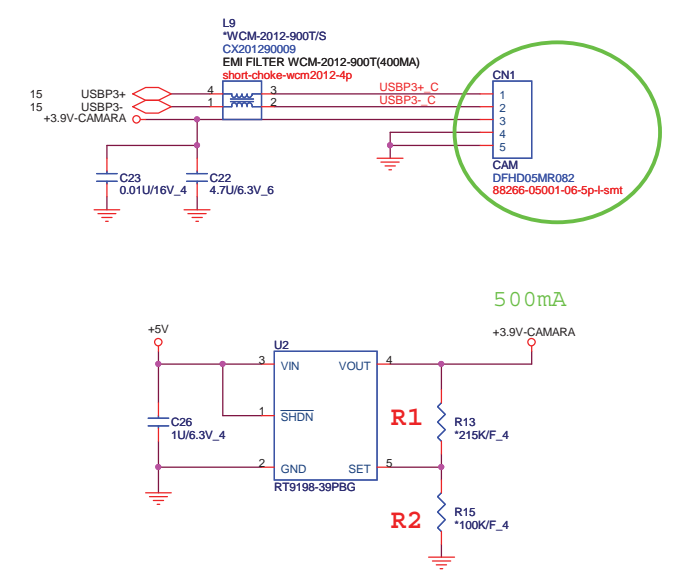
### LEFT SIDE USBX1



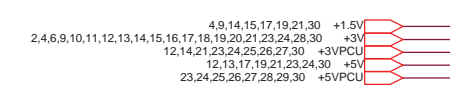
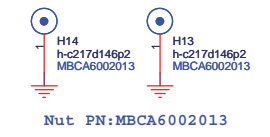
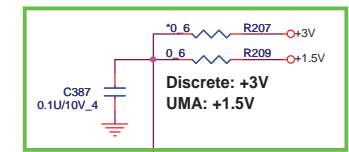
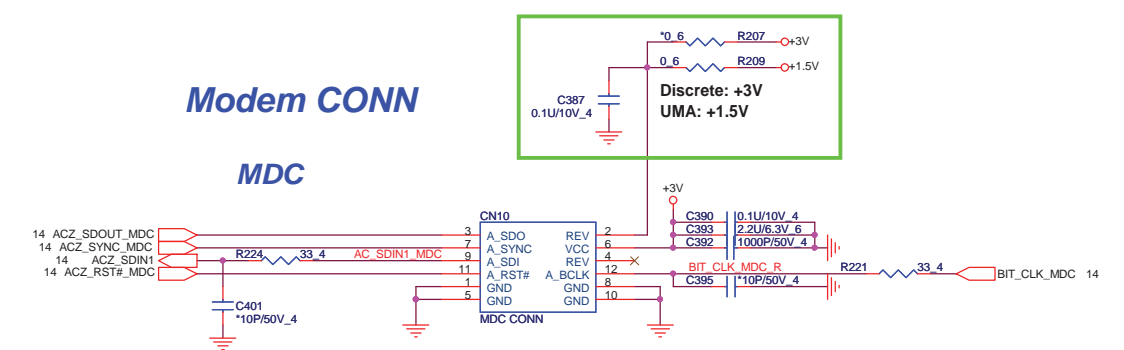
### Right SIDE USBX1



### CAMERA



### Modem CONN

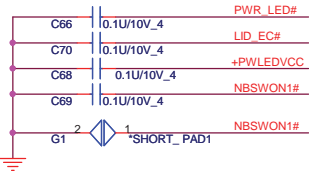
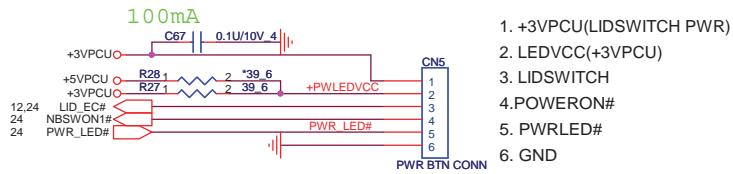


## PROJECT :AX3

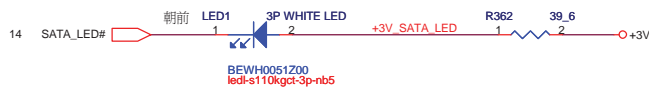
### Quanta Computer Inc.

Size Custom	Document Number <b>USB/BT/CAMERA/Modem</b>	Rev 1A
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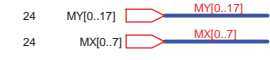
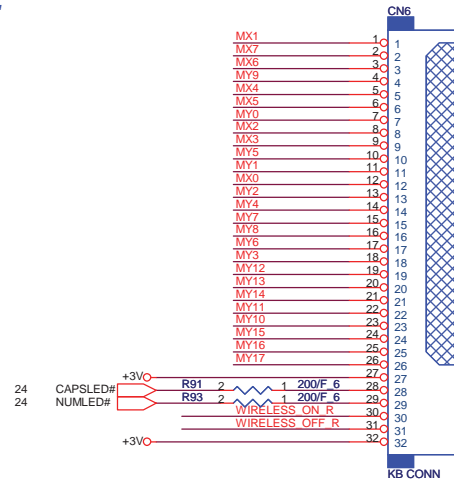
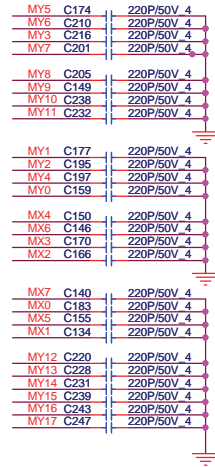
### POWER BOTTON CONNECT



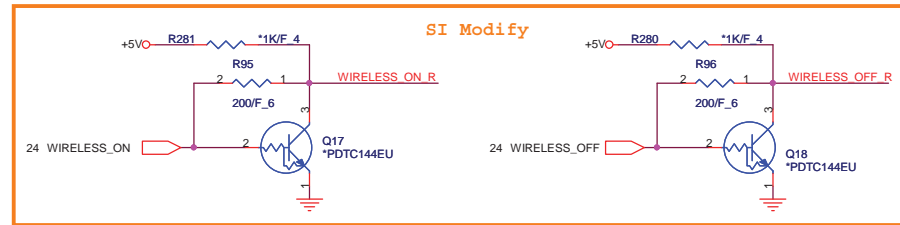
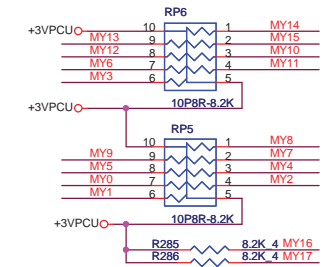
### SATA LED



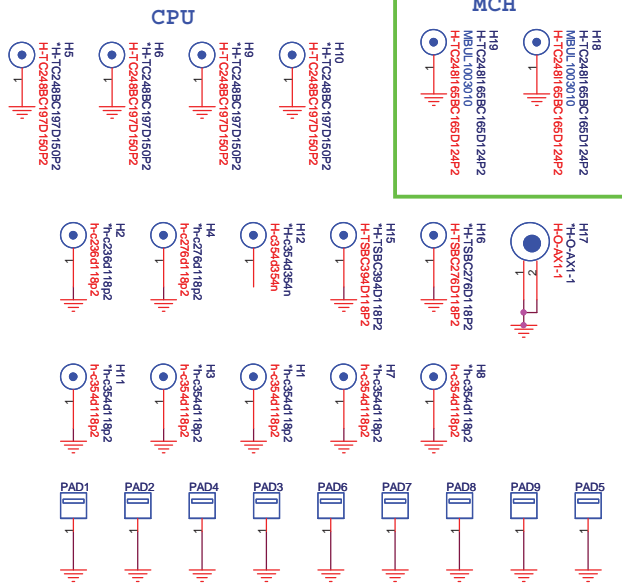
### KEYBOARD Con.



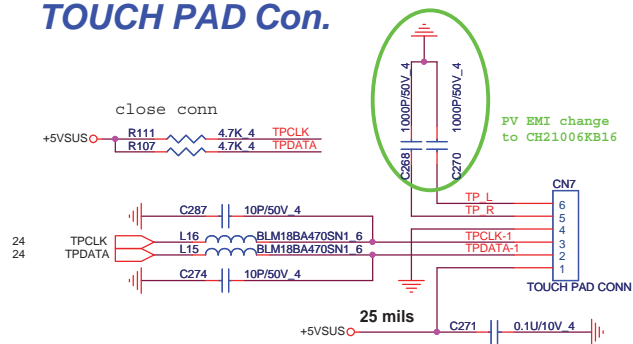
### KEYBOARD PULL-UP



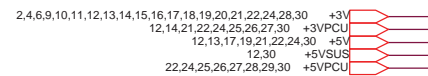
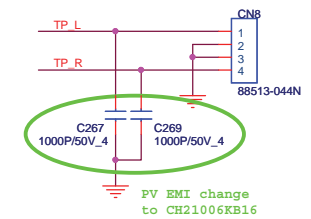
### HOLE & PAD

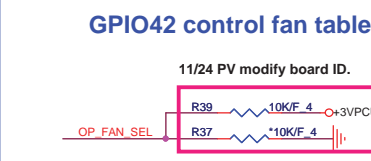
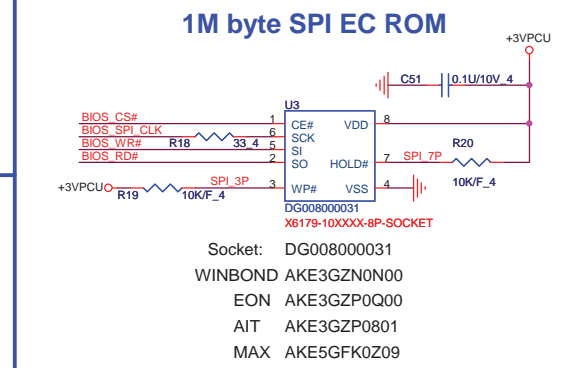
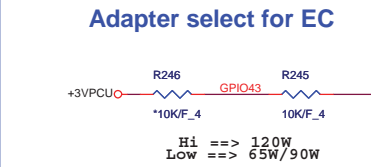
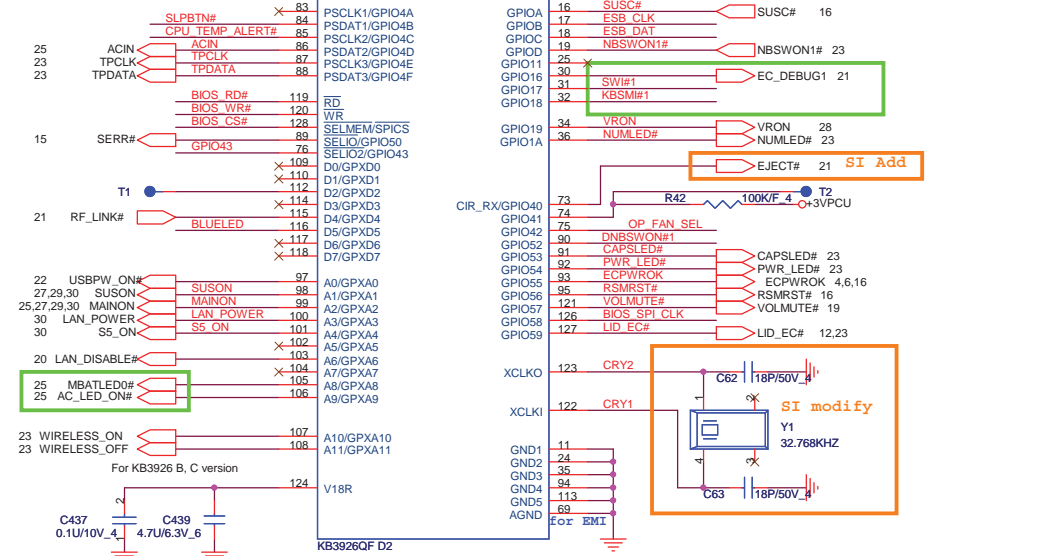
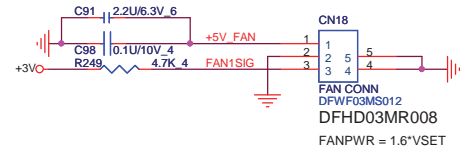
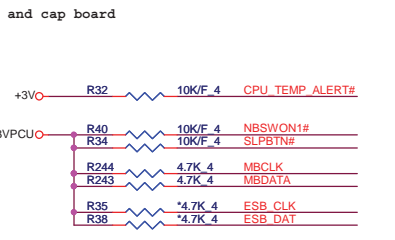
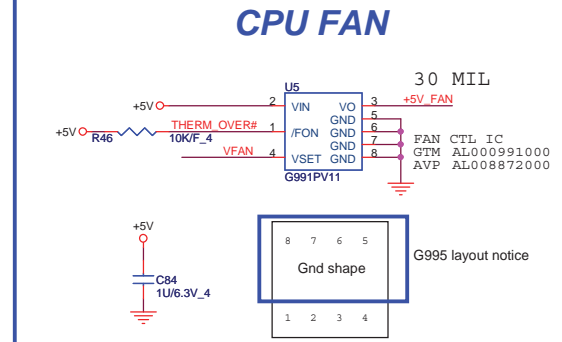
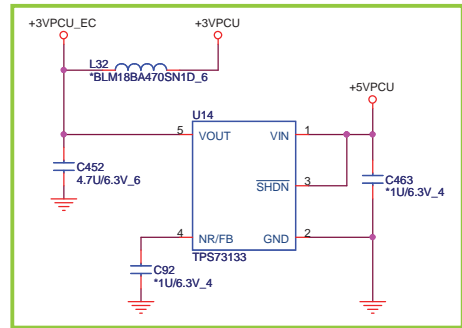
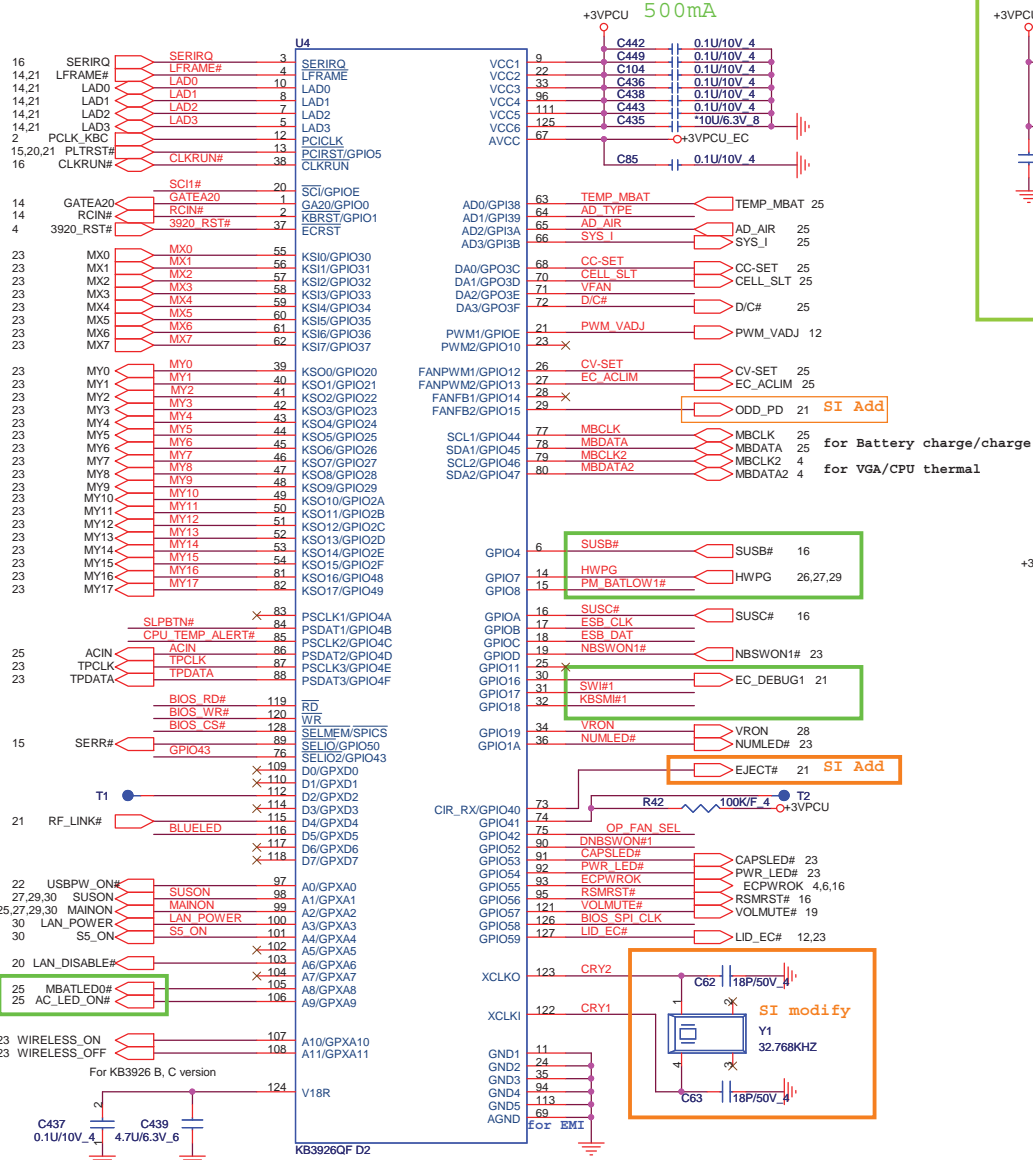


### TOUCH PAD Con.



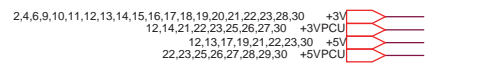
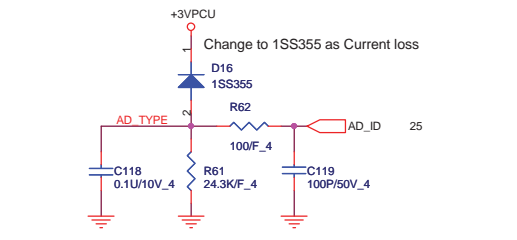
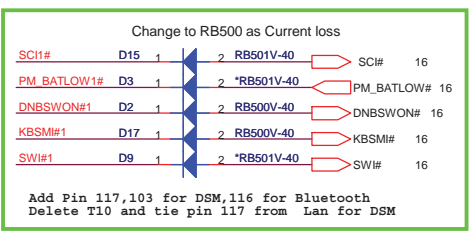
### To TOUCH PAD SW board





**GPIO42 control fan table**

Project Model	GPIO42
AX 14"	High
AX 15.6"	Low
AX 17.3"	Middle (1.5V)

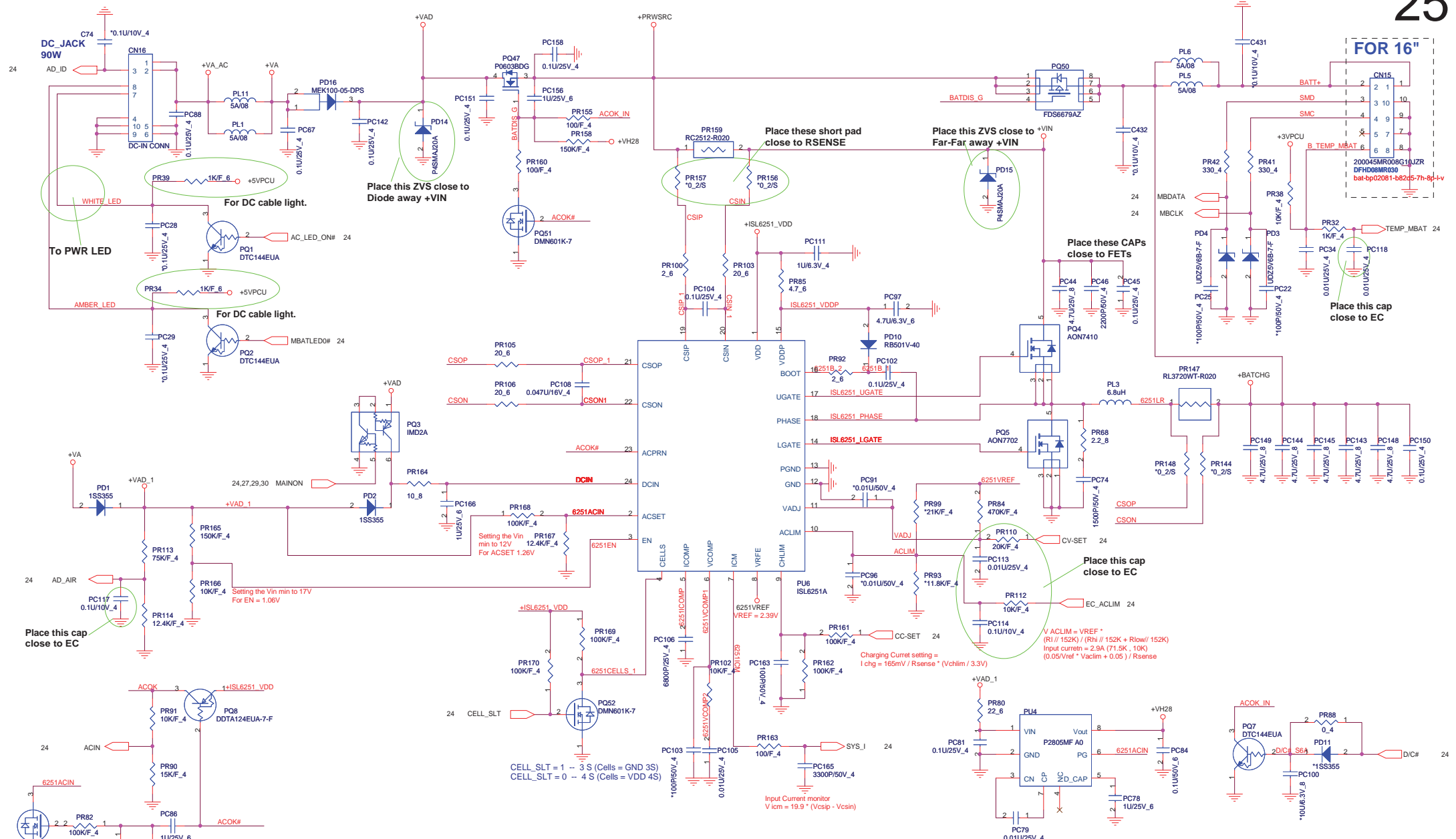


**PROJECT :AX3**  
Quanta Computer Inc.

Socket: DG008000031  
WINBOND AKE3GZNON00  
EON AKE3GZP0Q00  
AIT AKE3GZP0801  
MAX AKE5GFK0Z09

**PROJECT :AX3**  
Quanta Computer Inc.

Size Custom Document Number EC (KB3926)/ROM Rev 1A  
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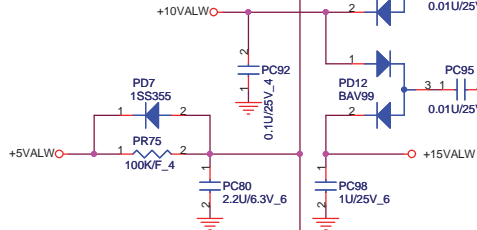
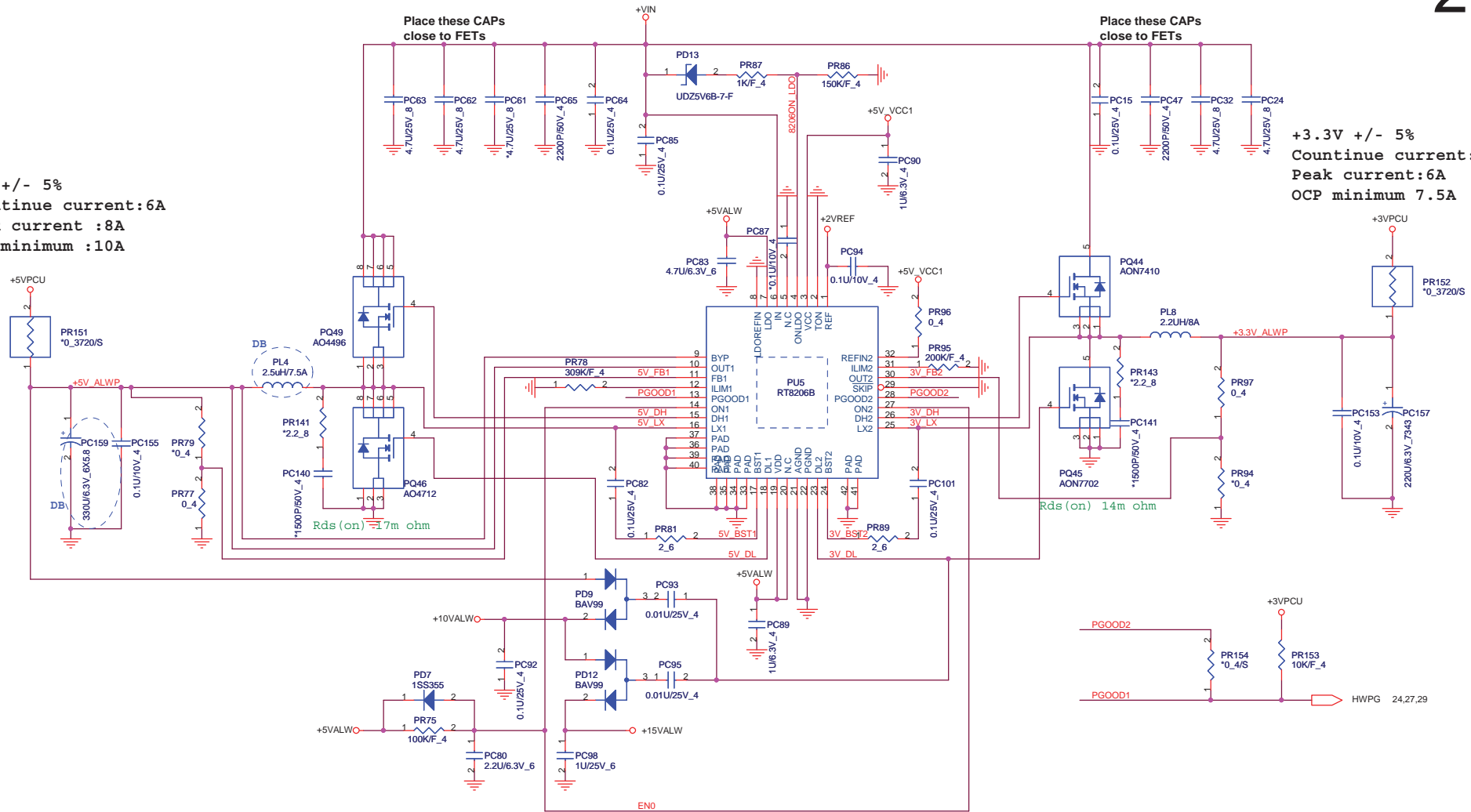
	<b>PROJECT :AX3</b>		Rev
	Quanta Computer Inc.		
Size Custom	Document Number <b>CHARGER (ISL6251)</b>		
Date: Friday, November 27, 2009	Sheet 25 of 30		

+5V +/- 5%  
 Countinue current:6A  
 Peak current :8A  
 OCP minimum :10A

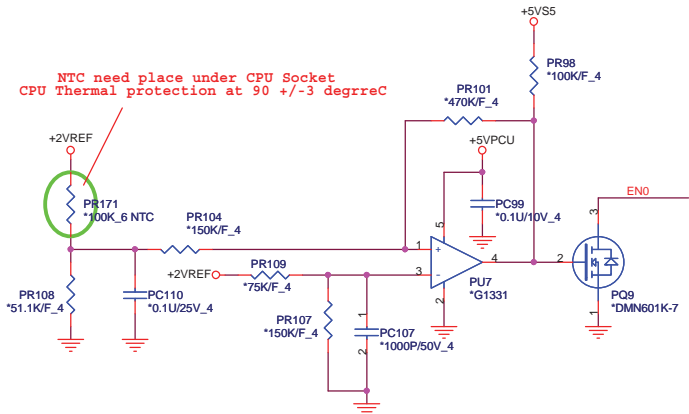
+3.3V +/- 5%  
 Countinue current:5A  
 Peak current:6A  
 OCP minimum 7.5A

Place these CAPs close to FETs

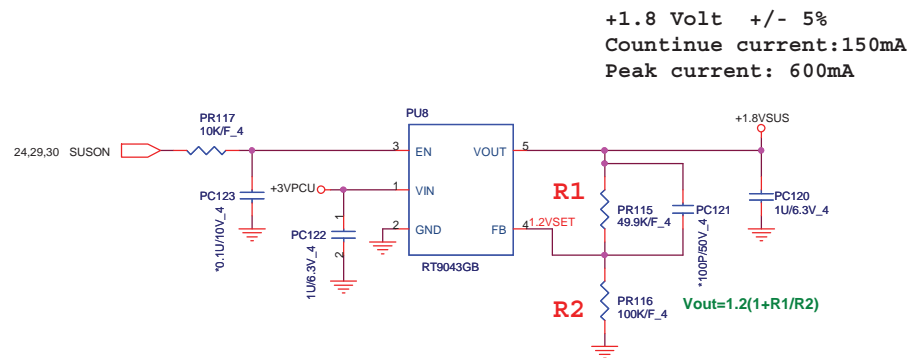
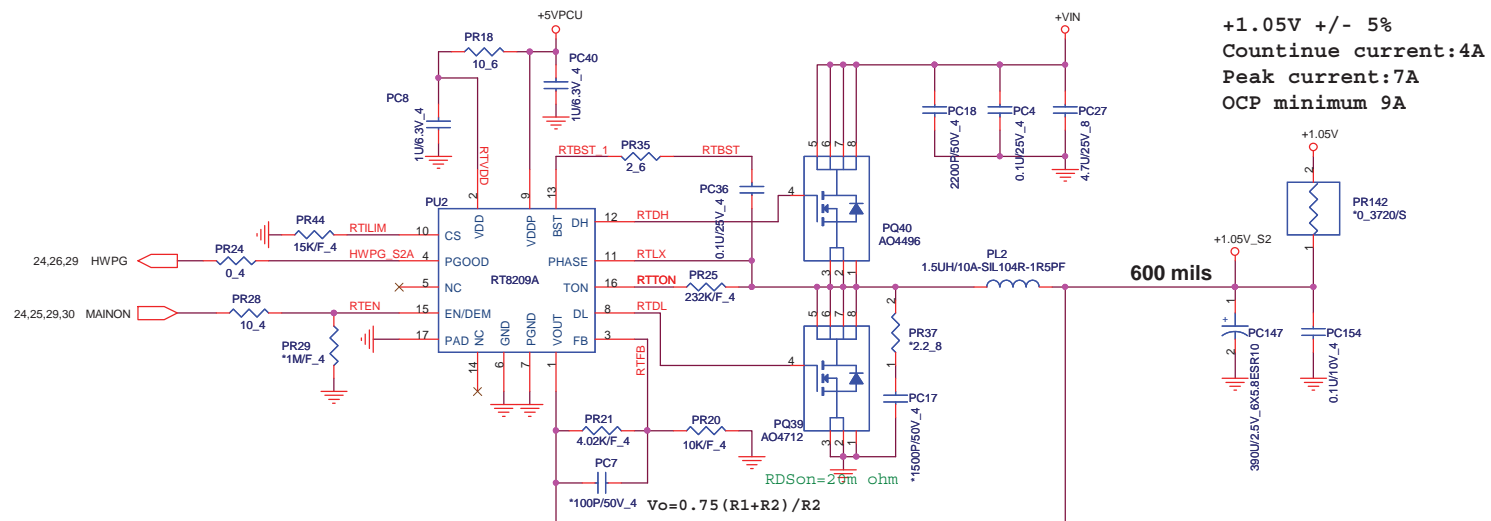
Place these CAPs close to FETs



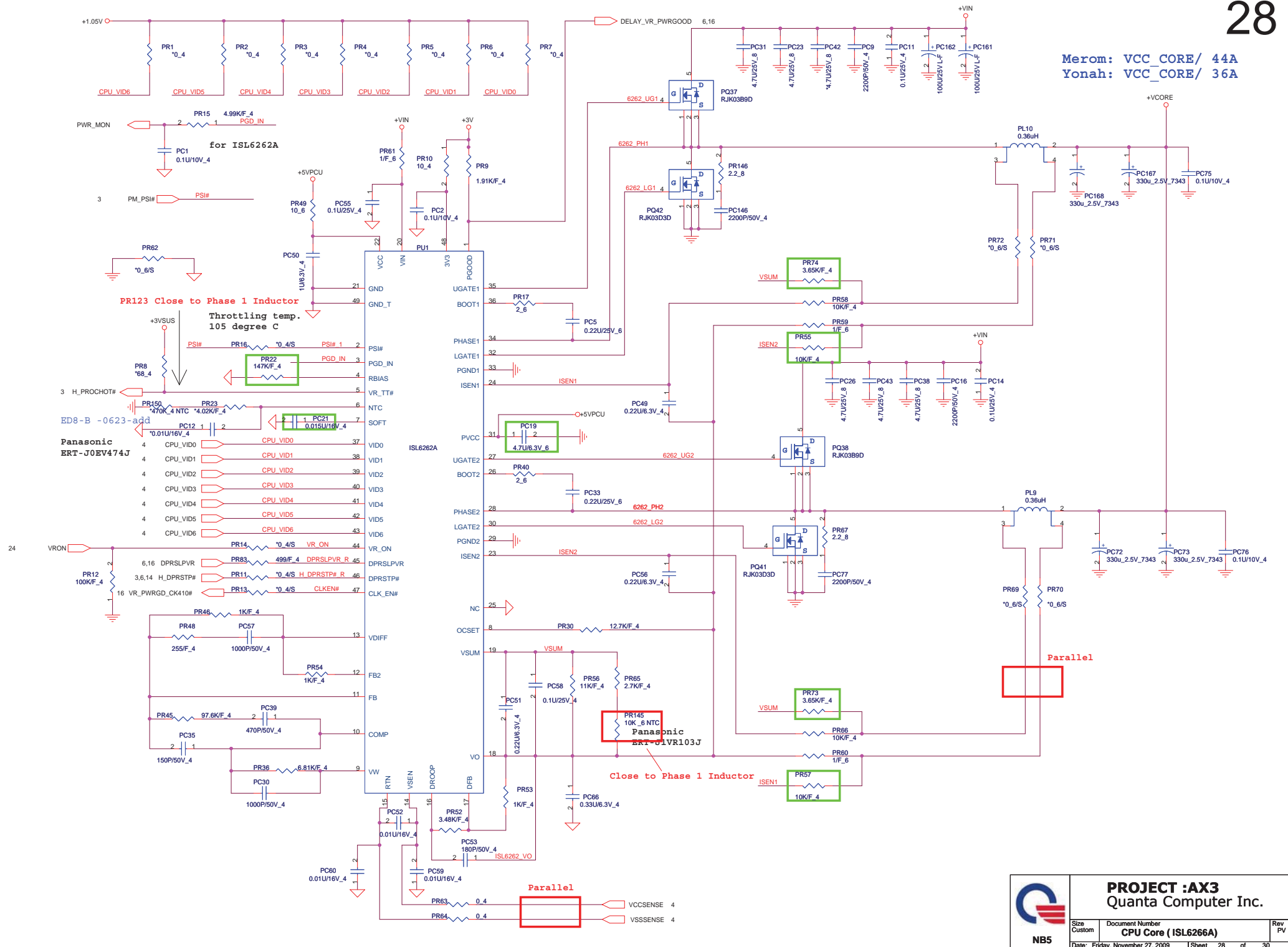
NTC need place under CPU Socket  
 CPU Thermal protection at 90 +/-3 deგრec




	<b>PROJECT :AX3</b> Quanta Computer Inc.	
	Size Custom	Document Number <b>+5V/+3V (RT8206B)</b>
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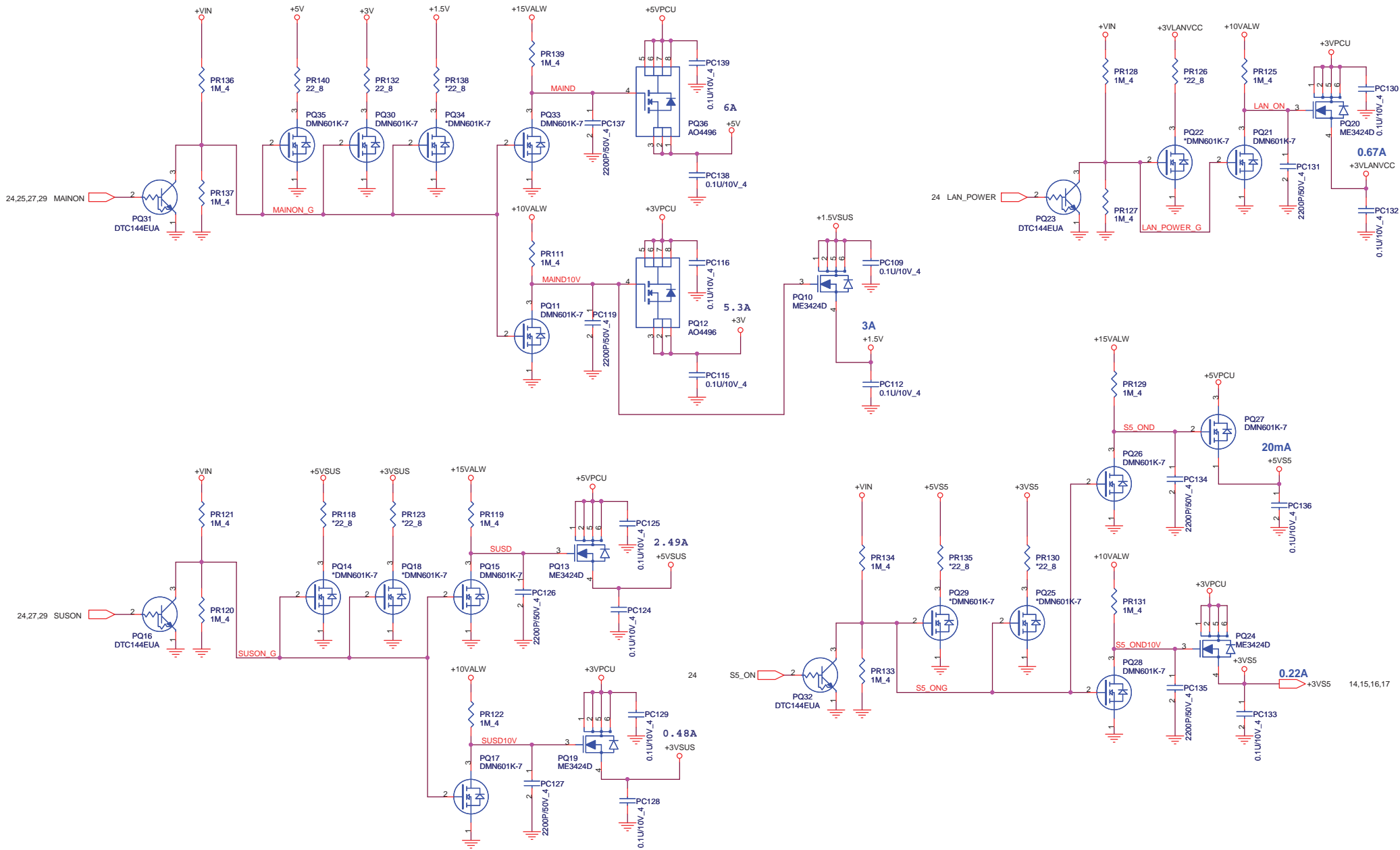



Merom: VCC\_CORE/ 44A  
Yonah: VCC\_CORE/ 36A



	<b>PROJECT :AX3</b>	
	Quanta Core Inc.	
	Size Custom	Document Number
CPU Core ( ISL6266A)		
Date: Friday, November 27, 2009		Sheet 28 of 30





 <b>NBS</b>	<b>PROJECT :AX3</b> Quanta Computer Inc.	
	Size Custom Document Number <b>DISCHARGE/3VS5/5VS5/LAN</b>	Date: Friday, November 27, 2009 Sheet 30 of 30

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