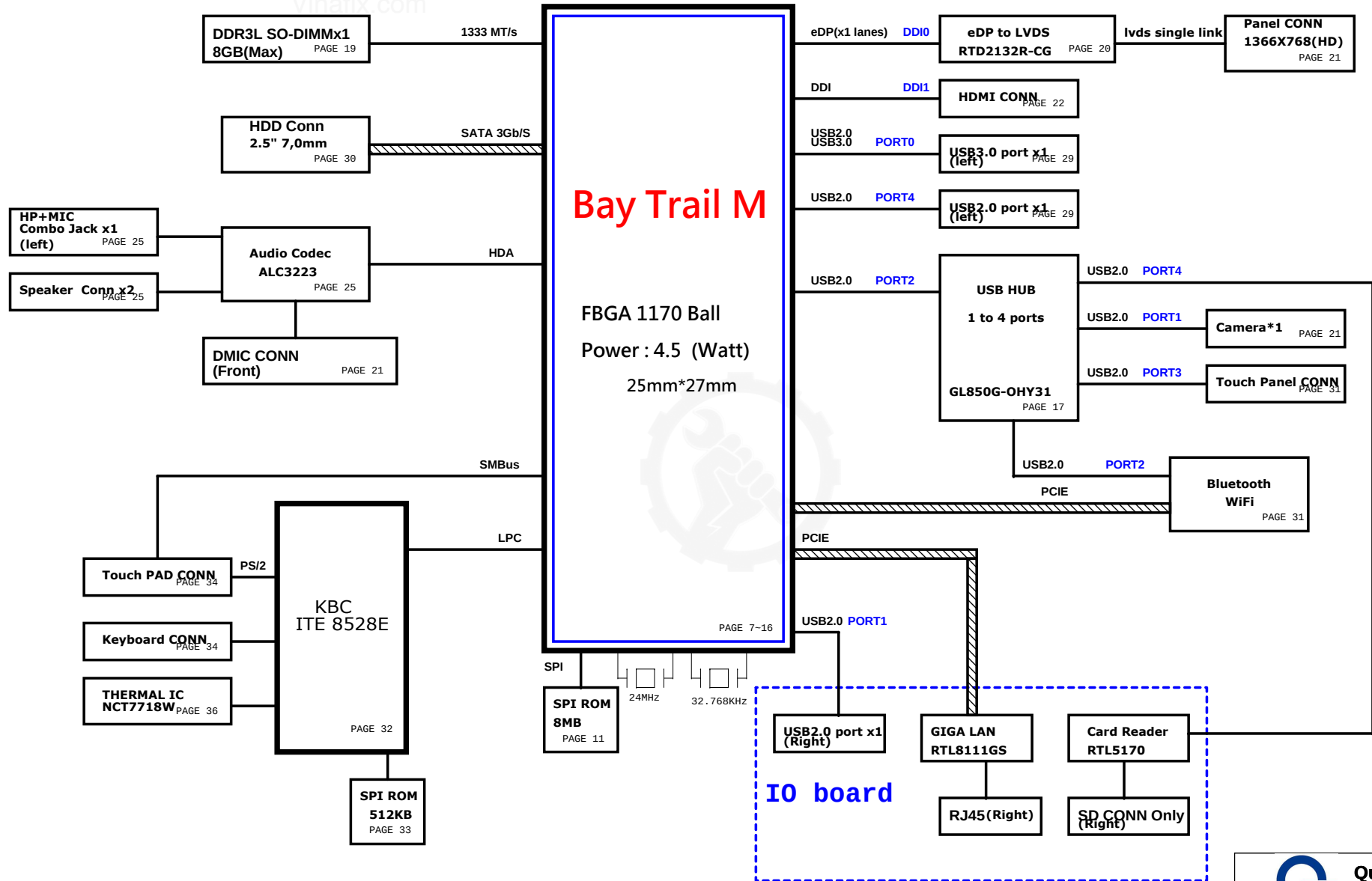


# ZM6 BLOCK DIAGRAM

01



| USB 2.0 | Port Assignment                                     |
|---------|-----------------------------------------------------|
| USBP0   | External port(USB3 CONN) <b>USB3</b>                |
| USBP1   | External port(USB2 CONN)(IO/B)<br><b>Debug Port</b> |
| USBP2   | USB HUB                                             |
| USBP3   | External port(USB2 CONN)                            |

| PCIE Master | Port Assignment |
|-------------|-----------------|
| PCIE 0      | WLAN/BT         |
| PCIE 1      | LAN             |
| PCIE 2      | (NC)            |
| PCIE 3      | (NC)            |

| SATA Master | Port Assignment |
|-------------|-----------------|
| SATA0       | HDD             |
| SATA1       | (NC)            |

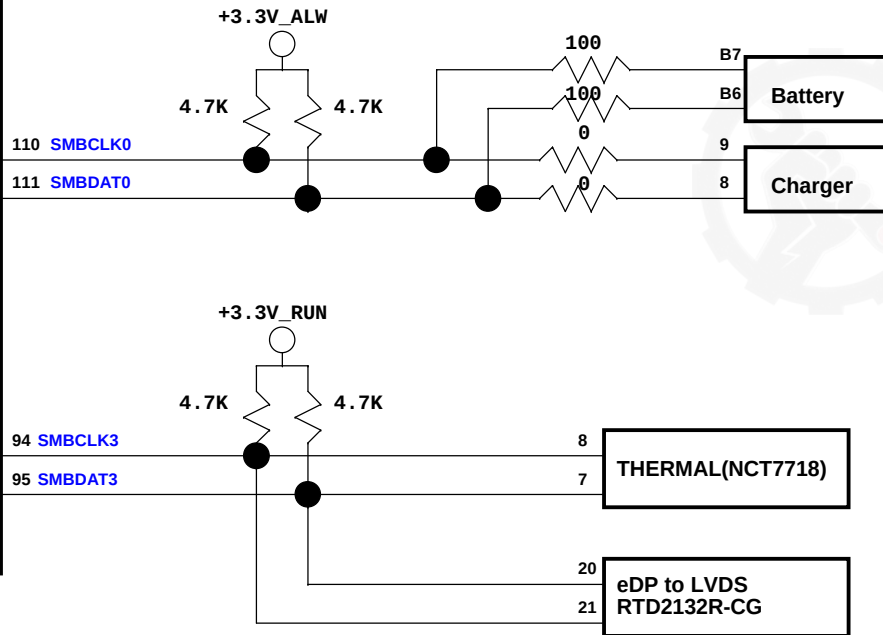
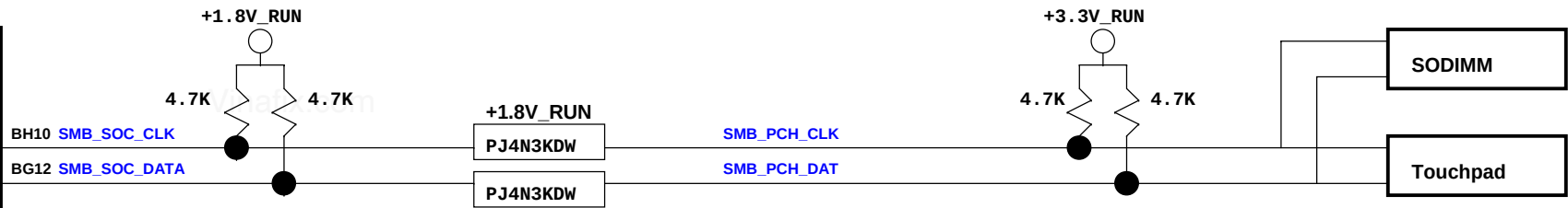
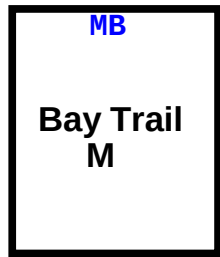
| USB 3.0 | Port Assignment           |
|---------|---------------------------|
| USBP0   | External port (USB3 CONN) |

| Display Port | Port Assignment |
|--------------|-----------------|
| DDI0         | eDP             |
| DDI1         | HDMI            |

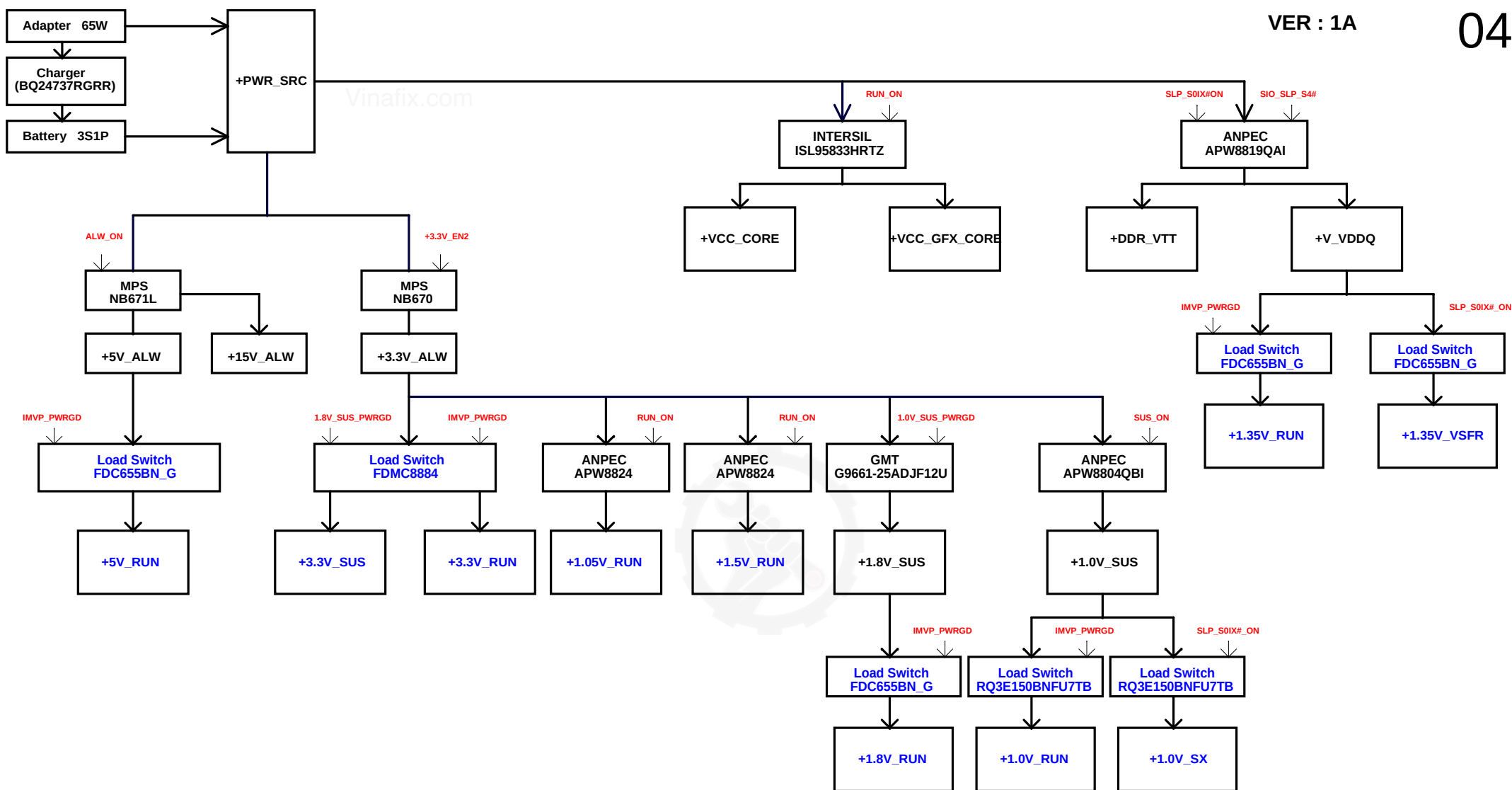
| USB HUB | Port Assignment |
|---------|-----------------|
| USBP1   | Touch Screen    |
| USBP2   | Camera Front    |
| USBP3   | BT              |
| USBP4   | Card Reader     |



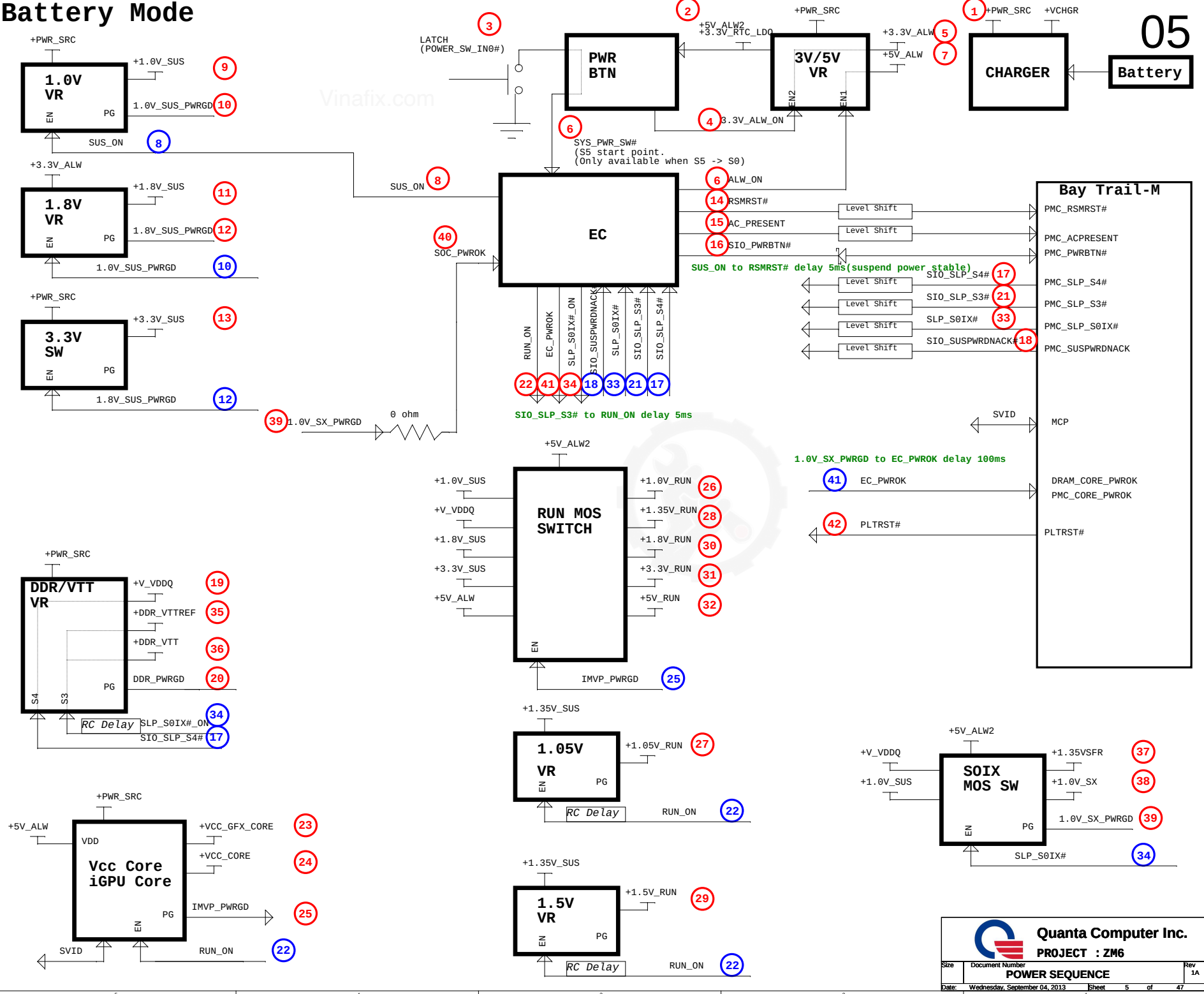
|                                                                                       |                 |                             |         |
|---------------------------------------------------------------------------------------|-----------------|-----------------------------|---------|
|  |                 | <b>Quanta Computer Inc.</b> |         |
|                                                                                       |                 | <b>PROJECT : ZM6</b>        |         |
| Size                                                                                  | Document Number | <b>PORT ASSIGNMENT</b>      |         |
| Date: Wednesday, September 04, 2013                                                   |                 | Sheet                       | 2 of 47 |
|                                                                                       |                 | Rev                         | 1A      |



|       | Function    | IC          | Address          |
|-------|-------------|-------------|------------------|
| SMBUS | Thermal IC  | NCT7718     | 1001100xb (98h)  |
|       | eDP to LVDS | RTD2132R-CG | 0x94             |
|       | Charge IC   | BQ24737RGRR | 00010010 (0x12h) |
|       | Battery     |             | 00010110 (0x16h) |
|       | DDR3        | JDIM1A      | A0h              |



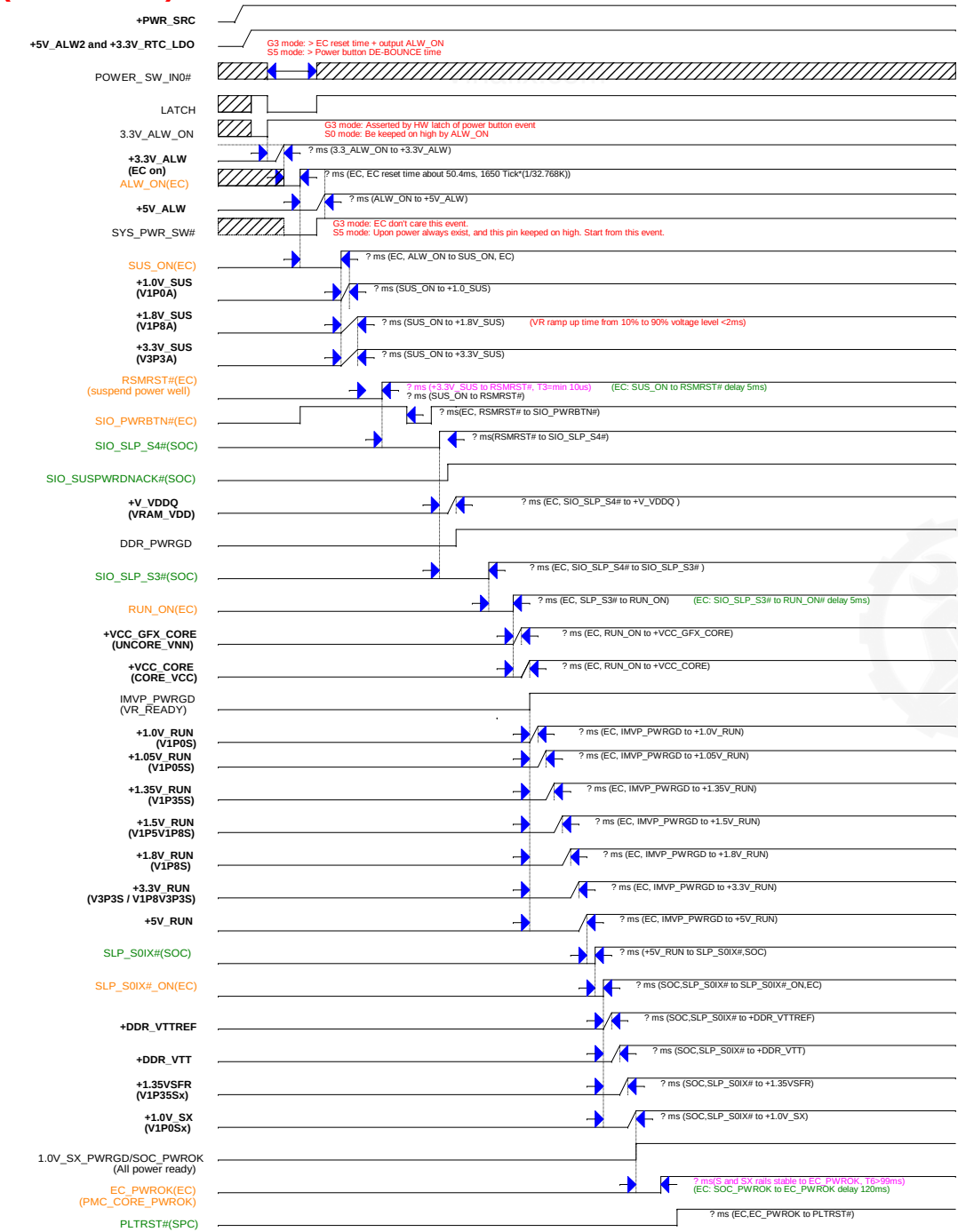
Battery Mode



ZM6 Power Sequence  
(G3 to S0)

Bay Trail-M EDS 512177, Rev1.2

06



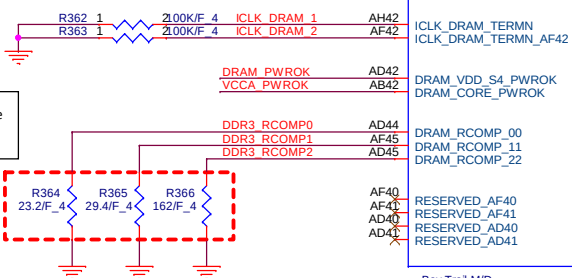
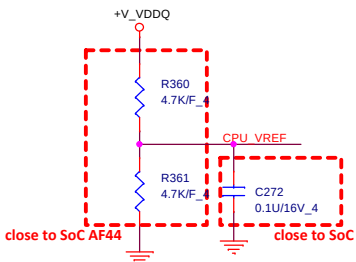
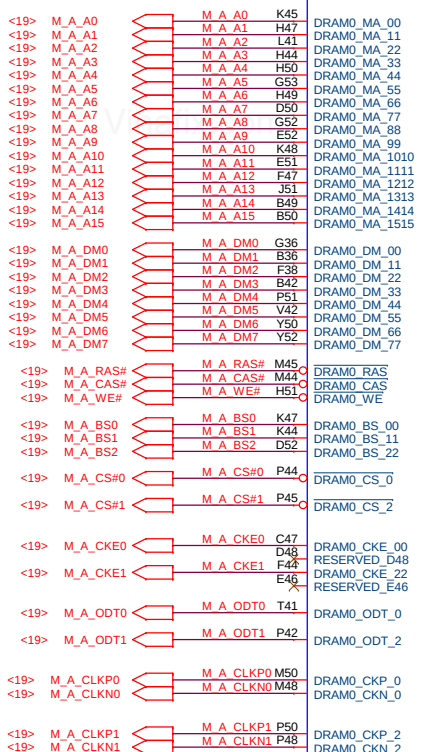
G3

S0

# DRAM: V1P35U (+V\_VDDQ\_VR)

U16A

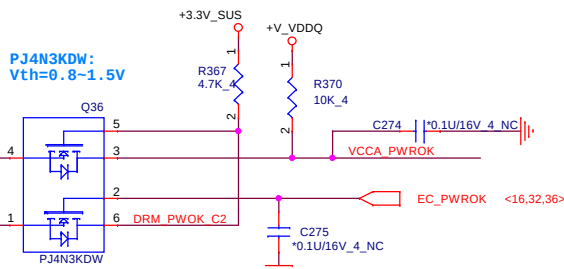
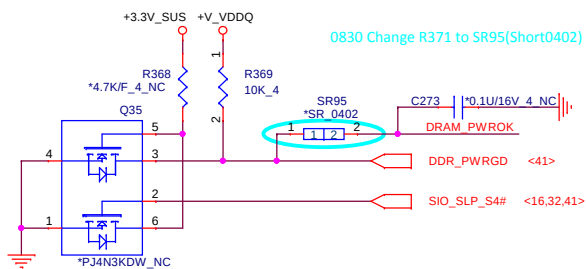
07



The DRAM\_RCOMP[2:0] signals should be referenced to either Vss or Vdd planes (avoid referencing to noisy planes)

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Bay Trail-M/D



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U16B

|      |               |               |      |
|------|---------------|---------------|------|
| AY45 | DRAM1_MA_00   | DRAM1_DQ_00   | BG38 |
| BB47 | DRAM1_MA_11   | DRAM1_DQ_11   | BC40 |
| AW47 | DRAM1_MA_22   | DRAM1_DQ_22   | BA42 |
| BB44 | DRAM1_MA_33   | DRAM1_DQ_33   | BD42 |
| BB90 | DRAM1_MA_44   | DRAM1_DQ_44   | BC38 |
| BC53 | DRAM1_MA_55   | DRAM1_DQ_55   | BD36 |
| BB49 | DRAM1_MA_66   | DRAM1_DQ_66   | BF42 |
| BF90 | DRAM1_MA_77   | DRAM1_DQ_77   | BC44 |
| BC52 | DRAM1_MA_88   | DRAM1_DQ_88   | BH32 |
| BE52 | DRAM1_MA_99   | DRAM1_DQ_99   | BG32 |
| AY49 | DRAM1_MA_1010 | DRAM1_DQ_1010 | BG36 |
| BE51 | DRAM1_MA_1111 | DRAM1_DQ_1111 | BJ37 |
| BD47 | DRAM1_MA_1212 | DRAM1_DQ_1212 | BG33 |
| BA51 | DRAM1_MA_1313 | DRAM1_DQ_1313 | BJ33 |
| BH49 | DRAM1_MA_1414 | DRAM1_DQ_1414 | BG37 |
| BH90 | DRAM1_MA_1515 | DRAM1_DQ_1515 | BH38 |
| BD38 | DRAM1_DM_00   | DRAM1_DQ_1616 | BJ36 |
| BH36 | DRAM1_DM_11   | DRAM1_DQ_1717 | AT36 |
| BC36 | DRAM1_DM_22   | DRAM1_DQ_1818 | AV40 |
| BH42 | DRAM1_DM_33   | DRAM1_DQ_1919 | AT40 |
| AT31 | DRAM1_DM_44   | DRAM1_DQ_2020 | BA36 |
| AW42 | DRAM1_DM_55   | DRAM1_DQ_2121 | AV36 |
| AK50 | DRAM1_DM_66   | DRAM1_DQ_2222 | AY42 |
| AK52 | DRAM1_DM_77   | DRAM1_DQ_2323 | AV40 |
| AV45 | DRAM1_RAS     | DRAM1_DQ_2424 | BJ41 |
| AV44 | DRAM1_CAS     | DRAM1_DQ_2525 | BG41 |
| BB51 | DRAM1_WE      | DRAM1_DQ_2626 | BJ45 |
| AY47 | DRAM1_BS_00   | DRAM1_DQ_2727 | BH46 |
| AY44 | DRAM1_BS_11   | DRAM1_DQ_2828 | BG40 |
| BF52 | DRAM1_BS_22   | DRAM1_DQ_2929 | BH40 |
| AT44 | DRAM1_CS_0    | DRAM1_DQ_3030 | BH48 |
| AT45 | DRAM1_CS_2    | DRAM1_DQ_3131 | BH47 |
| BG47 | DRAM1_CKE_00  | DRAM1_DQ_3232 | AY52 |
| BE46 | RESERVED_BE46 | DRAM1_DQ_3333 | AY51 |
| BD44 | DRAM1_CKE_22  | DRAM1_DQ_3434 | AP52 |
| BF46 | RESERVED_BF48 | DRAM1_DQ_3535 | AP51 |
| AP41 | DRAM1_ODT_0   | DRAM1_DQ_3636 | AW51 |
| AT42 | DRAM1_ODT_2   | DRAM1_DQ_3737 | AW53 |
| AV50 | DRAM1_CKP_0   | DRAM1_DQ_3838 | AR51 |
| AV48 | DRAM1_CKN_0   | DRAM1_DQ_3939 | AR53 |
| AT50 | DRAM1_CKP_2   | DRAM1_DQ_4040 | AP47 |
| AT48 | DRAM1_CKN_2   | DRAM1_DQ_4141 | AP45 |
| AT41 | DRAM1_DRAMRST | DRAM1_DQ_4242 | AK40 |
|      |               | DRAM1_DQ_4343 | AM41 |
|      |               | DRAM1_DQ_4444 | AP48 |
|      |               | DRAM1_DQ_4545 | AP50 |
|      |               | DRAM1_DQ_4646 | AK42 |
|      |               | DRAM1_DQ_4747 | BH40 |
|      |               | DRAM1_DQ_4848 | AM45 |
|      |               | DRAM1_DQ_4949 | AM47 |
|      |               | DRAM1_DQ_5050 | AF48 |
|      |               | DRAM1_DQ_5151 | AF50 |
|      |               | DRAM1_DQ_5252 | AM48 |
|      |               | DRAM1_DQ_5353 | AM50 |
|      |               | DRAM1_DQ_5454 | BH44 |
|      |               | DRAM1_DQ_5555 | AK45 |
|      |               | DRAM1_DQ_5656 | AM52 |
|      |               | DRAM1_DQ_5757 | AL51 |
|      |               | DRAM1_DQ_5858 | AG53 |
|      |               | DRAM1_DQ_5959 | AG51 |
|      |               | DRAM1_DQ_6060 | AL53 |
|      |               | DRAM1_DQ_6161 | AK51 |
|      |               | DRAM1_DQ_6262 | AF52 |
|      |               | DRAM1_DQ_6363 | AF51 |
|      |               | DRAM1_DQSP_00 | BF40 |
|      |               | DRAM1_DQSN_00 | BD40 |
|      |               | DRAM1_DQSP_11 | BG35 |
|      |               | DRAM1_DQSN_11 | BH34 |
|      |               | DRAM1_DQSP_22 | BA38 |
|      |               | DRAM1_DQSN_22 | AY38 |
|      |               | DRAM1_DQSP_33 | BH44 |
|      |               | DRAM1_DQSN_33 | BG43 |
|      |               | DRAM1_DQSP_44 | BU53 |
|      |               | DRAM1_DQSN_44 | AV52 |
|      |               | DRAM1_DQSP_55 | AP42 |
|      |               | DRAM1_DQSN_55 | AP44 |
|      |               | DRAM1_DQSP_66 | AK47 |
|      |               | DRAM1_DQSN_66 | AK48 |
|      |               | DRAM1_DQSP_77 | BH52 |
|      |               | DRAM1_DQSN_77 | BU51 |

2 OF 13

Bay Trail-M/D



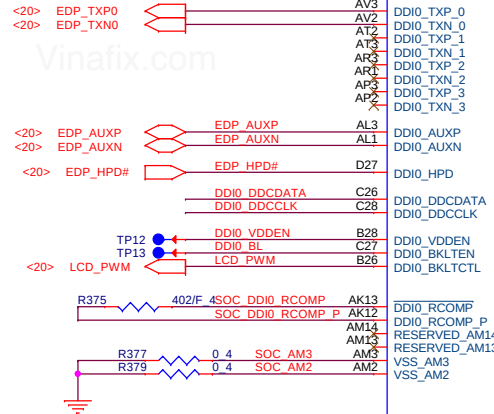
Quanta Computer Inc.  
PROJECT : ZM6

|       |                               |               |
|-------|-------------------------------|---------------|
| Size  | Document Number               | Rev           |
|       | Bay Trail M (Memory 0,1)      | 1A            |
| Date: | Wednesday, September 04, 2013 | Sheet 8 of 47 |

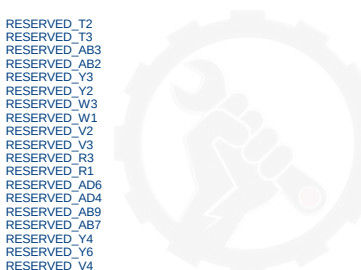
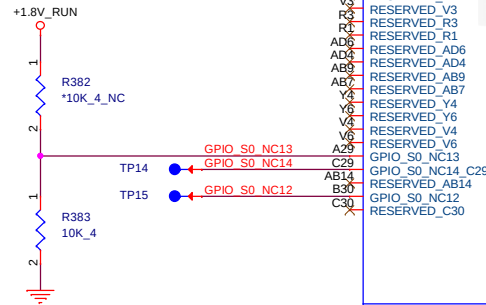


DDIX TX/AUX: VIP0SX (+1.0V\_SX)  
DDIX HPD/DDC/VDDEN/BKLT: VIP8S (+1.8V\_RUN)

EDP



| HW Strap:DDI0 Detect |                                |
|----------------------|--------------------------------|
| DDI0_DDCDATA         |                                |
| PU                   | 1 = DDI0 detected              |
| PD                   | 0 = DDI0 not detected(Default) |

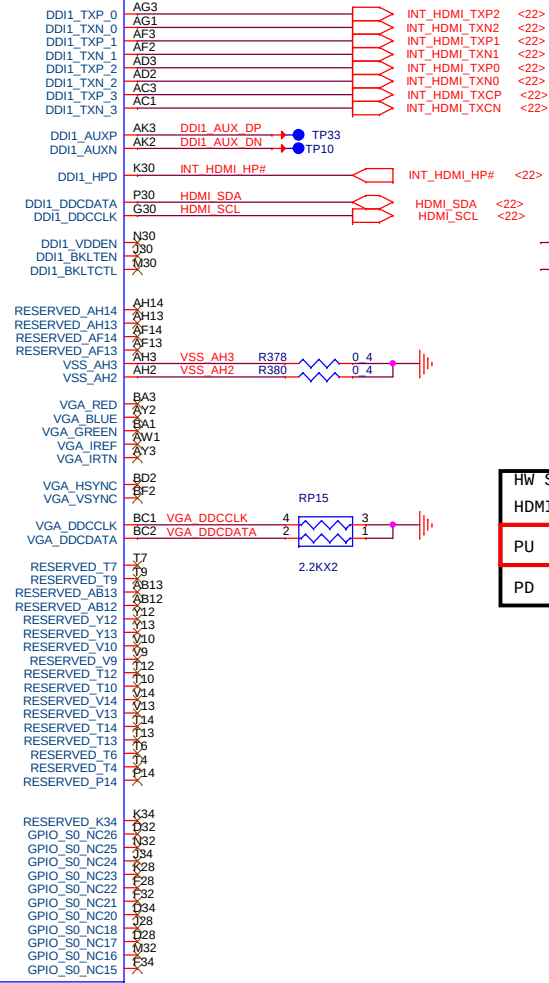


Bay Trail-M/D

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09

HDMI

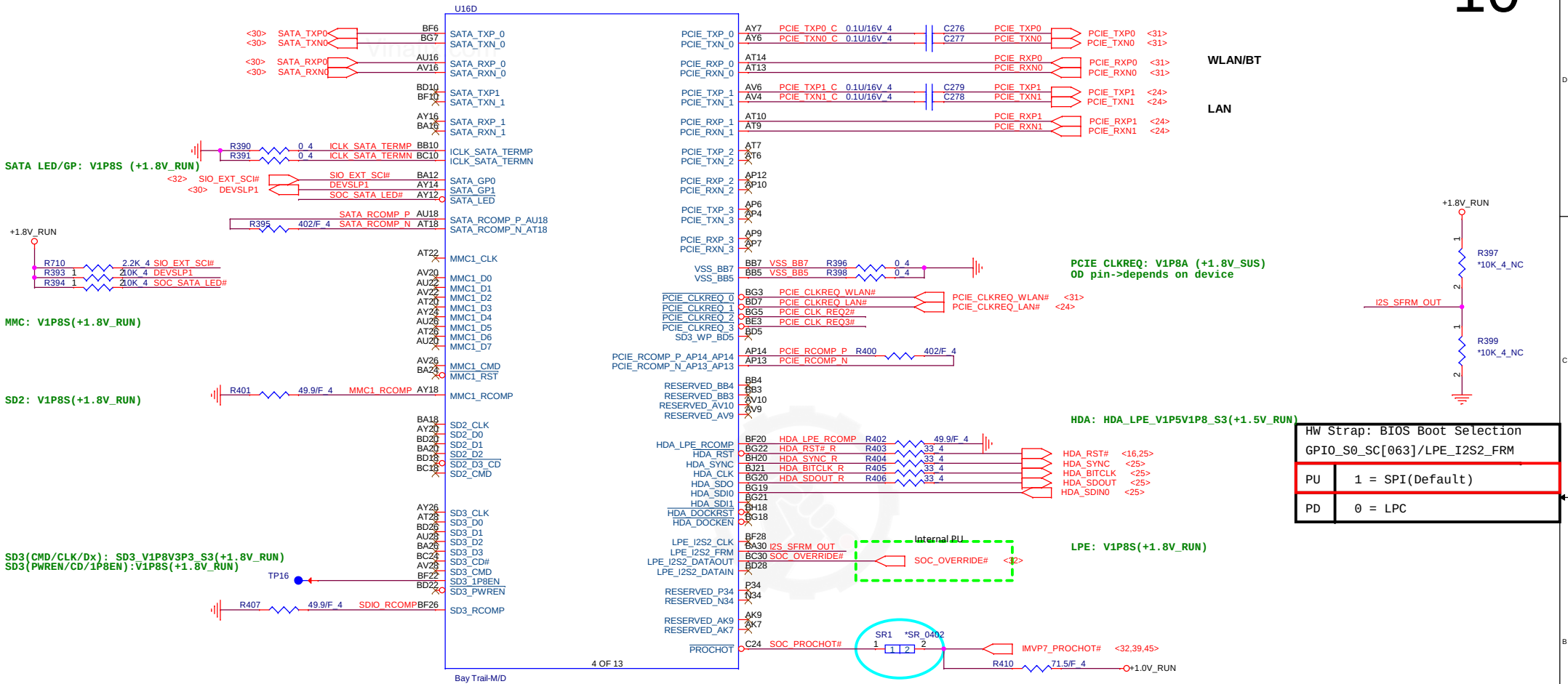


| HW Strap:DDI1 Detect |                                |
|----------------------|--------------------------------|
| HDMI_SDA             |                                |
| PU                   | 1 = DDI1 detected              |
| PD                   | 0 = DDI1 not detected(Default) |



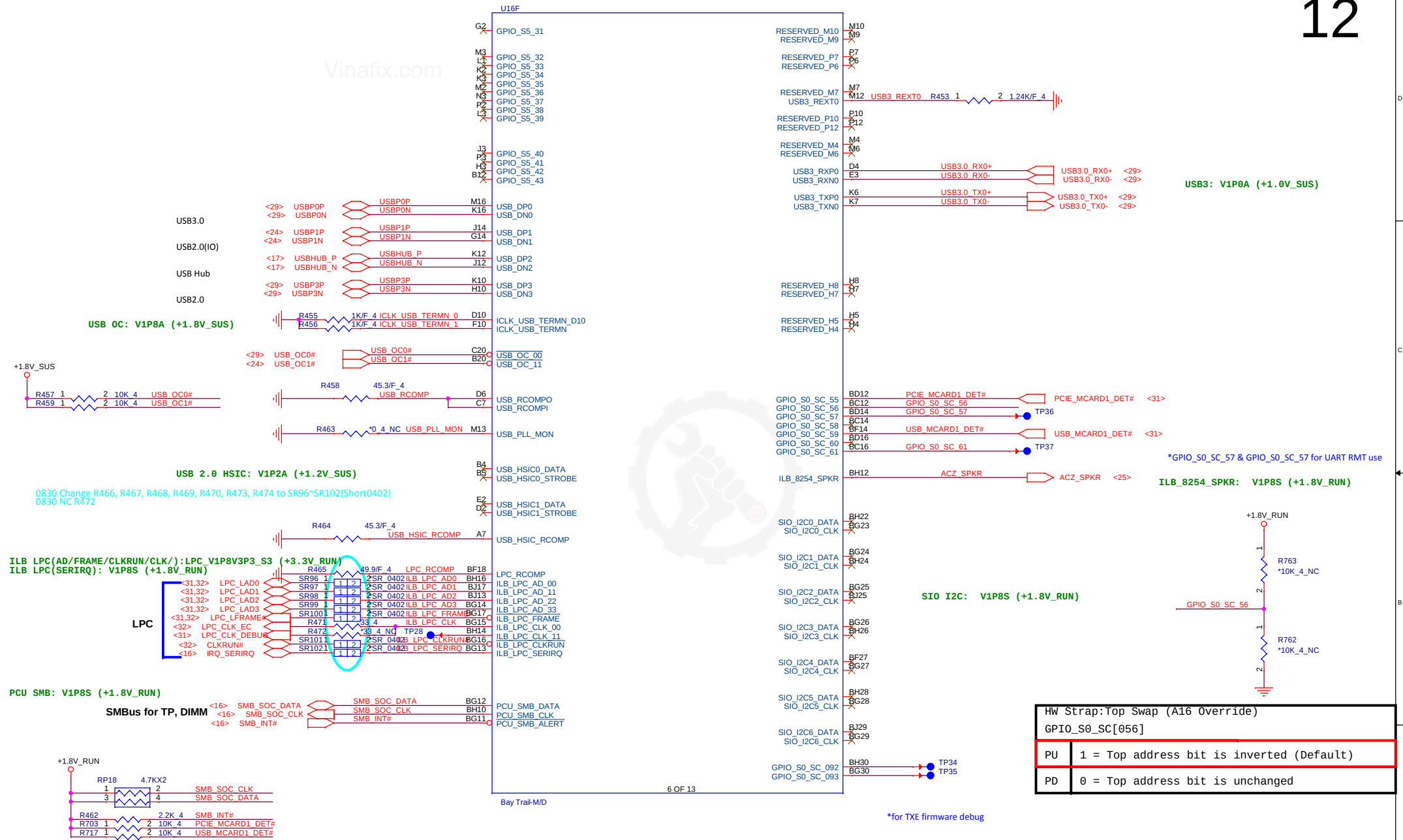
Quanta Computer Inc.  
PROJECT : ZM6

| Size  | Document Number               | Rev           |
|-------|-------------------------------|---------------|
|       | Bay Trail M (DISPLAY)         | 1A            |
| Date: | Wednesday, September 04, 2013 | Sheet 9 of 47 |

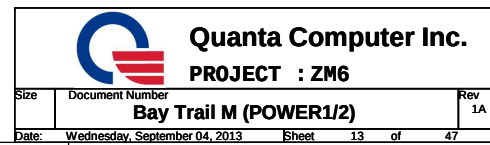


**Quanta Computer Inc.**  
**PROJECT : ZM6**



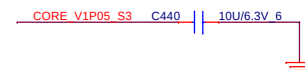
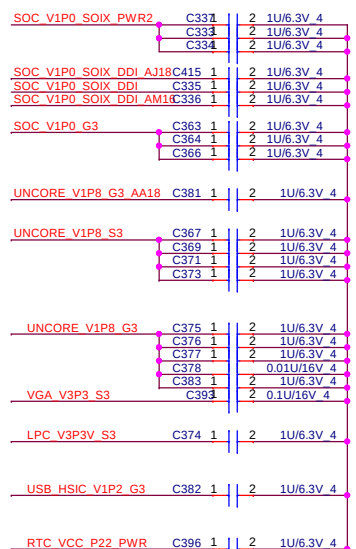


**Quanta Computer Inc.**  
**PROJECT : ZM6**

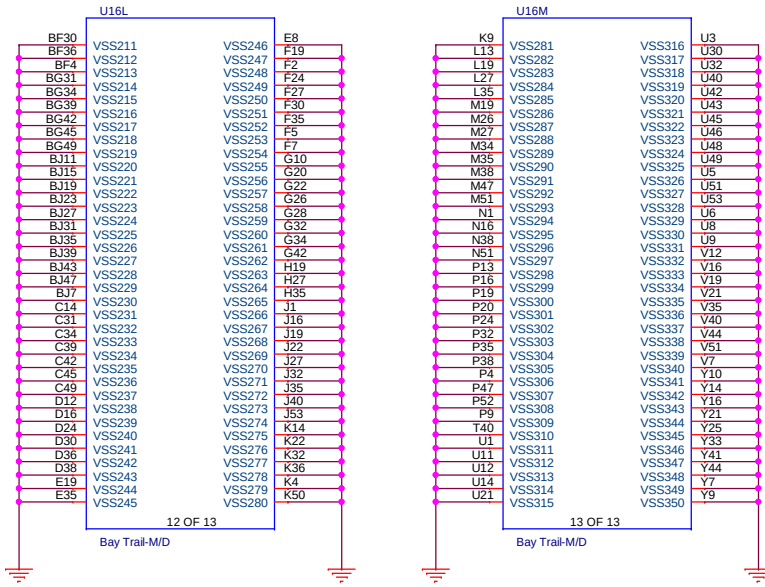
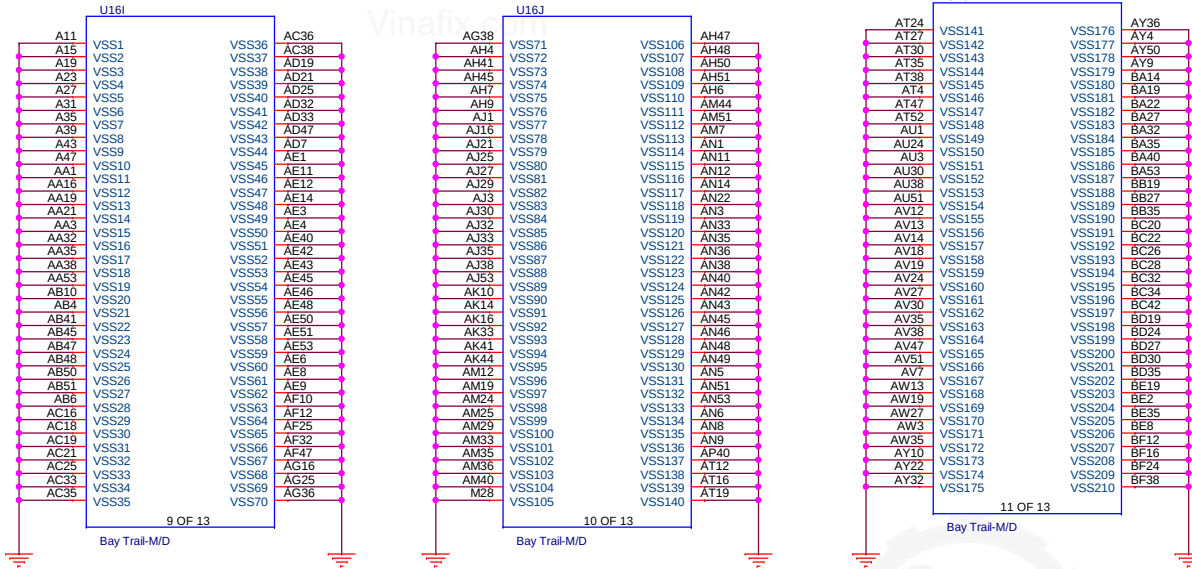




8 OF 13

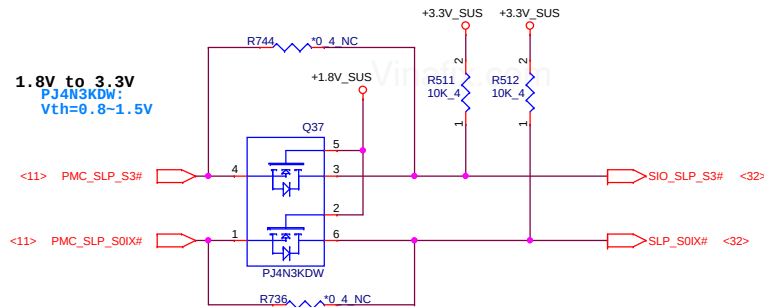




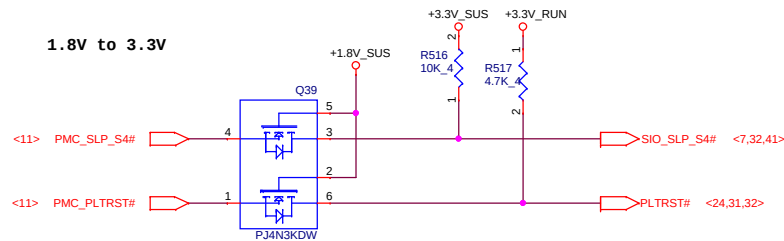


Co-layout:  
If mount R736, R744, need NC R511, R512, Q37

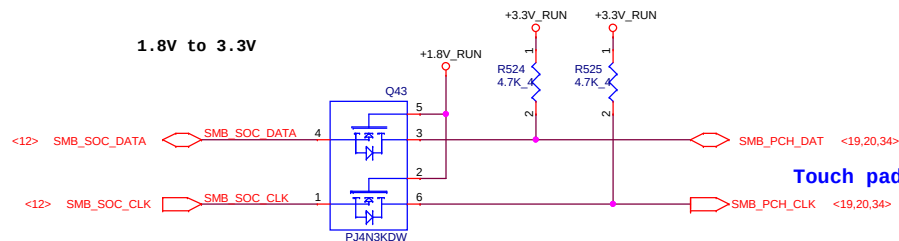
1.8V to 3.3V  
PJ4N3KDW  
Vth=0.8~1.5V



1.8V to 3.3V

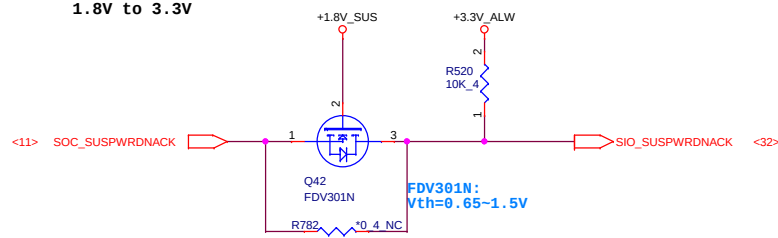


1.8V to 3.3V



Touch pad, DIMM

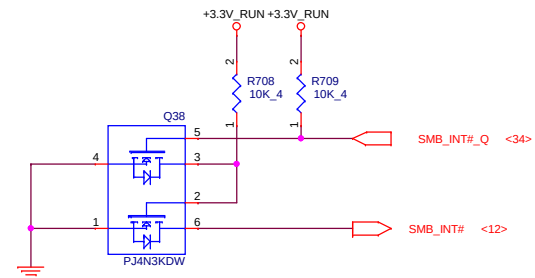
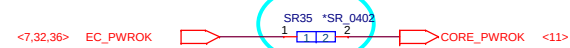
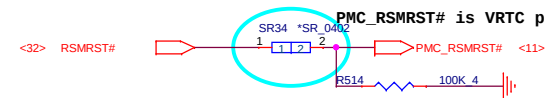
1.8V to 3.3V



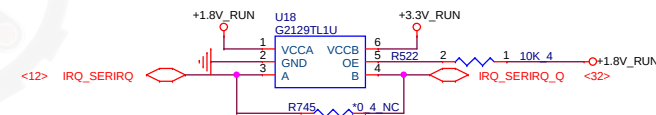
FDV301N:  
Vth=0.65~1.5V

0815 Remove R510, R515, add SR34,SR35

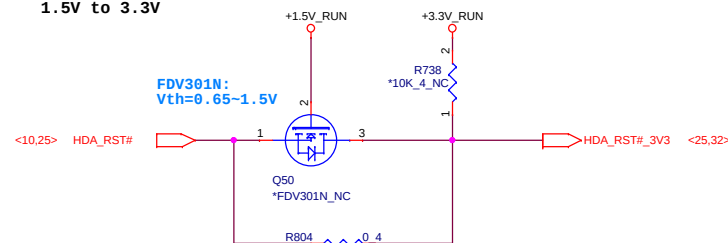
PMC\_RSMRST# is VRTC power rail



Co-layout:  
If mount R745, need NC U18, R522



1.5V to 3.3V



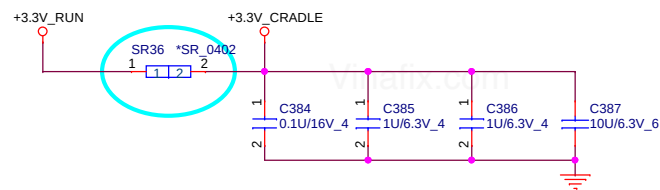
FDV301N:  
Vth=0.65~1.5V



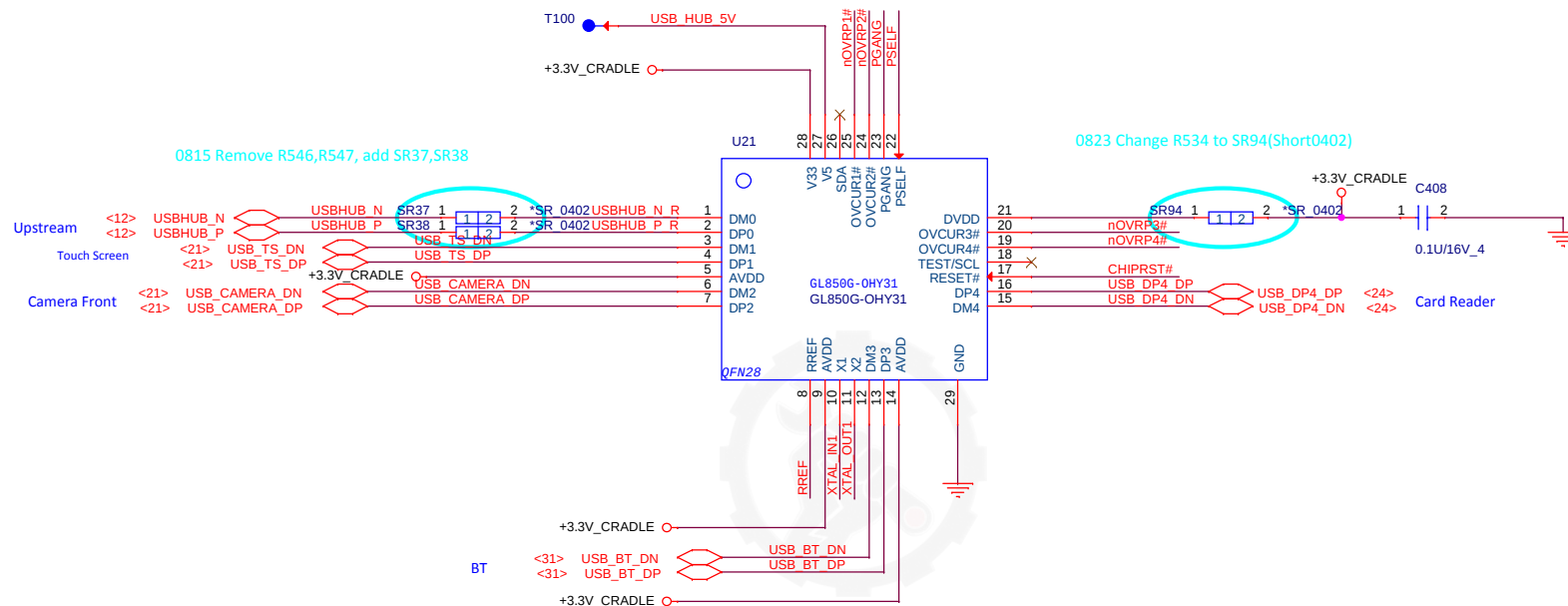
Quanta Computer Inc.  
PROJECT : ZM6

| Size  | Document Number               | Rev            |
|-------|-------------------------------|----------------|
|       | Bay Trail M Level Shift       | 1A             |
| Date: | Wednesday, September 04, 2013 | Sheet 16 of 47 |



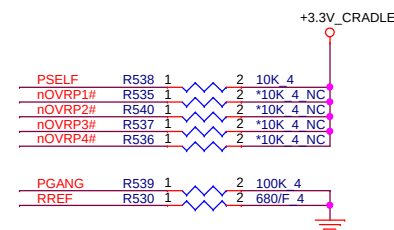
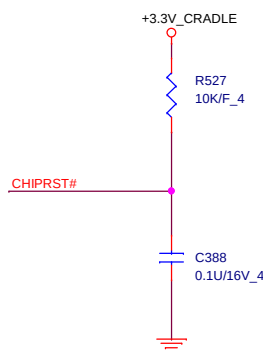
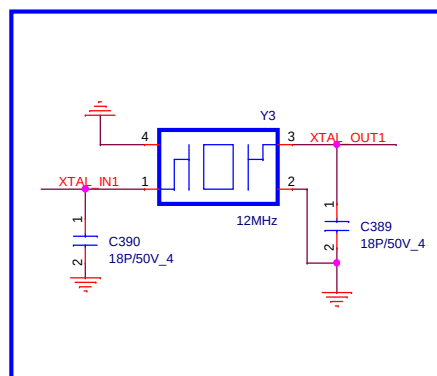


0815 Remove R716, add SR36



0815 Remove R546,R547, add SR37,SR38

0823 Change R534 to SR94(Short0402)

TEST  
NC:Normal hub operation.

| PGANG |                 |
|-------|-----------------|
| H     | Gang mode       |
| L     | Individual mode |

| PSELF |            |
|-------|------------|
| H     | Power Self |
| L     | Power Bus  |

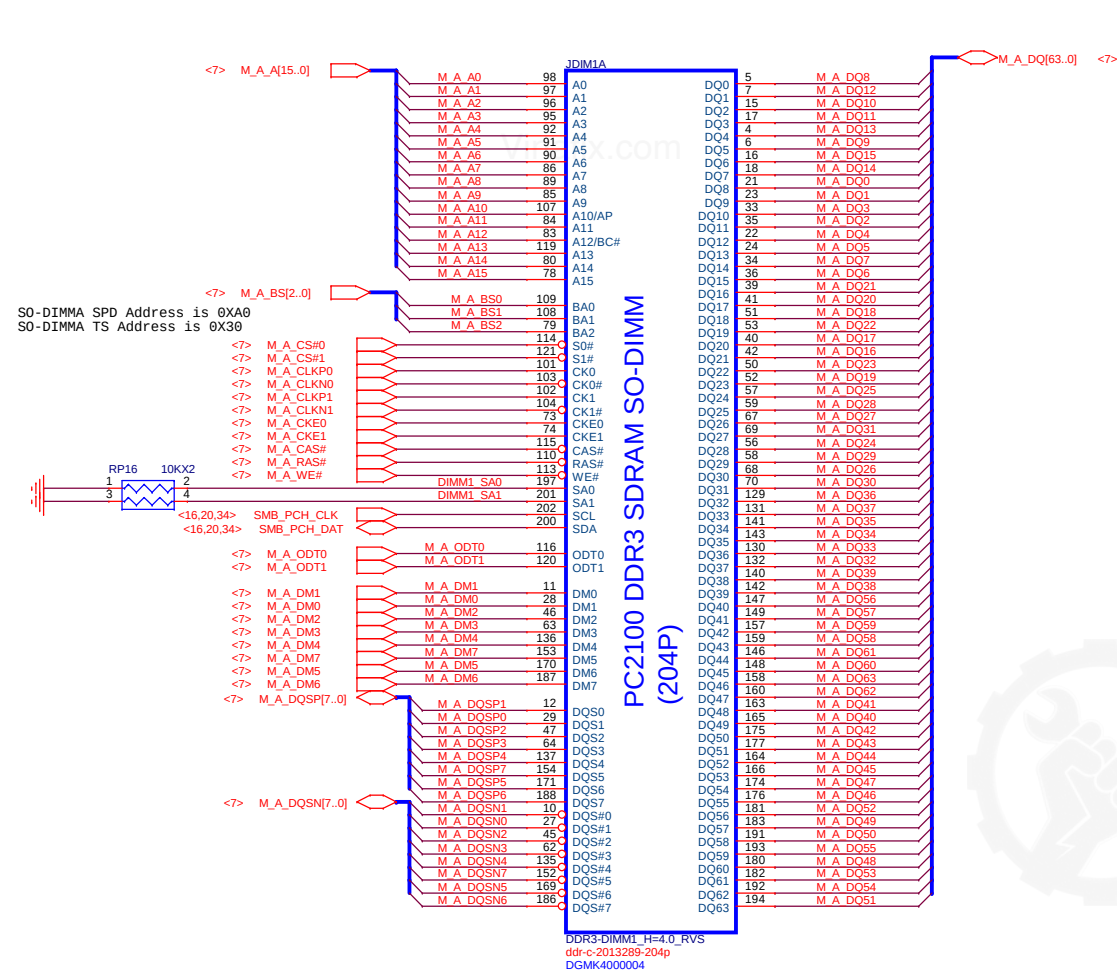
| nOVRPx    |               |
|-----------|---------------|
| Floating  | Non-removable |
| Pull high | Removable     |

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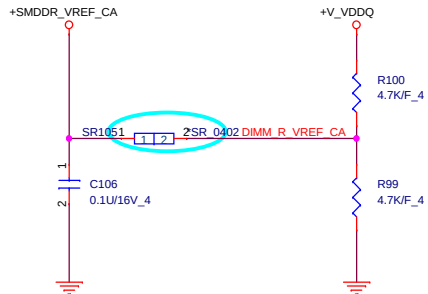
**Quanta Computer Inc.**  
**PROJECT : ZM6**

|       |                               |                |
|-------|-------------------------------|----------------|
| Size  | Document Number               | Rev            |
|       | <b>Blank</b>                  | <b>1A</b>      |
| Date: | Wednesday, September 04, 2013 | Sheet 18 of 47 |

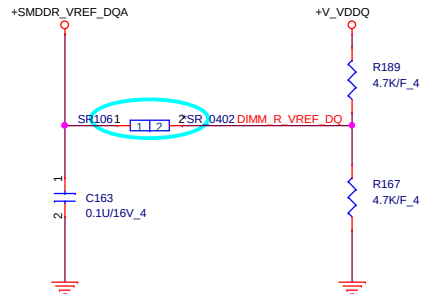


0830 Change R129, R152 to SR105, SR106(Short0402)

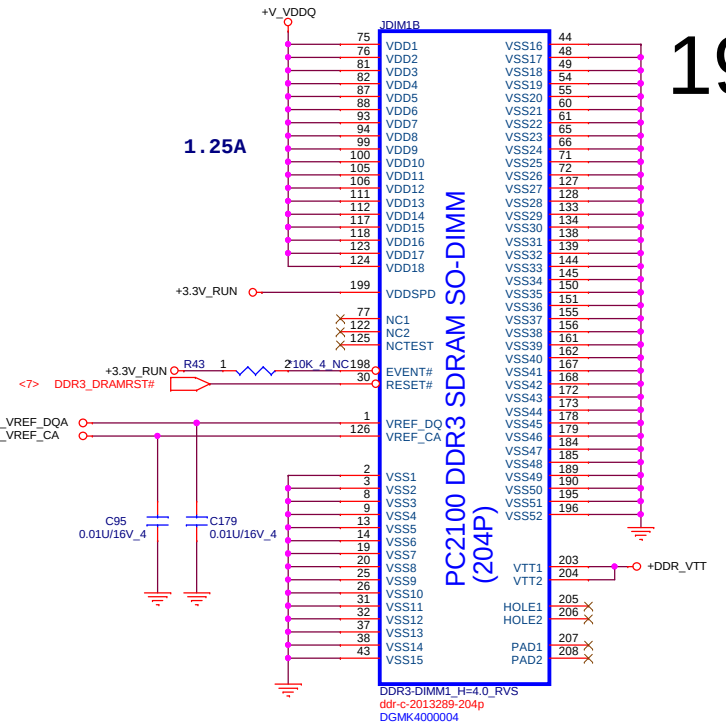
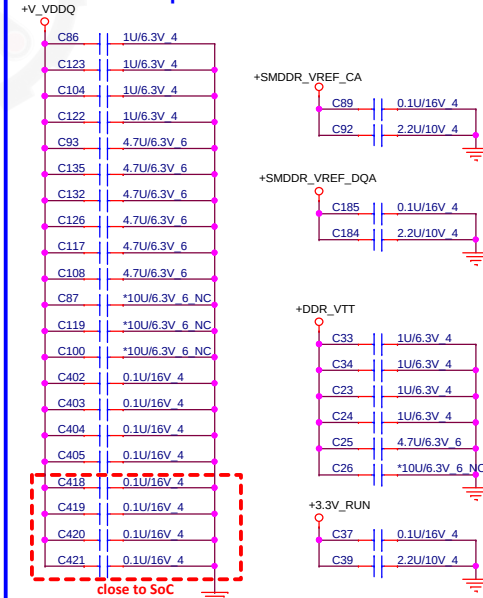
## M1 VREF



## M1 VREF

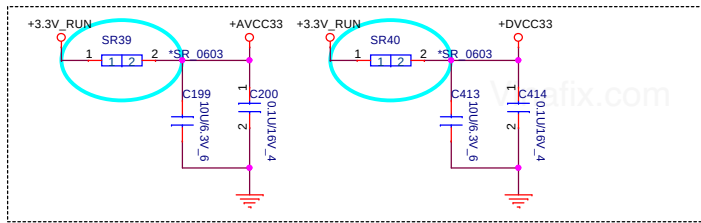


## Place these Caps near So-Dimm1.



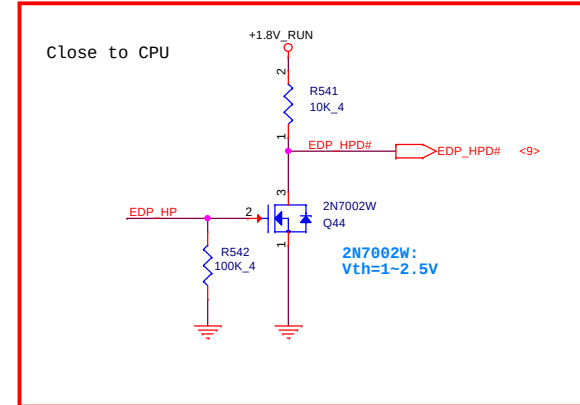
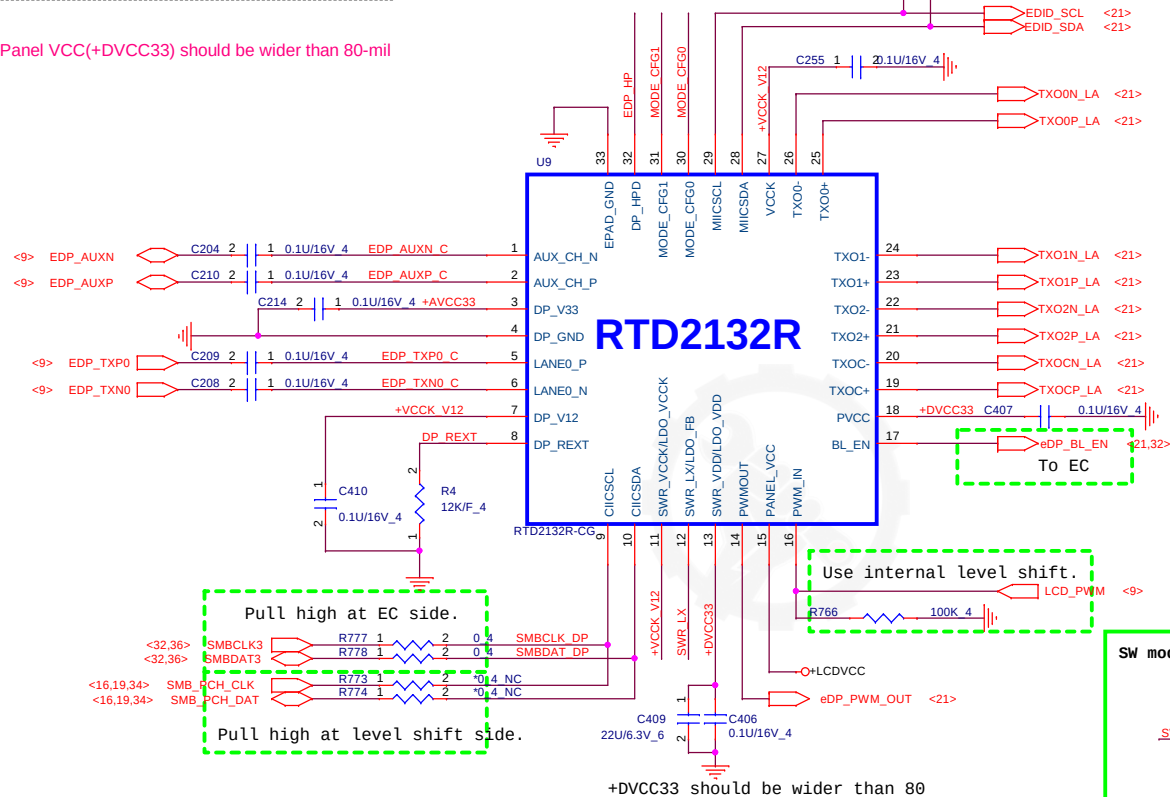
Quanta Computer Inc.  
PROJECT : ZM6

| Size                 | Document Number               | Rev            |
|----------------------|-------------------------------|----------------|
|                      |                               | 1A             |
| System Memory (4.0H) |                               |                |
| Date:                | Wednesday, September 04, 2013 | Sheet 19 of 47 |

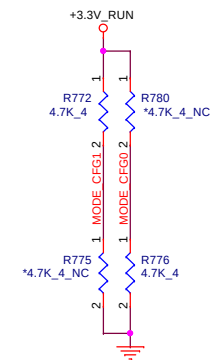


Note:

1. Entire trace of Panel VCC(+DVCC33) should be wider than 80-mil



+DVCC33 should be wider than 80



|                  |   | MODE_CFG0(PIN30) |             |
|------------------|---|------------------|-------------|
|                  |   | 0                | 1           |
| MODE_CFG1(PIN31) | 0 | X                | EP MODE     |
|                  | 1 | ROM ONLY MODE    | EEPROM MODE |

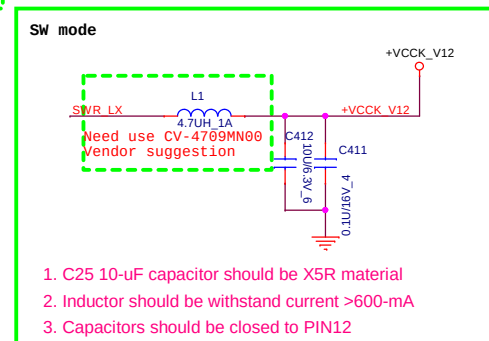
RTD2132 Supports three operation mode for system design.

Reserved 4.7K resistor pull up/low for mode selection

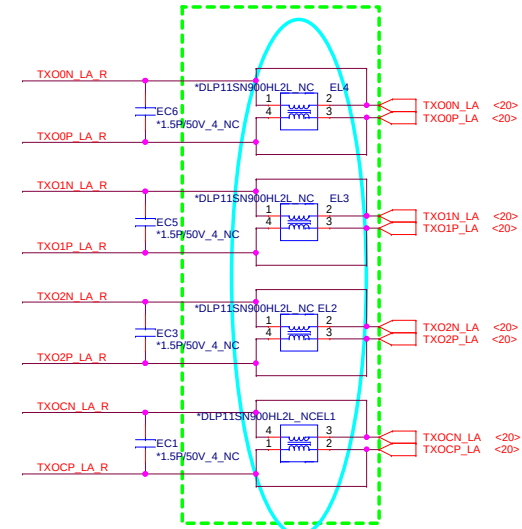
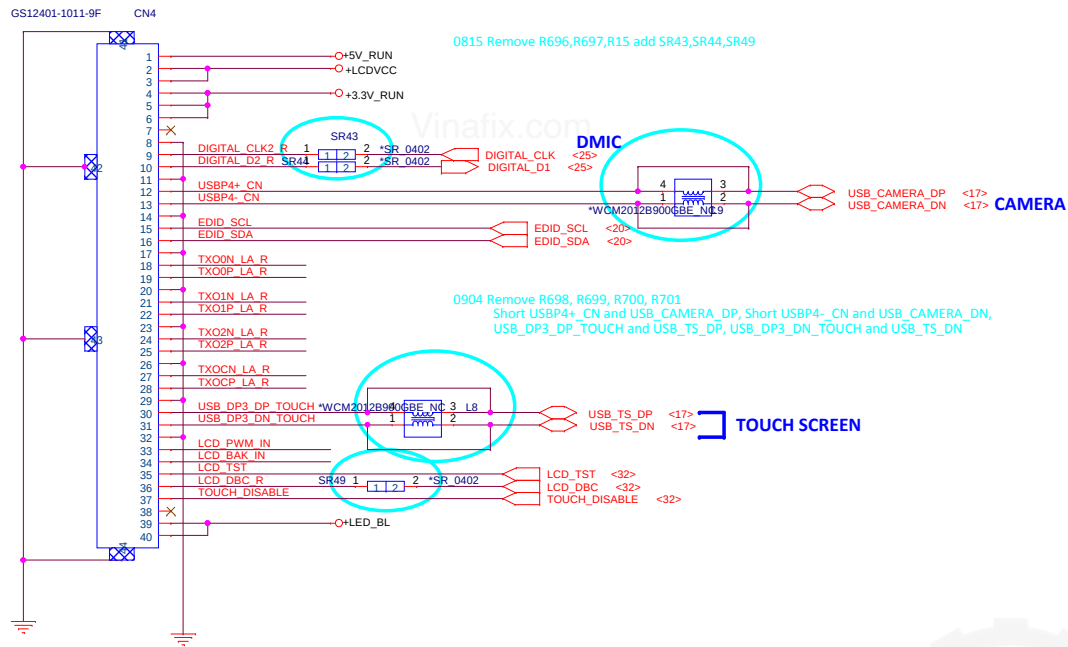
ROM ONLY Mode : PIN30 4.7K pull low, PIN31 4.7K pull high

EP Mode : PIN30 4.7K pull high, PIN31 4.7K pull low

EEPROM Mode : PIN30 4.7K pull high, PIN31 4.7K pull high



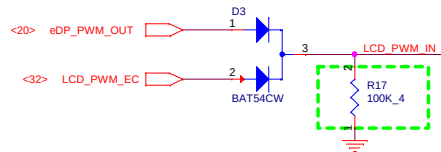
1. C25 10-uF capacitor should be X5R material
2. Inductor should be withstand current >600-mA
3. Capacitors should be closed to PIN12



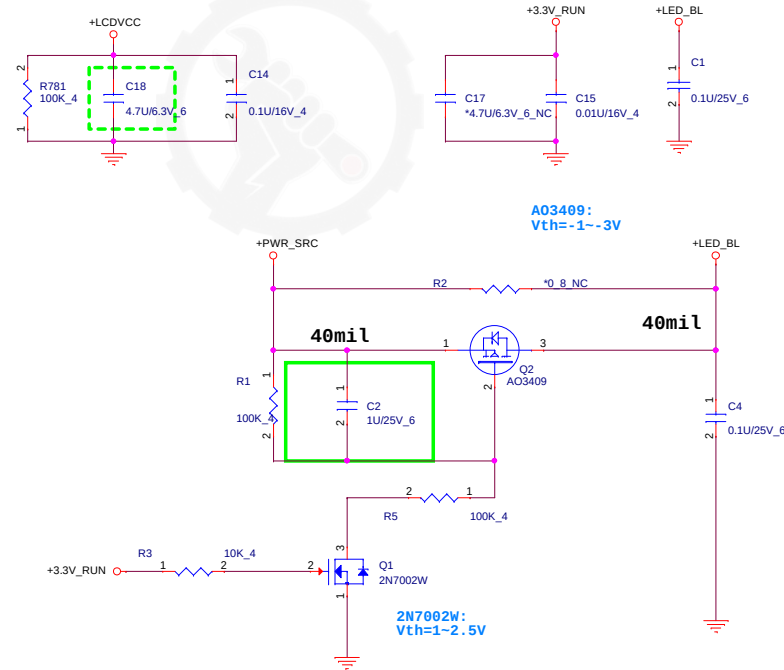
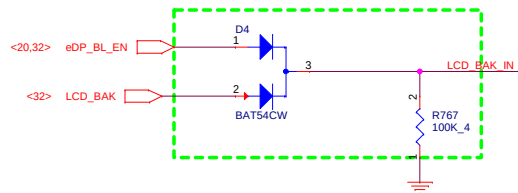
0902\_1 Remove R746, R747, R748, R749, R750, R751, R752, R753

Short TXO0N\_LA\_R and TXO0N\_LA, TXO0P\_LA\_R and TXO0P\_LA, TXO1N\_LA\_R and TXO1N\_LA, TXO1P\_LA\_R and TXO1P\_LA, TXO2N\_LA\_R and TXO2N\_LA, TXO2P\_LA\_R and TXO2P\_LA, TXOCN\_LA\_R and TXOCN\_LA, TXOCP\_LA\_R and TXOCP\_LA,

### Brightness Control



### BAK\_EN



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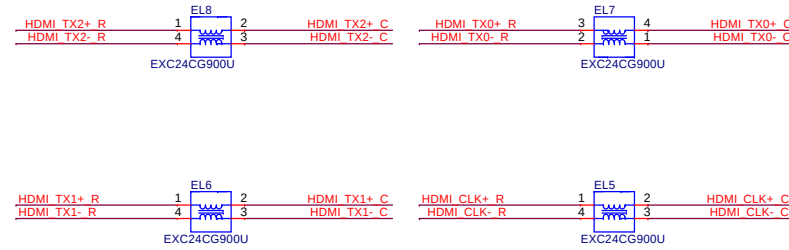
PROJECT : ZM6

| Size  | Document Number               | Rev            |
|-------|-------------------------------|----------------|
|       | LVD5/Camera                   | 1A             |
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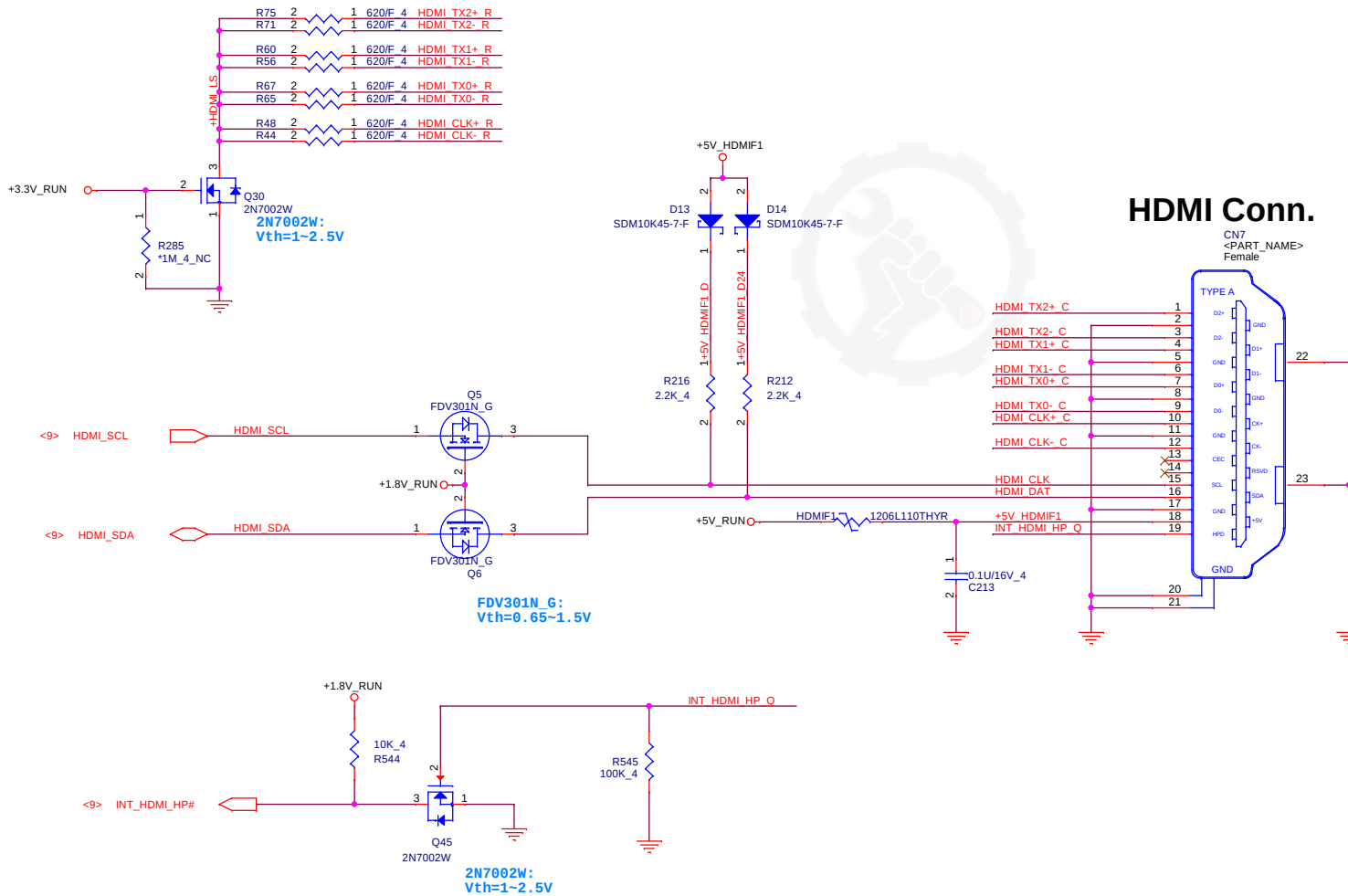
## INT HDMI

|                   |               |     |   |   |          |   |             |
|-------------------|---------------|-----|---|---|----------|---|-------------|
| <9> INT_HDMI_TXP2 | INT_HDMI_TXP2 | C46 | 1 | 2 | 0.1U/16V | 4 | HDMI TX2+ R |
| <9> INT_HDMI_TXN2 | INT_HDMI_TXN2 | C44 | 1 | 2 | 0.1U/16V | 4 | HDMI TX2- R |
| <9> INT_HDMI_TXP1 | INT_HDMI_TXP1 | C40 | 1 | 2 | 0.1U/16V | 4 | HDMI TX1+ R |
| <9> INT_HDMI_TXN1 | INT_HDMI_TXN1 | C38 | 1 | 2 | 0.1U/16V | 4 | HDMI TX1- R |
| <9> INT_HDMI_TXP0 | INT_HDMI_TXP0 | C43 | 1 | 2 | 0.1U/16V | 4 | HDMI TX0+ R |
| <9> INT_HDMI_TXN0 | INT_HDMI_TXN0 | C42 | 1 | 2 | 0.1U/16V | 4 | HDMI TX0- R |
| <9> INT_HDMI_TXCP | INT_HDMI_TXCP | C36 | 1 | 2 | 0.1U/16V | 4 | HDMI CLK+ R |
| <9> INT_HDMI_TXCN | INT_HDMI_TXCN | C35 | 1 | 2 | 0.1U/16V | 4 | HDMI CLK- R |

## Reserve for EMI and close to HDMI CONN



22

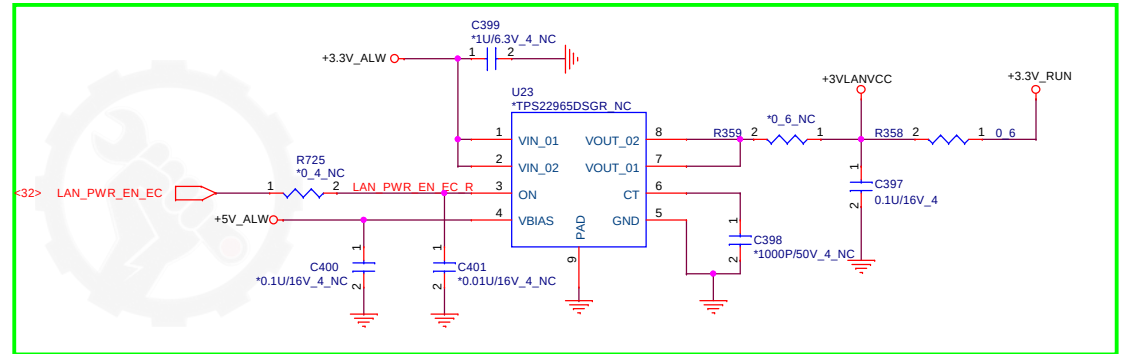
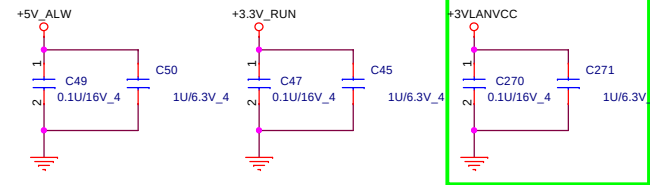
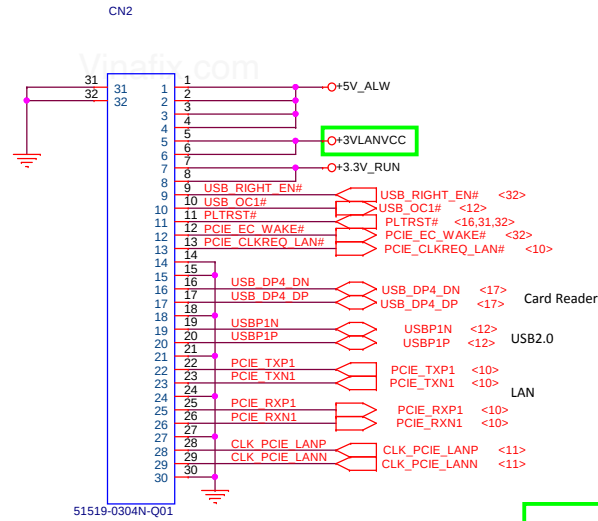


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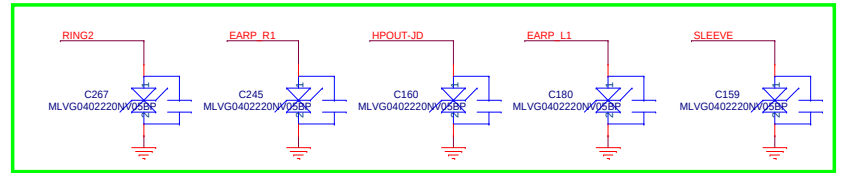
**Quanta Computer Inc.**  
**PROJECT : ZM6**

|       |                               |                |
|-------|-------------------------------|----------------|
| Size  | Document Number               | Rev            |
|       | <b>Blank</b>                  | <b>1A</b>      |
| Date: | Wednesday, September 04, 2013 | Sheet 23 of 47 |



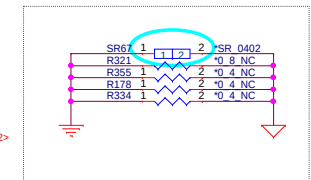
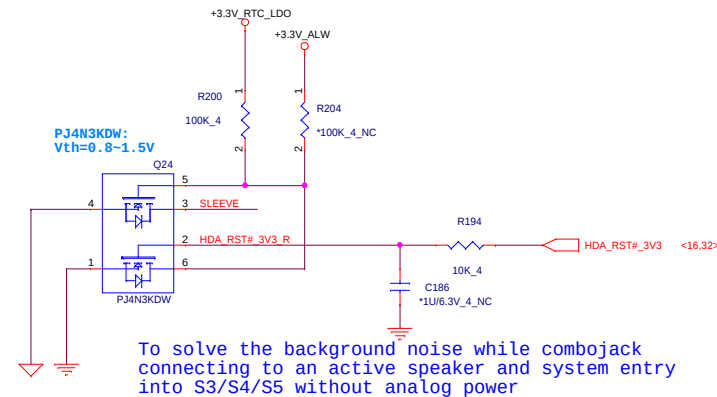
Quanta Computer Inc.  
PROJECT : ZM6





The schematic diagram illustrates the audio output stage of the TDA2050 amplifier. The circuit includes the following components and connections:

- Inputs:**
  - MIC2-VREFO:** Connected to the amplifier's input.
  - HP-OUT-L SLEEVE:** Connected to the amplifier's input.
  - HP-OUT-JD RING2:** Connected to the amplifier's input.
  - HP-OUT-R:** Connected to the amplifier's input.
  - LINE1 VREFO\_L:** Connected to the amplifier's input.
  - LINE1 VREFO\_R:** Connected to the amplifier's input.
- Resistors:**
  - R337, R353:** 2.2K 4.
  - R164:** 8.2F 6.
  - R149:** 8.2F 6.
  - R171:** 4.7K 4.
  - R335:** 4.7K 4.
  - R182:** 1K 4.
  - R336:** 1K 4.
  - R176:** \*10K 4 NC.
  - R142:** \*10K 4 NC.
  - R333:** \*10K 4 NC.
  - R179:** \*10K 4 NC.
- Capacitors:**
  - EC21, EC17:** 100P 4.
  - EC20, EC33:** 100P 4.
- Outputs:**
  - CON1:** Speaker connection.
  - 2S3J080-03011F:** Speaker connection.

[illegible]

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|       |                               |                |
|-------|-------------------------------|----------------|
| Size  | Document Number               | Rev            |
|       | <b>Blank</b>                  | 1A             |
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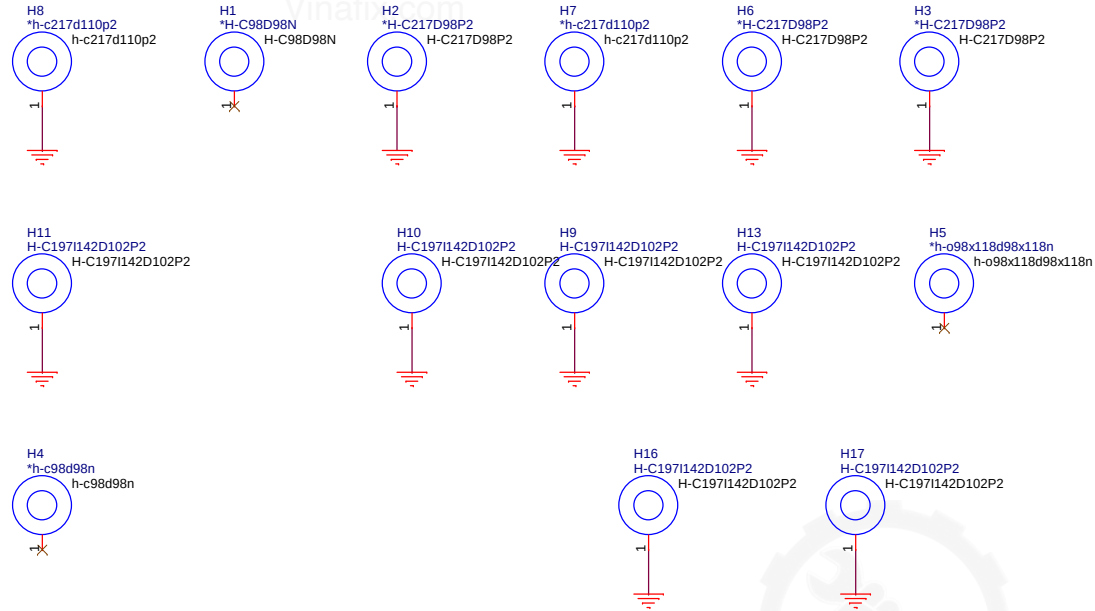


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**PROJECT : ZM6**

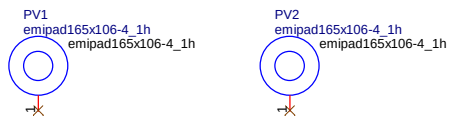
|       |                               |                |
|-------|-------------------------------|----------------|
| Size  | Document Number               | Rev            |
|       | <b>Blank</b>                  | <b>1A</b>      |
| Date: | Wednesday, September 04, 2013 | Sheet 27 of 47 |

# SCREW PAD

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## Spring

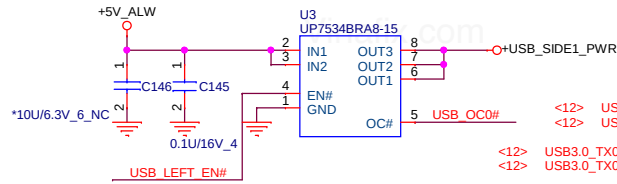


**Quanta Computer Inc.**  
**PROJECT : ZM6**

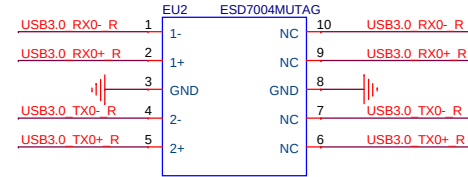
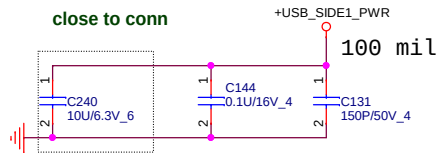
|      |                  |     |
|------|------------------|-----|
| Size | Document Number  | Rev |
|      | <b>SCREW PAD</b> | 1A  |

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## I continuous 1.5A OC 2.0A M14 Request

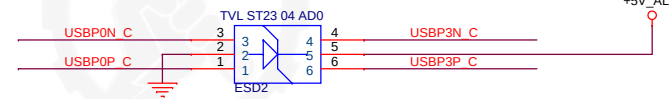


close to conn

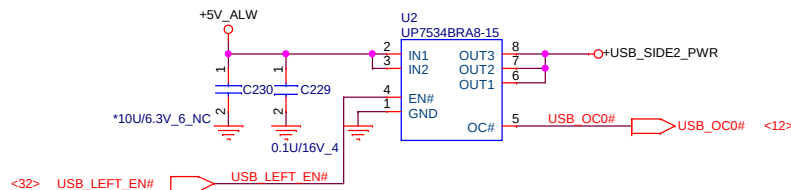


## ESD Function

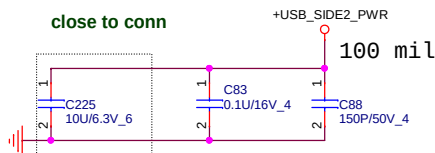
Place ESD diodes as close as USB connector.



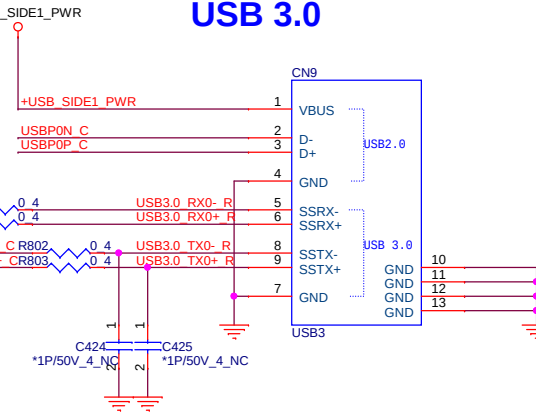
## I continuous 1.5A OC 2.0A M14 Request



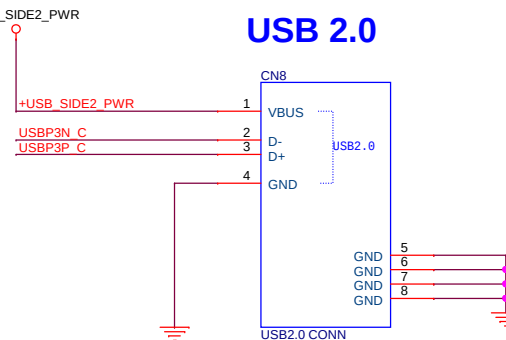
close to conn



## USB 3.0

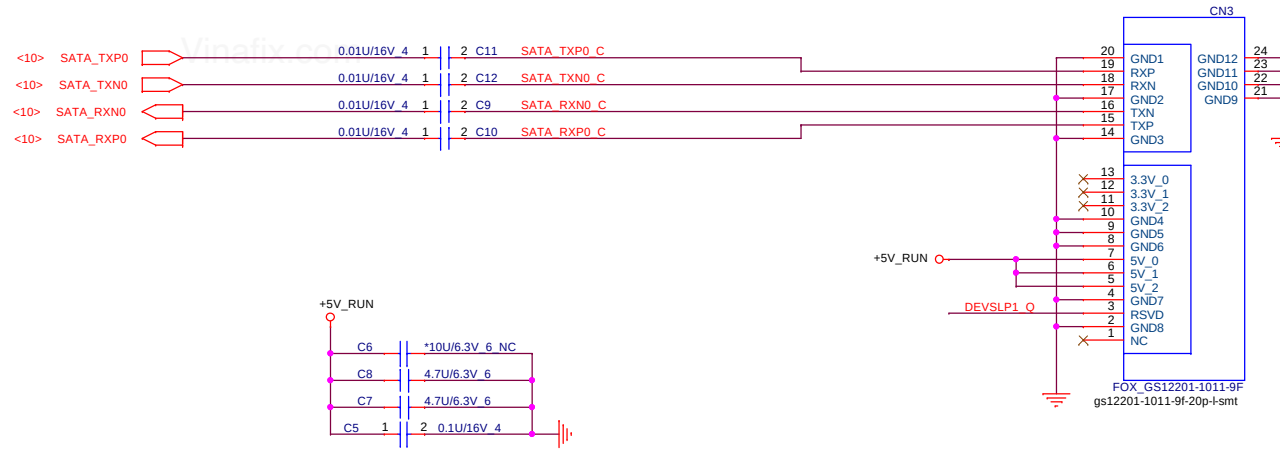


## USB 2.0

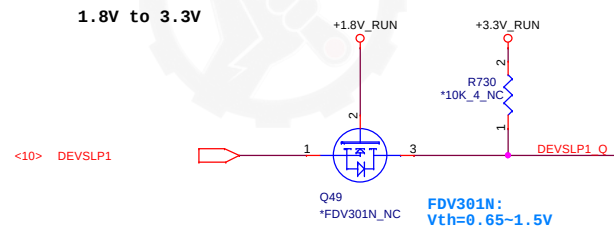


# SATA HDD Connector

30



+5V: 2 A(4 Pin)  
+3V: 2 A(4 Pin)  
Gnd : (5 Pin)



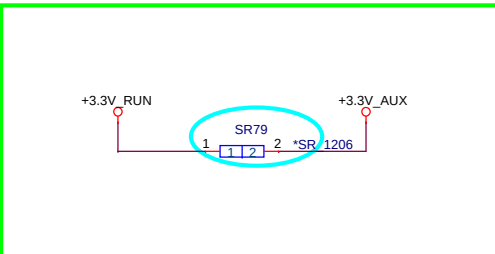
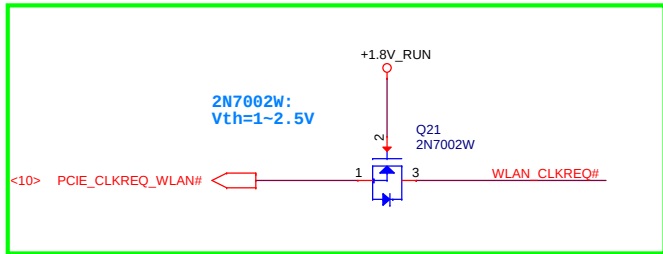
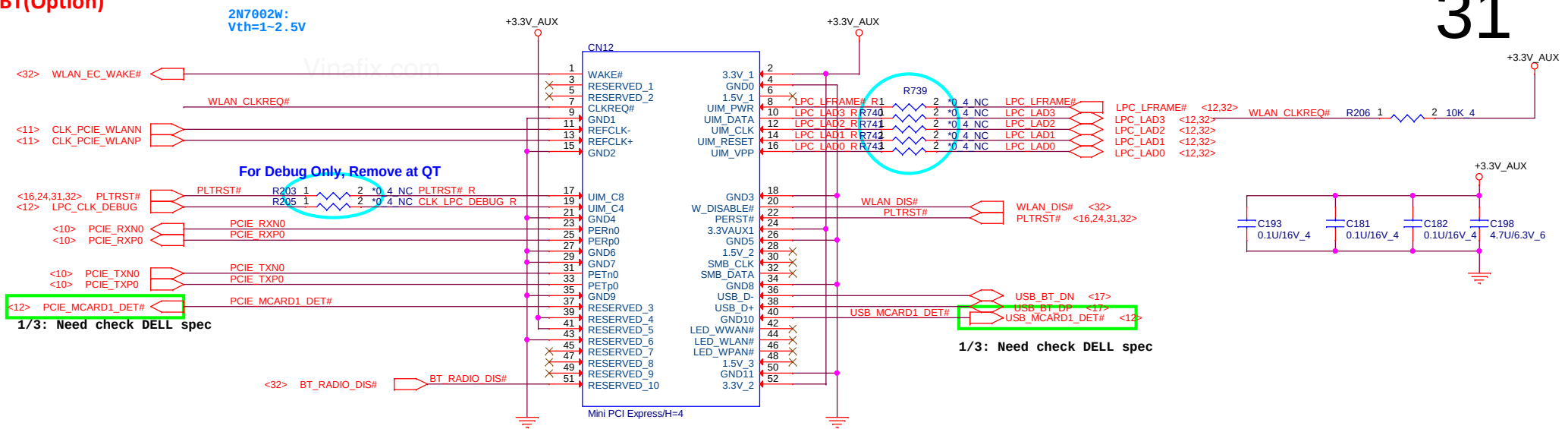
Quanta Computer Inc.  
PROJECT : ZM6

|       |                               |                |
|-------|-------------------------------|----------------|
| Size  | Document Number               | Rev            |
|       | SATA HDD                      | 1A             |
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Mini Card  
WLAN/BT(Optional)

0815 Remove R173, add SR79  
0822 NC R203,R205,R739,R740,R741,R742,R743,R173

31



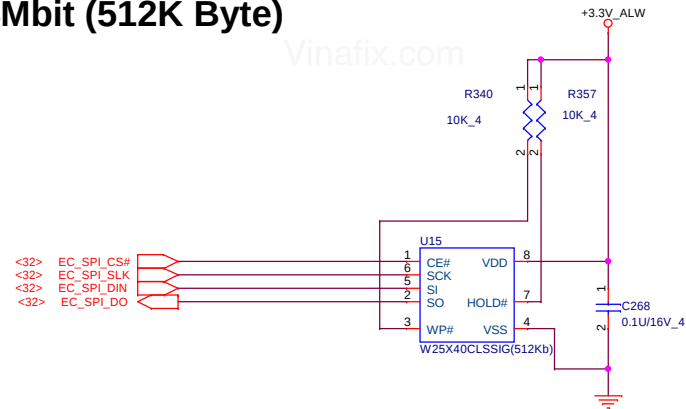
|       |                             |                |
|-------|-----------------------------|----------------|
| Size  | Document Number             | Rev            |
|       | <b>SIO (ITE8528E)</b>       | 1A             |
| Date: | Tuesday, September 17, 2013 | Sheet 32 of 47 |



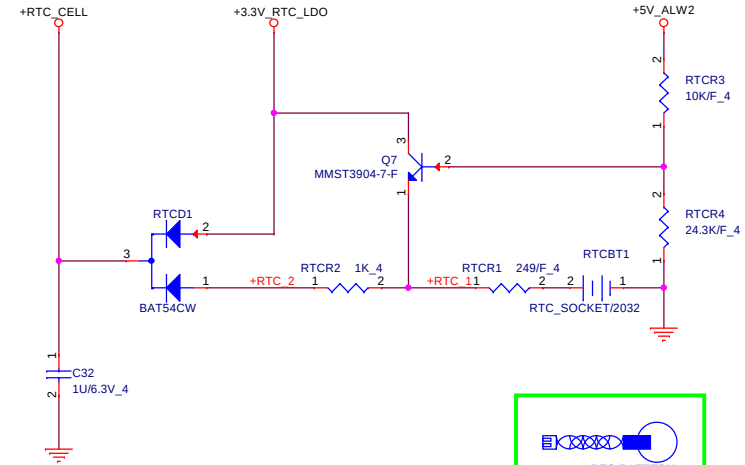
| Co-layout list (Change to IT8928) |                    |                          |  |
|-----------------------------------|--------------------|--------------------------|--|
| Net                               | Mount              | NC                       |  |
| SIQ_EXT_SMI#                      | R734               | D7                       |  |
| SIQ_EXT_SCI#                      | R735               | D6                       |  |
| SIQ_PWRBNT#                       | R760               | D9                       |  |
| SOC_Ovrdrd#                       | R765               | D16                      |  |
| SYS_PWR_SW#                       | R769               | R768                     |  |
| AC_PRESENT                        | R759               | D15                      |  |
| IRQ_SERIRQ_Q                      | R745               | U18, R522                |  |
| PMC_SLP_S3#                       | R736, R744         | R511, R512, Q37          |  |
| PMC_SLP_S0IX#                     |                    |                          |  |
| SIQ_SUSPWRDRNACK                  | R782               | R520, Q42                |  |
| pin 104, 106, 113, 122, 114, 3    | R755, R756<br>R799 | R757, R758<br>R731, C154 |  |



## For EC 4Mbit (512K Byte)



## RTC BATTERY



$5 * [12 / (5.1 + 12)] - 0.7 = 2.8V$   
RTC Battery Charger when lower than 2.8V

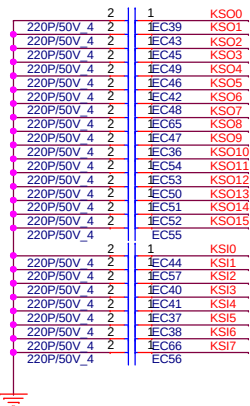
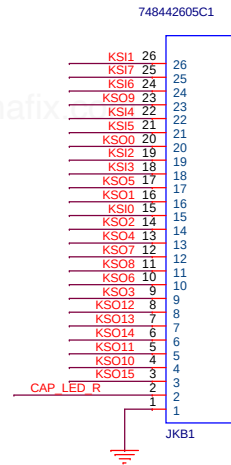


0815 Remove R199,R190,R191,R706,R707, add SR84,SR85,SR86,SR87,SR88

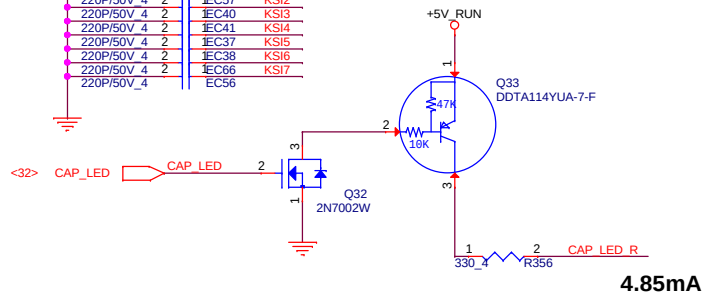
Vinafix

&lt;32&gt; KSO[0..15]

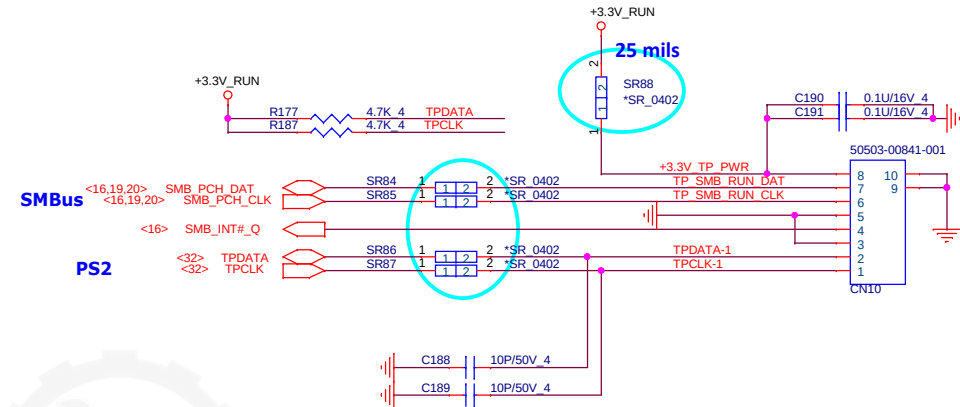
&lt;32&gt; KSI[0..7]



$V_i(\text{on\_max}) = -1.4\text{V}$   
 $V_i(\text{off\_min}) = -0.3$



## Touch Pad Connector



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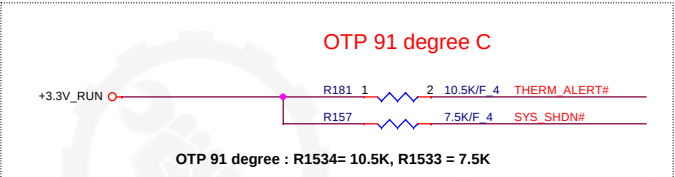
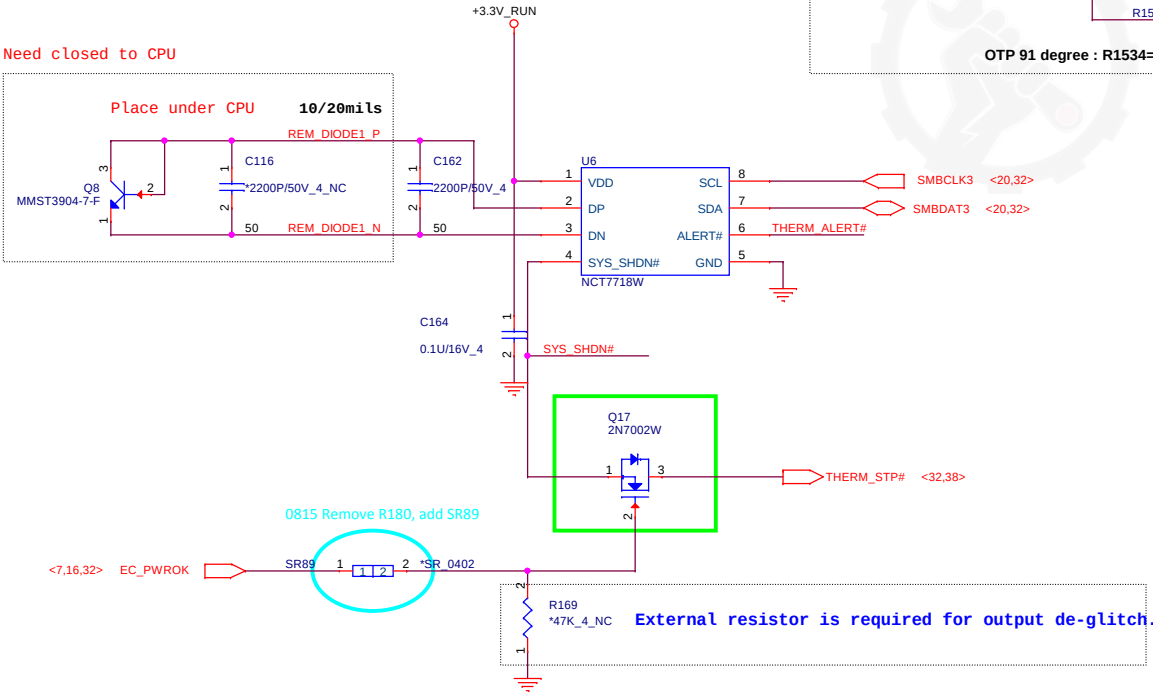
| Size  | Document Number | Rev |
|-------|-----------------|-----|
| KB/TP |                 | 1A  |

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|       |                               |                |
|-------|-------------------------------|----------------|
| Size  | Document Number               | Rev            |
|       | <b>3VALW ON POWER LOGIC</b>   | 1A             |
| Date: | Wednesday, September 04, 2013 | Sheet 35 of 47 |

# THERMAL IC



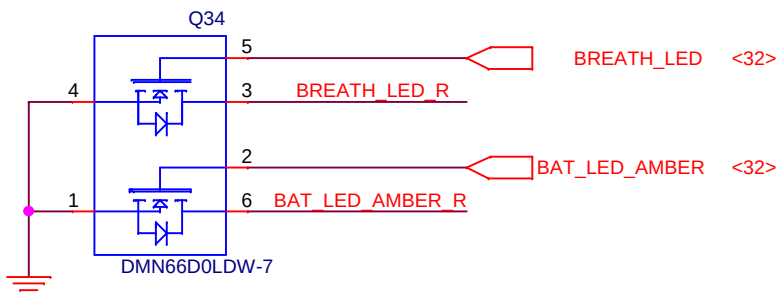
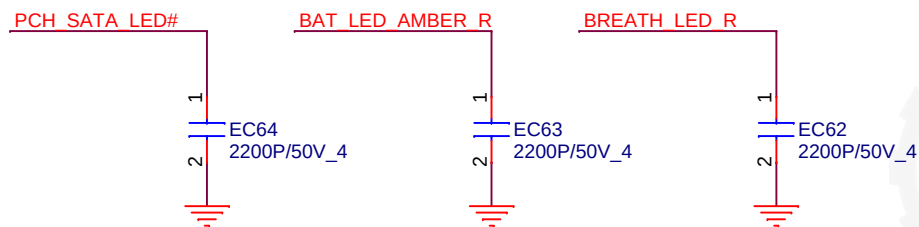
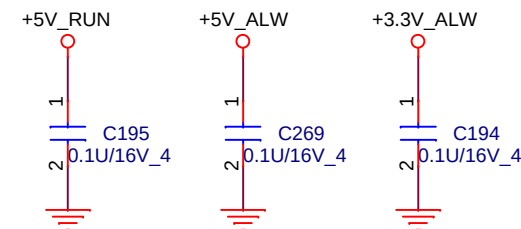
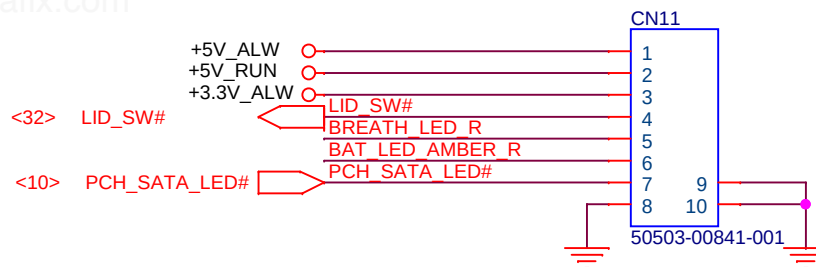
| SYS_SHD# | 2K   | 7.5K | 10.5K | 14K   | 18.7K |
|----------|------|------|-------|-------|-------|
| ALERT#   |      |      |       |       |       |
| 2K       | 77'C | 87'C | 97'C  | 107'C | 117'C |
| 7.5K     | 79'C | 89'C | 99'C  | 109'C | 119'C |
| 10.5K    | 81'C | 91'C | 101'C | 111'C | 121'C |
| 14K      | 83'C | 93'C | 103'C | 113'C | 123'C |
| 18.7K    | 85'C | 95'C | 105'C | 115'C | 125'C |

## LED Status

## Battrey charger LED HDD access LED

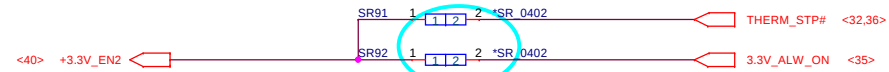
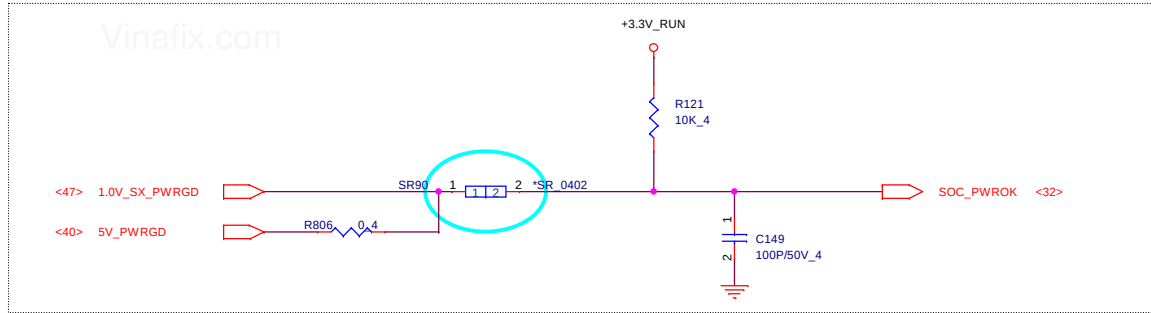
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# 37



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**PROJECT : ZM6**

| Size  | Document Number               | Rev            |
|-------|-------------------------------|----------------|
|       | <b>LED</b>                    | 1A             |
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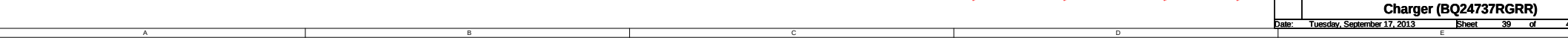


0815 Remove R114,R168,R184, add SR90,SR91,SR92



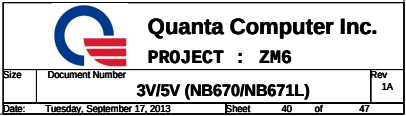
**Quanta Computer Inc.**  
**PROJECT : ZM6**

|       |                             |                |
|-------|-----------------------------|----------------|
| Size  | Document Number             | Rev            |
|       |                             | 1A             |
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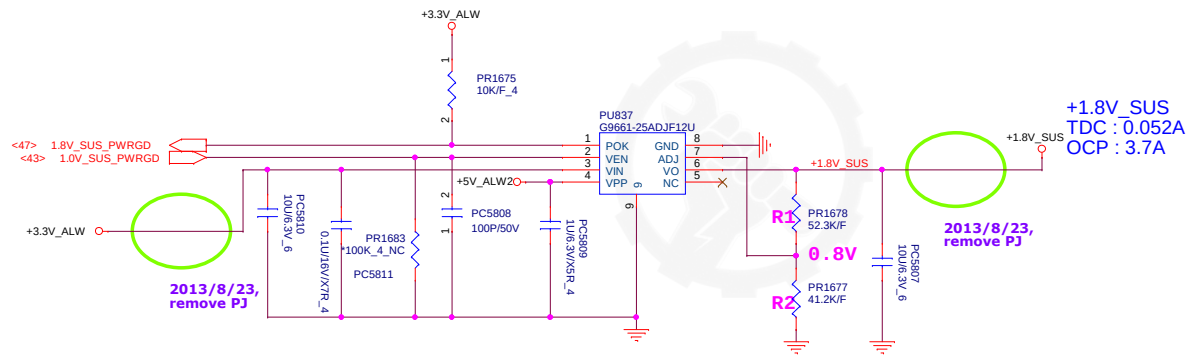
Discharge Current Limit Set to 0.8A  
for 10W Battery boost

Setup 85W for PROCHOT



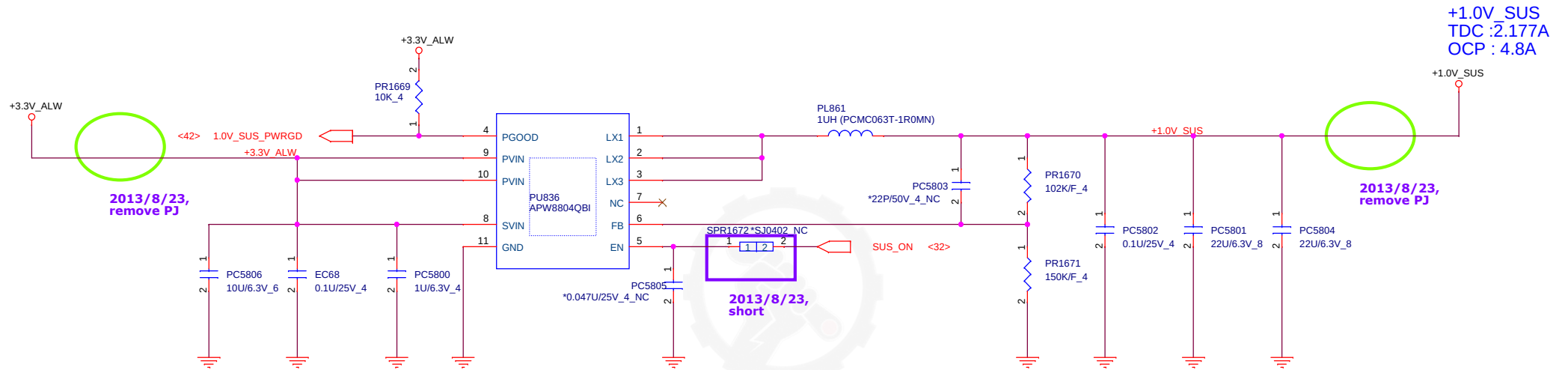






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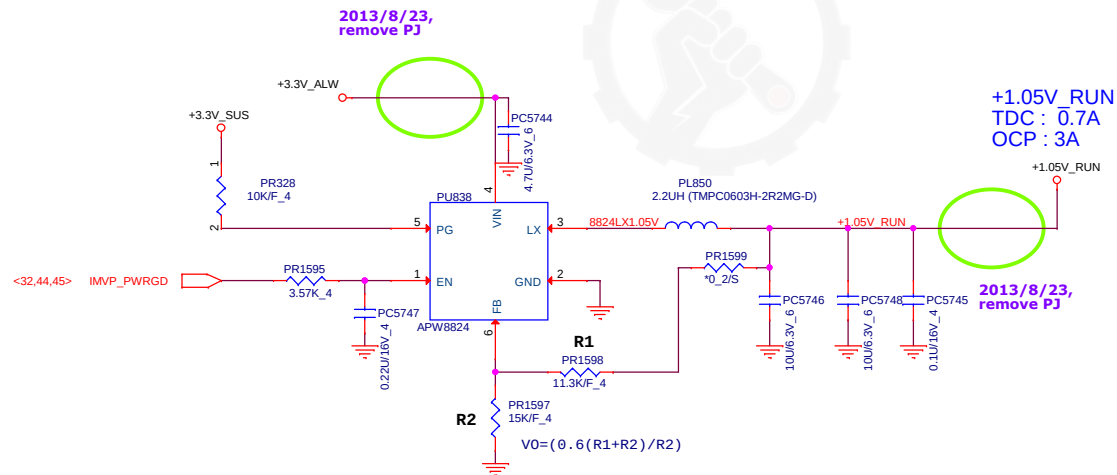
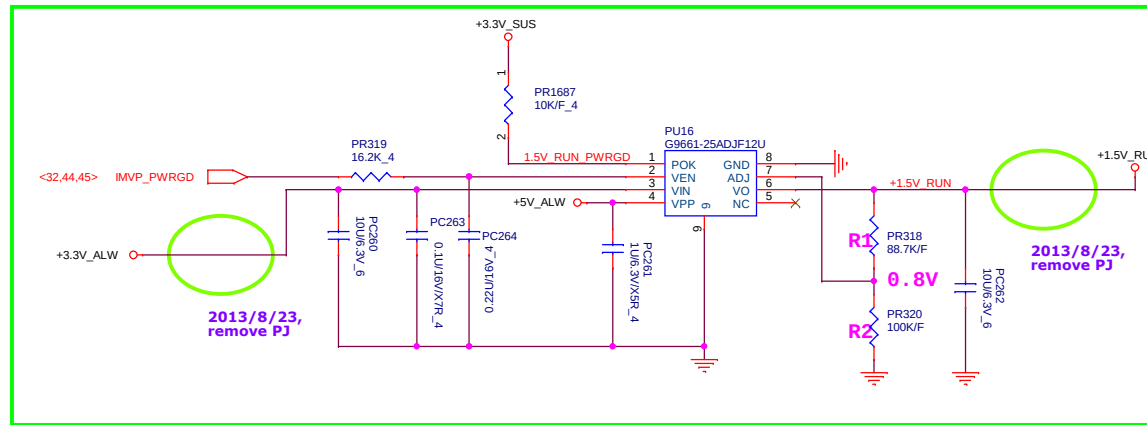
|       |                             |                |
|-------|-----------------------------|----------------|
| Size  | Document Number             | Rev            |
|       | 1.8V_SUS(G9661-25ADJF12U)   | 1A             |
| Date: | Tuesday, September 17, 2013 | Sheet 42 of 47 |



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| Size  | Document Number              | Rev            |
|-------|------------------------------|----------------|
|       | 1.0V_SUS(APW8804QBI)         | 1A             |
| Date: | Thursday, September 05, 2013 | Sheet 43 of 47 |

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